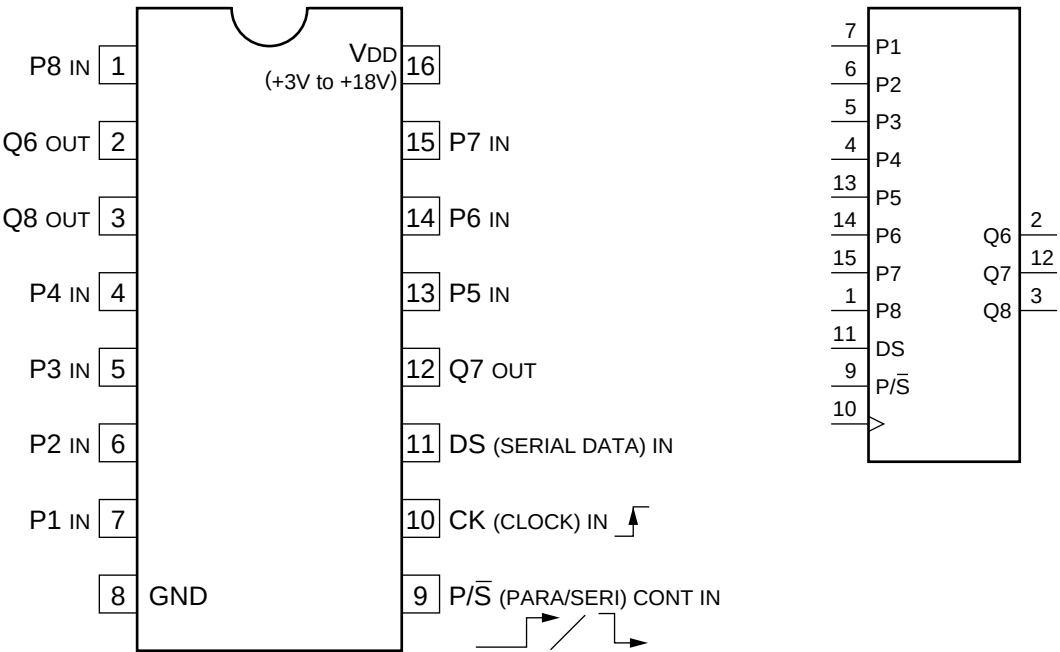

C-MOS SYNCHRONOUS SERIAL / PARALLEL 8-BIT STATIC SHIFT REGISTER
-TOP VIEW-



tn				tn + 1							
INPUTS				INTERNAL OUTPUTS					OUTPUTS		
P/ $\bar{S}$ CONT	CK	P	DS	Q1	Q2	Q3	Q4	Q5	Q6	Q7	Q8
1		P1 to P8	X	P1	P2	P3	P4	P5	P6	P7	P8
0		X	1	1	Q1n	Q2n	Q3n	Q4n	Q5n	Q6n	Q7n
0		X	0	0	Q1n	Q2n	Q3n	Q4n	Q5n	Q6n	Q7n
X	0	X	X	Q1n	Q2n	Q3n	Q4n	Q5n	Q6n	Q7n	Q8n

0 ; LOW LEVEL X ; DON'T CARE. tn ; BEFORE CLOCK
1 ; HIGH LEVEL tn + 1 ; AFTER CLOCK

