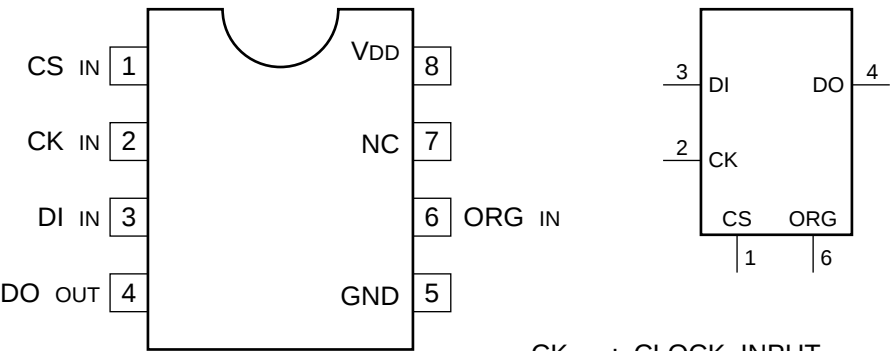


C-MOS 4 K-BIT SERIAL EEPROM

—TOP VIEW—



- CK ; CLOCK INPUT
- CS ; CHIP SELECT
- DI ; SERIAL DATA INPUT
- DO ; SERIAL DATA OUTPUT
- ORG ; MEMORY ORGANIZATION (256 × 16/512 × 8)

