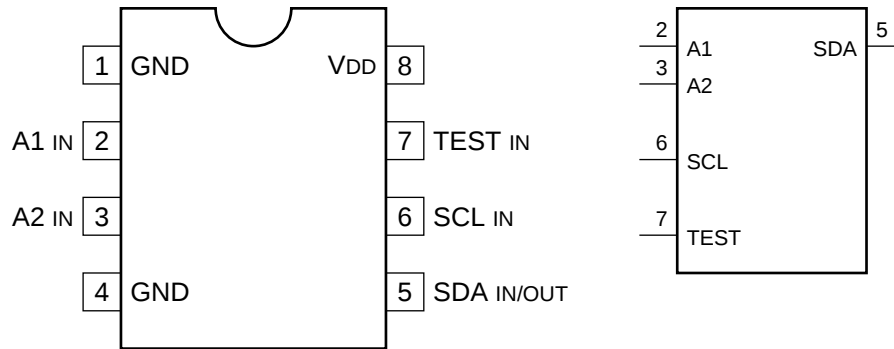


C-MOS 4,096 (512 × 8)-BIT EEPROM

—TOP VIEW—



A1, A2 ; ADDRESS INPUT

SCL ; SERIAL CLOCK INPUT

SDA ; SERIAL DATA/ADDRESS INPUT/OUTPUT

TEST ; TEST INPUT

