



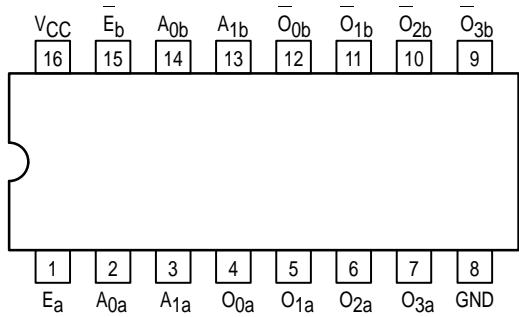
**MOTOROLA**

**MC74AC139  
MC74ACT139**

## Dual 1-of-4 Decoder/Demultiplexer

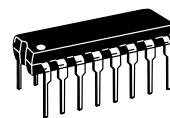
The MC74AC139/74ACT139 is a high-speed, dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each accepting two inputs and providing four mutually-exclusive active-LOW outputs. Each decoder has an active-LOW Enable input which can be used as a data input for a 4-output demultiplexer. Each half of the MC74AC139/74ACT139 can be used as a function generator providing four minterms of two variables.

- Multifunction Capability
- Two Completely Independent 1-of-4 Decoders
- Active LOW Mutually Exclusive Outputs
- Outputs Source/Sink 24 mA
- 'ACT139 Has TTL Compatible Inputs



### PIN NAMES

A<sub>0</sub>, A<sub>1</sub> Address Inputs  
E Enable Inputs  
O<sub>0</sub>-O<sub>3</sub> Outputs



N SUFFIX  
CASE 648-08  
PLASTIC



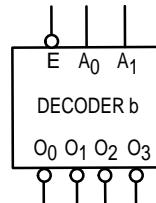
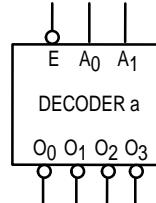
D SUFFIX  
CASE 751B-05  
PLASTIC

### TRUTH TABLE

Inputs			Outputs			
E	A <sub>0</sub>	A <sub>1</sub>	O <sub>0</sub>	O <sub>1</sub>	O <sub>2</sub>	O <sub>3</sub>
H	X	X	H	H	H	H
L	L	L	L	H	H	H
L	H	L	H	L	H	H
L	L	H	H	H	L	H
L	H	H	H	H	H	L

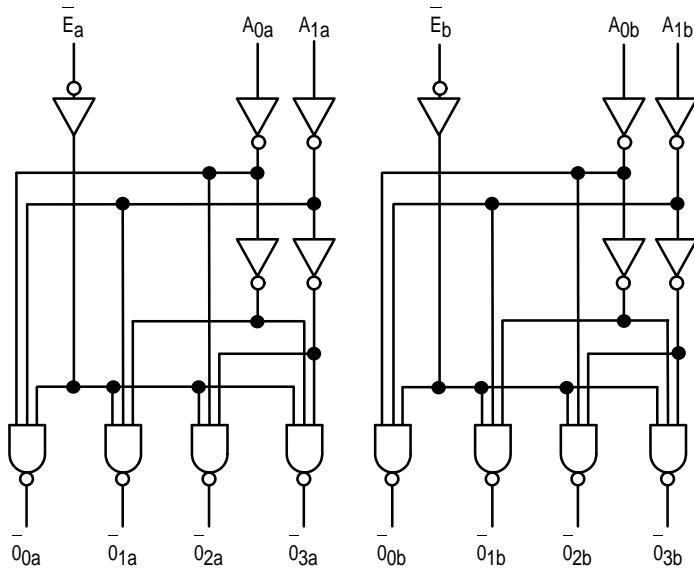
H = HIGH Voltage Level  
L = LOW Voltage Level  
X = Immaterial

### LOGIC SYMBOL



# MC74AC139 MC74ACT139

## LOGIC DIAGRAM



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

## FUNCTIONAL DESCRIPTION

The MC74AC139/74ACT139 is a high-speed dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each of which accepts two binary weighted inputs ( $A_0$ - $A_1$ ) and provides four mutually exclusive active-LOW outputs ( $O_0$ - $O_3$ ). Each decoder has an active-LOW enable ( $E$ ). When  $E$  is HIGH all outputs are forced HIGH. The enable can be used as the data input for a 4-output demultiplexer application. Each half of the MC74AC139/74ACT139 generates all four minterms of two variables. These four minterms are useful in some applications, replacing multiple gate functions as shown in Figure a, and thereby reducing the number of packages required in a logic network.

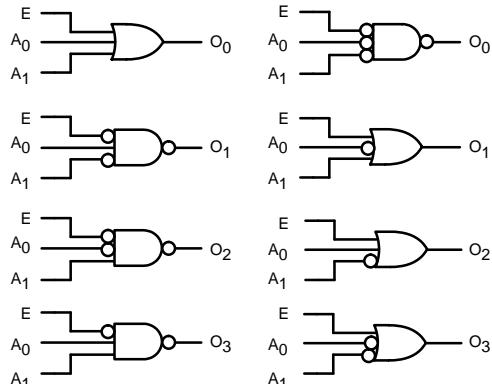


Figure a: Gate Functions (each half)

## MAXIMUM RATINGS\*

Symbol	Parameter	Value	Unit
$V_{CC}$	DC Supply Voltage (Referenced to GND)	-0.5 to +7.0	V
$V_{in}$	DC Input Voltage (Referenced to GND)	-0.5 to $V_{CC}$ +0.5	V
$V_{out}$	DC Output Voltage (Referenced to GND)	-0.5 to $V_{CC}$ +0.5	V
$I_{in}$	DC Input Current, per Pin	$\pm 20$	mA
$I_{out}$	DC Output Sink/Source Current, per Pin	$\pm 50$	mA
$I_{CC}$	DC $V_{CC}$ or GND Current per Output Pin	$\pm 50$	mA
$T_{stg}$	Storage Temperature	-65 to +150	°C

\* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

# MC74AC139 MC74ACT139

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter		Min	Typ	Max	Unit
V <sub>CC</sub>	Supply Voltage	'AC	2.0	5.0	6.0	V
		'ACT	4.5	5.0	5.5	
V <sub>in</sub> , V <sub>out</sub>	DC Input Voltage, Output Voltage (Ref. to GND)		0		V <sub>CC</sub>	V
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time (Note 1) 'AC Devices except Schmitt Inputs	V <sub>CC</sub> @ 3.0 V		150		ns/V
		V <sub>CC</sub> @ 4.5 V		40		
		V <sub>CC</sub> @ 5.5 V		25		
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time (Note 2) 'ACT Devices except Schmitt Inputs	V <sub>CC</sub> @ 4.5 V		10		ns/V
		V <sub>CC</sub> @ 5.5 V		8.0		
T <sub>J</sub>	Junction Temperature (PDIP)				140	°C
T <sub>A</sub>	Operating Ambient Temperature Range		-40	25	85	°C
I <sub>OH</sub>	Output Current — High				-24	mA
I <sub>OL</sub>	Output Current — Low				24	mA

1. V<sub>in</sub> from 30% to 70% V<sub>CC</sub>; see individual Data Sheets for devices that differ from the typical input rise and fall times.

2. V<sub>in</sub> from 0.8 V to 2.0 V; see individual Data Sheets for devices that differ from the typical input rise and fall times.

## DC CHARACTERISTICS

Symbol	Parameter	V <sub>CC</sub> (V)	74AC		Unit	Conditions		
			T <sub>A</sub> = +25°C					
			Typ	Guaranteed Limits				
V <sub>IH</sub>	Minimum High Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	2.1 3.15 3.85	V V <sub>OUT</sub> = 0.1 V or V <sub>CC</sub> - 0.1 V		
V <sub>IL</sub>	Maximum Low Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	0.9 1.35 1.65	V V <sub>OUT</sub> = 0.1 V or V <sub>CC</sub> - 0.1 V		
V <sub>OH</sub>	Minimum High Level Output Voltage	3.0 4.5 5.5	2.99 4.49 5.49	2.9 4.4 5.4	2.9 4.4 5.4	V I <sub>OUT</sub> = -50 μA		
		3.0 4.5 5.5		2.56 3.86 4.86	2.46 3.76 4.76	V *V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> -12 mA I <sub>OH</sub> -24 mA -24 mA		
V <sub>OL</sub>	Maximum Low Level Output Voltage	3.0 4.5 5.5	0.002 0.001 0.001	0.1 0.1 0.1	0.1 0.1 0.1	V I <sub>OUT</sub> = 50 μA		
		3.0 4.5 5.5		0.36 0.36 0.36	0.44 0.44 0.44	V *V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> 12 mA I <sub>OL</sub> 24 mA 24 mA		
I <sub>IN</sub>	Maximum Input Leakage Current	5.5		±0.1	±1.0	μA V <sub>I</sub> = V <sub>CC</sub> , GND		
I <sub>OLD</sub>	†Minimum Dynamic Output Current	5.5			75	mA V <sub>OLD</sub> = 1.65 V Max		
I <sub>OHD</sub>		5.5			-75	mA V <sub>OHD</sub> = 3.85 V Min		
I <sub>CC</sub>	Maximum Quiescent Supply Current	5.5		8.0	80	μA V <sub>IN</sub> = V <sub>CC</sub> or GND		

\* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms, one output loaded at a time.

Note: I<sub>IN</sub> and I<sub>CC</sub> @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V V<sub>CC</sub>.

# MC74AC139 MC74ACT139

**AC CHARACTERISTICS** (For Figures and Waveforms — See Section 3)

Symbol	Parameter	$V_{CC}^*$ (V)	74AC			74AC		Unit	Fig. No.		
			$T_A = +25^\circ C$ $C_L = 50 \text{ pF}$			$T_A = -40^\circ C$ to $+85^\circ C$ $C_L = 50 \text{ pF}$					
			Min	Typ	Max	Min	Max				
$t_{PLH}$	Propagation Delay $A_n$ to $O_n$	3.3 5.0	4.0 3.0	8.0 6.5	11.5 8.5	3.5 2.5	13 9.5	ns	3-6		
$t_{PHL}$	Propagation Delay $A_n$ to $O_n$	3.3 5.0	3.0 2.5	7.0 5.5	10 7.5	2.5 2.0	11 8.5	ns	3-6		
$t_{PLH}$	Propagation Delay $E_n$ to $O_n$	3.3 5.0	4.5 3.5	9.5 7.0	12 8.5	3.5 3.0	13 10	ns	3-6		
$t_{PHL}$	Propagation Delay $E_n$ to $O_n$	3.3 5.0	4.0 2.5	8.0 6.0	10 7.5	3.0 2.5	11 8.5	ns	3-6		

\* Voltage Range 3.3 V is  $3.3 \text{ V} \pm 0.3 \text{ V}$ .

Voltage Range 5.0 V is  $5.0 \text{ V} \pm 0.5 \text{ V}$ .

## DC CHARACTERISTICS

Symbol	Parameter	$V_{CC}$ (V)	74ACT		74ACT		Unit	Conditions		
			$T_A = +25^\circ C$		$T_A = -40^\circ C$ to $+85^\circ C$					
			Typ	Guaranteed Limits	Typ	Guaranteed Limits				
$V_{IH}$	Minimum High Level Input Voltage	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0		V	$V_{OUT} = 0.1 \text{ V}$ or $V_{CC} - 0.1 \text{ V}$		
$V_{IL}$	Maximum Low Level Input Voltage	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8		V	$V_{OUT} = 0.1 \text{ V}$ or $V_{CC} - 0.1 \text{ V}$		
$V_{OH}$	Minimum High Level Output Voltage	4.5 5.5	4.49 5.49	4.4 5.4	4.4 5.4		V	$I_{OUT} = -50 \mu\text{A}$		
		4.5 5.5		3.86 4.86	3.76 4.76		V	$*V_{IN} = V_{IL}$ or $V_{IH}$ $I_{OH} = -24 \text{ mA}$		
$V_{OL}$	Maximum Low Level Output Voltage	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1		V	$I_{OUT} = 50 \mu\text{A}$		
		4.5 5.5		0.36 0.36	0.44 0.44		V	$*V_{IN} = V_{IL}$ or $V_{IH}$ $I_{OL} = 24 \text{ mA}$		
$I_{IN}$	Maximum Input Leakage Current	5.5		$\pm 0.1$	$\pm 1.0$		$\mu\text{A}$	$V_I = V_{CC}, \text{ GND}$		
$\Delta I_{CCT}$	Additional Max. $I_{CC}$ /Input	5.5	0.6		1.5		mA	$V_I = V_{CC} - 2.1 \text{ V}$		
$I_{OLD}$	†Minimum Dynamic Output Current	5.5			75		mA	$V_{OLD} = 1.65 \text{ V Max}$		
$I_{OHD}$		5.5			-75		mA	$V_{OHD} = 3.85 \text{ V Min}$		
$I_{CC}$	Maximum Quiescent Supply Current	5.5		8.0	80		$\mu\text{A}$	$V_{IN} = V_{CC}$ or GND		

\* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms, one output loaded at a time.

# MC74AC139 MC74ACT139

**AC CHARACTERISTICS** (For Figures and Waveforms — See Section 3)

Symbol	Parameter	$V_{CC}^*$ (V)	74ACT			74ACT		Unit	Fig. No.		
			$T_A = +25^\circ C$ $C_L = 50 \text{ pF}$			$T_A = -40^\circ C$ to $+85^\circ C$ $C_L = 50 \text{ pF}$					
			Min	Typ	Max	Min	Max				
$t_{PLH}$	Propagation Delay $A_n$ to $O_n$	5.0	1.5	6.0	8.5	1.5	9.5	ns	3-6		
$t_{PHL}$	Propagation Delay $A_n$ to $O_n$	5.0	1.5	6.0	9.5	1.5	10.5	ns	3-6		
$t_{PLH}$	Propagation Delay $E_n$ to $O_n$	5.0	2.5	7.0	10.0	2.0	11.0	ns	3-6		
$t_{PHL}$	Propagation Delay $E_n$ to $O_n$	5.0	2.0	7.0	9.5	1.5	10.5	ns	3-6		

\* Voltage Range 5.0 V is  $5.0 \text{ V} \pm 0.5 \text{ V}$ .

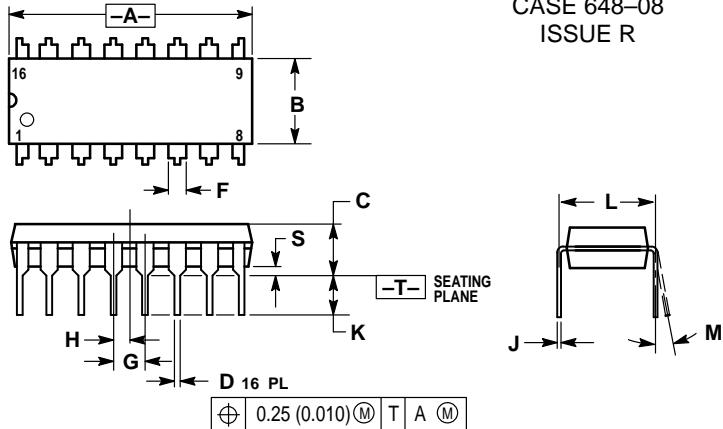
## CAPACITANCE

Symbol	Parameter	Value Typ	Unit	Test Conditions
$C_{IN}$	Input Capacitance	4.5	pF	$V_{CC} = 5.0 \text{ V}$
$C_{PD}$	Power Dissipation Capacitance	40	pF	$V_{CC} = 5.0 \text{ V}$

# MC74AC139 MC74ACT139

## OUTLINE DIMENSIONS

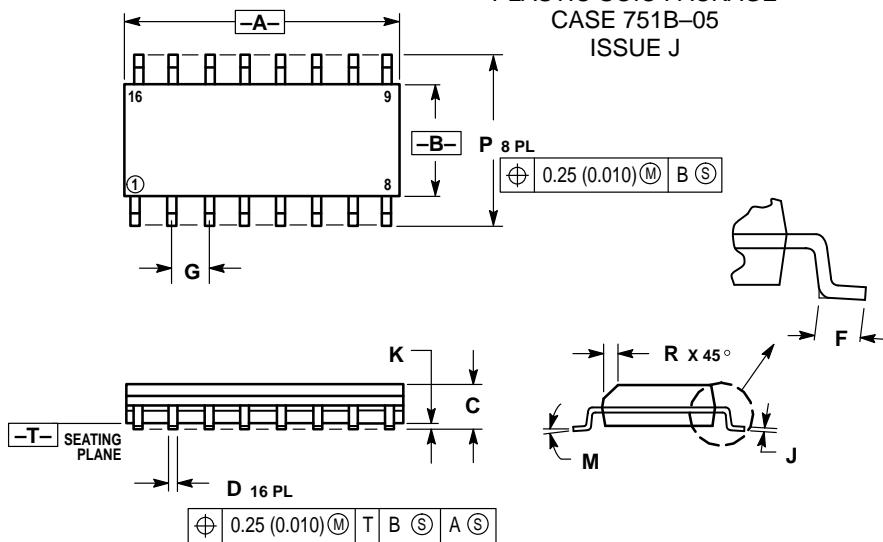
**N SUFFIX**  
PLASTIC DIP PACKAGE  
CASE 648-08  
ISSUE R



NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.  
 3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.  
 4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.  
 5. ROUNDED CORNERS OPTIONAL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.740	0.770	18.80	19.55
B	0.250	0.270	6.35	6.85
C	0.145	0.175	3.69	4.44
D	0.015	0.021	0.39	0.53
F	0.040	0.70	1.02	1.77
G	0.100 BSC		2.54 BSC	
H	0.050 BSC		1.27 BSC	
J	0.008	0.015	0.21	0.38
K	0.110	0.130	2.80	3.30
L	0.295	0.305	7.50	7.74
M	0°	10°	0°	10°
S	0.020	0.040	0.51	1.01

**D SUFFIX**  
PLASTIC SOIC PACKAGE  
CASE 751B-05  
ISSUE J



NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: MILLIMETER.  
 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.  
 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.  
 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.80	10.00	0.386	0.393
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.008	0.009
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

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