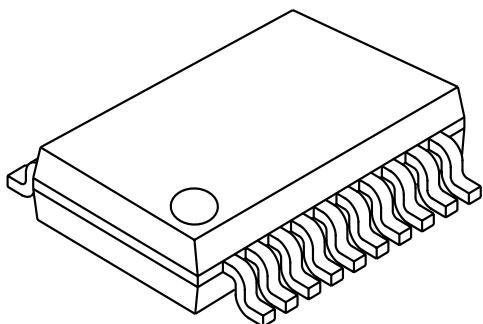


DATA SHEET



BZA109TS

9-fold ESD transient voltage
suppressor

Product specification

1998 Nov 26

Supersedes data of 1997 Dec 11

Philips
Semiconductors



PHILIPS

9-fold ESD transient voltage suppressor**BZA109TS****FEATURES**

- ESD rating >8 kV, according to IEC1000-4-2
- SOT339-1 surface mount package
- Common anode configuration
- Non-clamping range -0.5 to +6.8 V
- Maximum non-repetitive peak reverse power dissipation: 25 W at $t_p = 1$ ms
- Maximum clamping voltage at peak pulse current: 10 V at $I_{ZSM} = 2.5$ A.

DESCRIPTION

9-fold monolithic transient voltage suppressor in an SSOP20; SOT339-1 surface mount package. The device is ideal in situations where board space is a premium.

PINNING

PIN	DESCRIPTION
1 to 5	input (IN1 to IN5)
6 and 15	common anode (GND)
7 to 10	input (IN6 to IN9)
11 to 14	output (OUT9 to OUT6)
16 to 20	output (OUT5 to OUT1)

APPLICATIONS

- For 9-bit wide undershoot/overshoot clamping and fast ESD transient suppression in:
 - Computers and peripherals
 - Audio and video equipment
 - Business machines
 - Communication systems
 - Medical equipment.

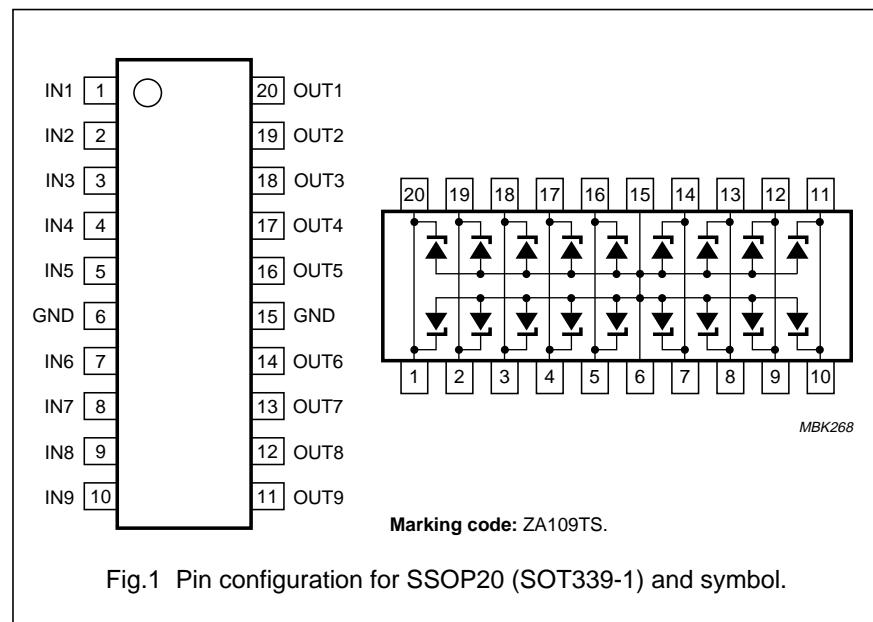


Fig.1 Pin configuration for SSOP20 (SOT339-1) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
I_Z	working current	$T_{amb} = 25$ °C	–	20	mA
I_F	continuous forward current	$T_{amb} = 25$ °C	–	100	mA
I_{FT}	feed-through current	$T_{amb} = 25$ °C; note 1	–	100	mA
I_{FSM}	non-repetitive peak forward current	$t_p = 1$ ms; square pulse	–	4.5	A
I_{ZSM}	non-repetitive peak reverse current	$t_p = 1$ ms; square pulse; see Fig.2	–	2.5	A
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 2; see Fig.3	–	0.95	W
P_{ZSM}	non-repetitive peak reverse power dissipation	$t_p = 1$ ms; square pulse; see Fig.4	–	25	W
T_{stg}	storage temperature		-65	+150	°C
T_j	operating junction temperature		-65	+150	°C

Notes

1. Current is flowing from input to corresponding output.
2. One or more diodes loaded.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	one or more diodes loaded	135	K/W

ELECTRICAL CHARACTERISTICS $T_j = 25^\circ C$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per diode						
V_Z	working voltage	$I_Z = 250 \mu A$	6.4	6.8	7.2	V
V_F	forward voltage	$I_F = 100 mA$	—	—	1.1	V
V_{ZSM}	non-repetitive peak reverse voltage	$I_{ZSM} = 2.5 A; t_p = 1 ms$	—	—	10	V
I_H	input high current	$V_{IN} = 5.25 V$	—	—	0.5	μA
r_{dif}	differential resistance	$I_Z = 250 \mu A$	—	—	100	Ω
S_Z	temperature coefficient of working voltage	$I_Z = 5 mA$	—	3	—	mV/K
C_d	diode capacitance	see Fig.5 $V_R = 0; f = 1 MHz$ $V_R = 5.25 V; f = 1 MHz$	—	—	200	pF
			—	—	100	pF

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GRAPHICAL DATA

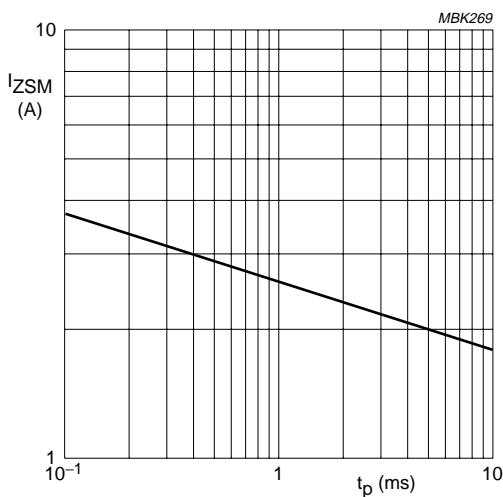
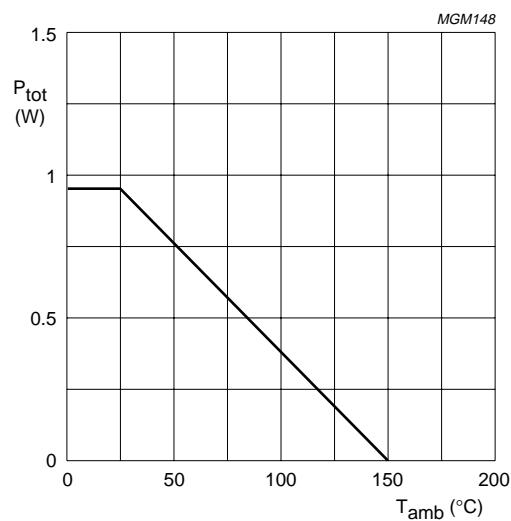
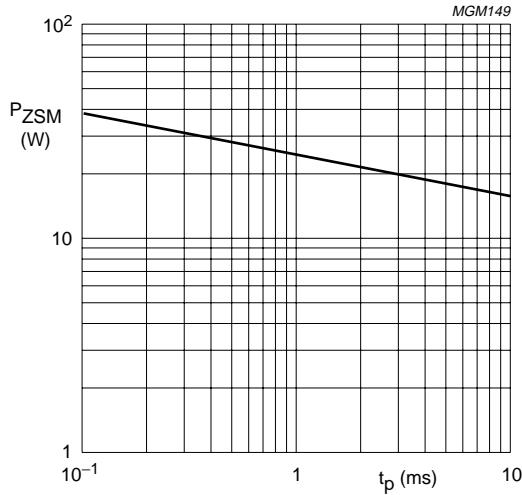


Fig.2 Maximum non-repetitive peak reverse current as a function of pulse time.



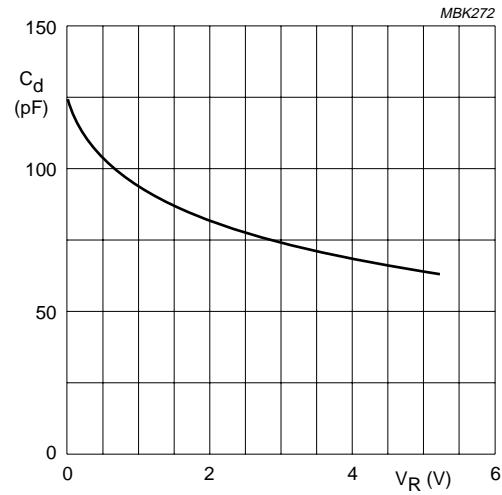
One or more diodes loaded.

Fig.3 Power derating curve.



$P_{ZSM} = V_{ZSM} \times I_{ZSM}$.
 V_{ZSM} is the non-repetitive peak reverse voltage at I_{ZSM} .

Fig.4 Maximum non-repetitive peak reverse power dissipation as a function of pulse duration (square pulse).

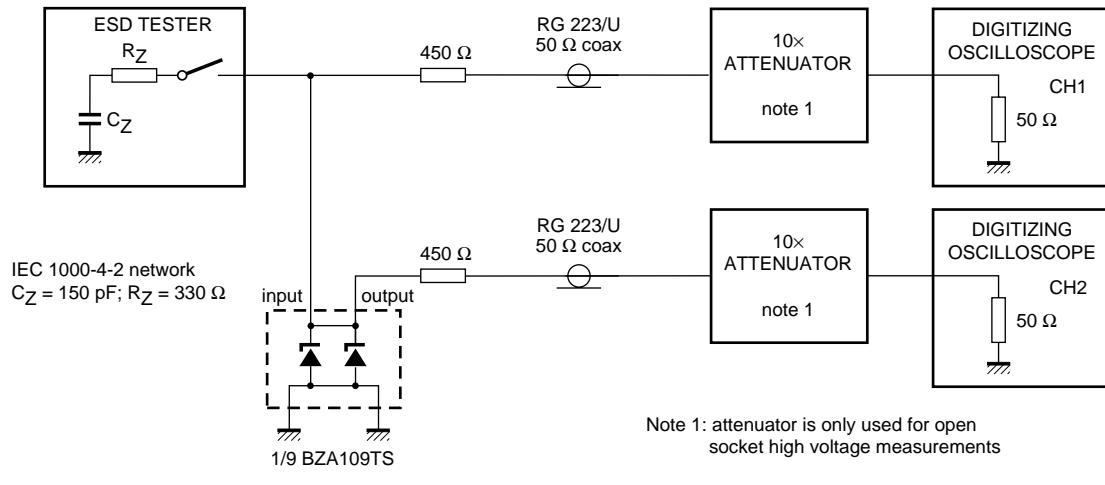


$T_j = 25$ °C; $f = 1$ MHz.

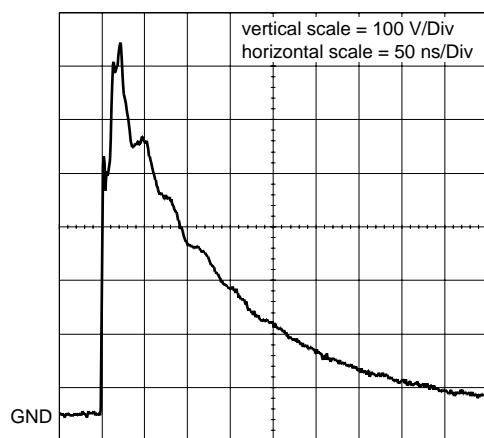
Fig.5 Diode capacitance as a function of reverse voltage; typical values.

9-fold ESD transient voltage suppressor

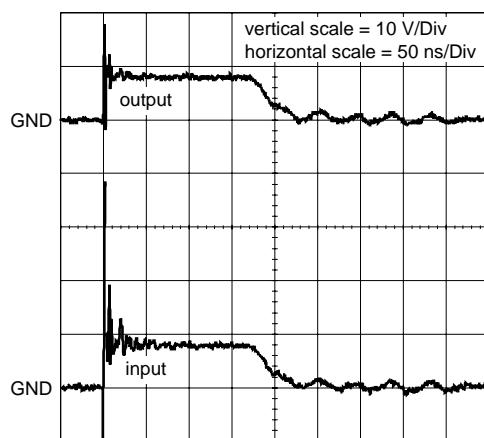
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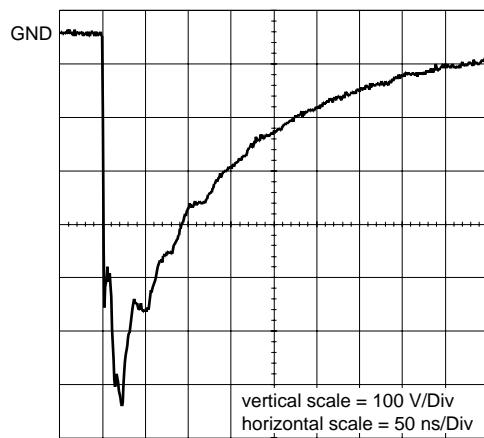
Note 1: attenuator is only used for open
socket high voltage measurements



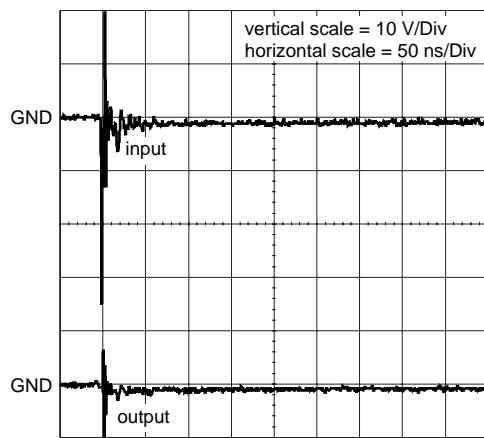
unclamped +1 kV ESD voltage waveform
(IEC1000-4-2 network)



clamped +1 kV ESD voltage waveform
(IEC1000-4-2 network)



unclamped -1 kV ESD voltage waveform
(IEC1000-4-2 network)



clamped -1 kV ESD voltage waveform
(IEC1000-4-2 network)

MBK392

Fig.6 ESD clamping test set-up and waveforms.

9-fold ESD transient voltage suppressor

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APPLICATION INFORMATION

Typical common anode application

A 9-fold transient suppressor in an SSOP20; SOT339-1 package makes it possible to protect nine separate lines using only one package. Two simplified examples are shown in Figs 7 and 8.

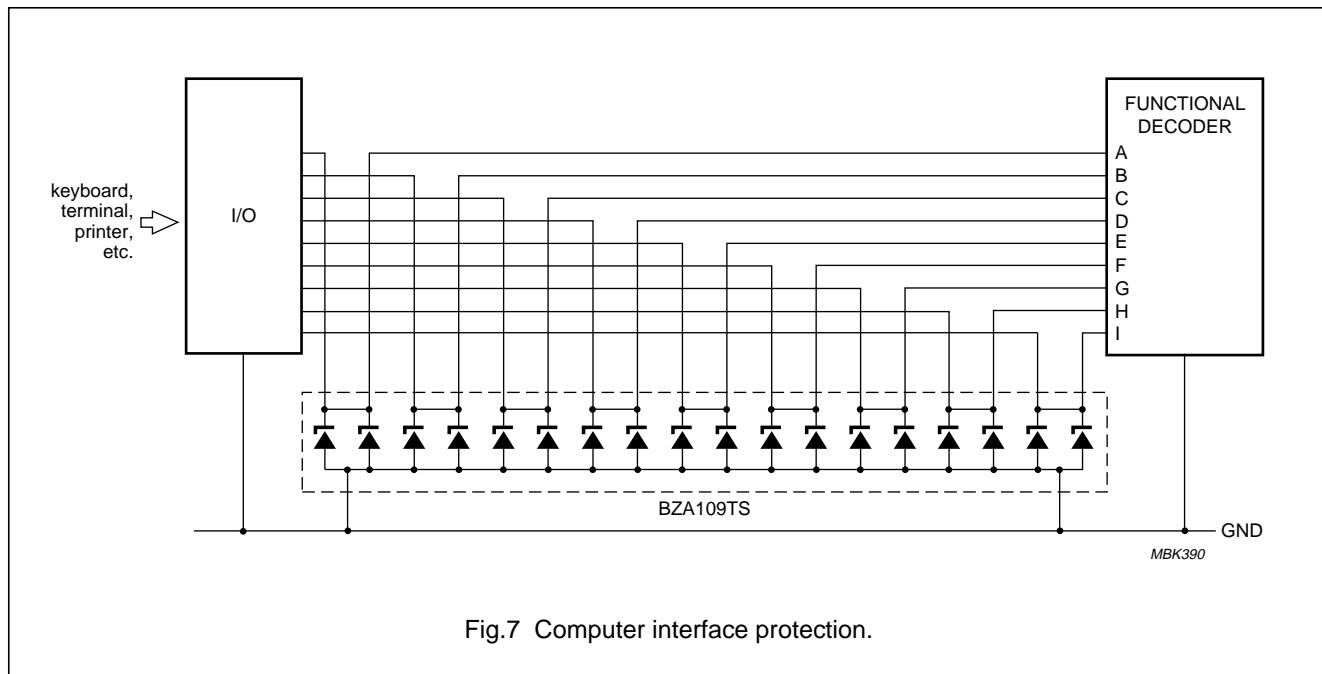


Fig.7 Computer interface protection.

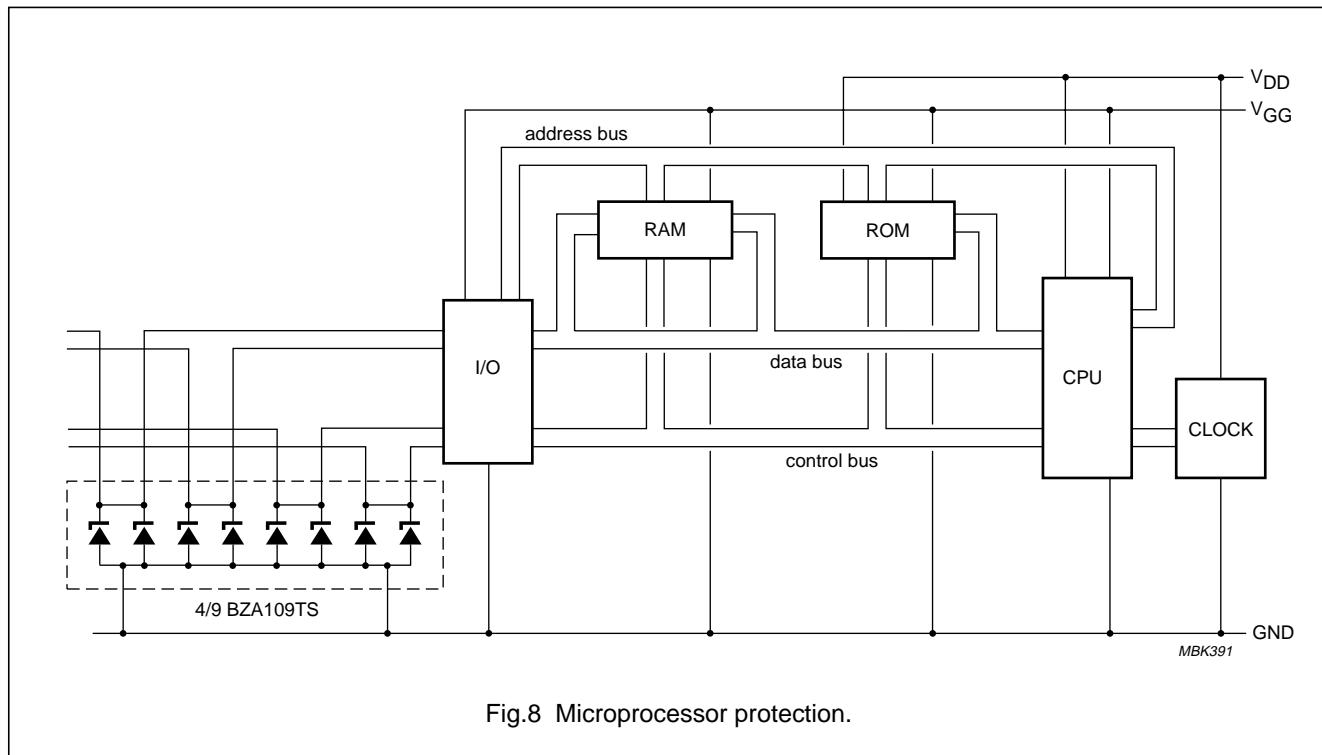


Fig.8 Microprocessor protection.

9-fold ESD transient voltage suppressor

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Device placement and printed-circuit board layout

Circuit board layout is of extreme importance in the suppression of transients. The clamping voltage of the BZA109TS is determined by the peak transient current and the rate of rise of that current (di/dt). Since parasitic inductances can further add to the clamping voltage ($V = L di/dt$) the series conductor lengths on the printed-circuit board should be kept to a minimum. This includes the lead length of the suppression element.

In addition to minimizing conductor length the following printed-circuit board layout guidelines are recommended:

1. Place the suppression element close to the input terminals or connectors.
2. Keep parallel signal paths to a minimum.
3. Avoid running protection conductors in parallel with unprotected conductors.
4. Minimize all printed-circuit board loop areas including power and ground loops.
5. Minimize the length of the transient return path to ground.
6. Avoid using shared transient return paths to a common ground point.

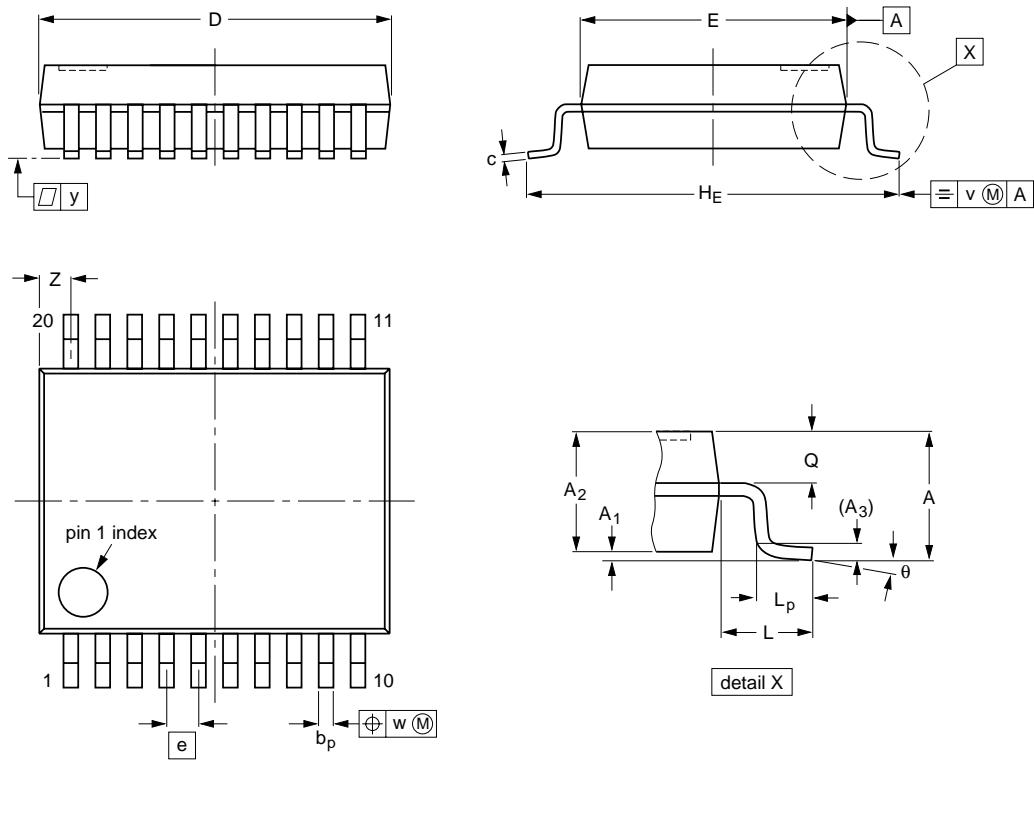
9-fold ESD transient voltage suppressor

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PACKAGE OUTLINE

SSOP20: plastic shrink small outline package; 20 leads; body width 5.3 mm

SOT339-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽¹⁾	e	H _E	L	L _p	Q	v	w	y	z ⁽¹⁾	θ
mm	2.0 0.05	0.21 1.65	1.80	0.25	0.38 0.25	0.20 0.09	7.4 7.0	5.4 5.2	0.65	7.9 7.6	1.25	1.03 0.63	0.9 0.7	0.2	0.13	0.1	0.9 0.5	8° 0°

Note

- Plastic or metal protrusions of 0.20 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT339-1		MO-150AE				-93-09-08 95-02-04

9-fold ESD transient voltage suppressor**BZA109TS****DEFINITIONS**

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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NOTES

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