BUT11XI

GENERAL DESCRIPTION

High-voltage, high-speed glass-passivated npn power transistor in a plastic full-pack envelope intended for use in electronic HF/OH lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

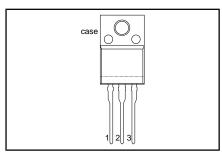
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	1000	V
V_{CEO}	Collector-emitter voltage (open base)		-	450	V
I _C	Collector current (DC)		-	5	Α
1 1	Collector current peak value		-	10	Α
P _{tot}	Total power dissipation	$T_{bc} \le 25 ^{\circ}C$	-	32	W
V _{CEsat}	Collector-emitter saturation voltage	$I_{hs} \le 25 ^{\circ}\text{C}$ $I_{C} = 2.5 \text{A}; I_{B} = 0.33 \text{A}$	-	1.5	V
l _{Csat}	Collector saturation current Fall time	I _{Con} =2.5 A; I _{Bon} =0.5 A	2.5 80	- 150	A ns

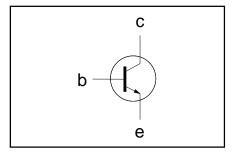
PINNING - SOT186A

PIN	DESCRIPTION	
1	base	
2	collector	
3	emitter	
case	isolated	

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	1000	V
V_{CEO}	Collector-emitter voltage (open base)		-	450	V
I _C	Collector current (DC)		-	5	Α
I _{CM}	Collector current peak value		-	10	Α
I _B	Base current (DC)		-	2	Α
I _{BM}	Base current peak value		-	4	Α
P _{tot}	Total power dissipation	$T_{hs} \le 25 ^{\circ}C$	-	32	W
T _{stg}	Storage temperature		-65	150	°C
Tj	Junction temperature		-	150	°C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R _{th j-hs}	Junction to heatsink	with heatsink compound	-	3.95	K/W
R _{th j-a}	Junction to ambient	in free air	55	-	K/W

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ISOLATION LIMITING VALUE & CHARACTERISTIC

 T_{hs} = 25 $^{\circ}$ C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{isol}	R.M.S. isolation voltage from all three terminals to external heatsink	f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65%; clean and dustfree	ı		2500	V
C _{isol}	Capacitance from T2 to external heatsink	f = 1 MHz	-	10	-	pF

STATIC CHARACTERISTICS

 T_{hs} = 25 $^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES}	Collector cut-off current 1	$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}, V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax};$	-	-	1.0	mĄ
I _{CES}		$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax};$ $T_i = 125 \text{ °C}$	-	-	2.0	mA
I _{EBO}	Emitter cut-off current	$V_{EB} = 9 \text{ V}; I_{C} = 0 \text{ A}$	-	-	10	mA
V _{CEOsust}	Collector-emitter sustaining voltage	I _B = 0 A; I _C = 100 mA; L = 25 mH	450	-	-	V
V_{CEsat}	Collector-emitter saturation voltages	$I_{\rm C} = 2.5 \text{ A}; I_{\rm B} = 0.33 \text{ A}$	-	-	1.5	V
V _{BEsat}	Base-emitter saturation voltage	$I_{\rm C} = 2.5 \text{A}; I_{\rm B} = 0.33 \text{A}$	-	-	1.3	V
h _{FE}	DC current gain	$I_{\rm C} = 5 \text{mA}; V_{\rm CE} = 5 \text{V}$	10	20	35	
h _{FE}		$I_{\rm C} = 500 \text{ mA}; V_{\rm CE} = 5 \text{ V}$	14	22	35	
h _{FEsat}		$I_C = 2.5 \text{ A}; V_{CE} = 5 \text{ V}$	9	13	17	

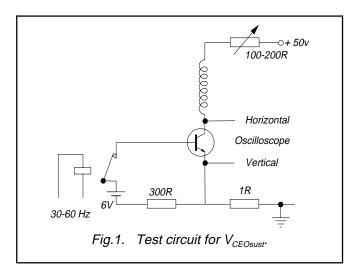
DYNAMIC CHARACTERISTICS

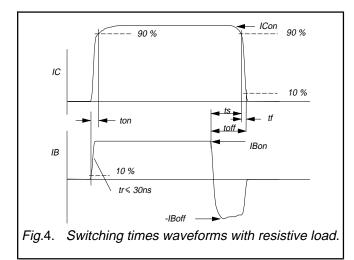
 $T_{hs} = 25$ °C unless otherwise specified

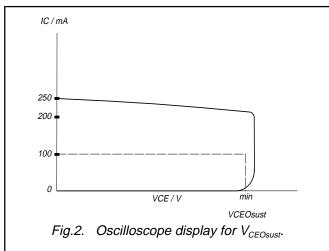
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
t _{on} t _s t _f	Switching times (resistive load) Turn-on time Turn-off storage time Turn-off fall time	$I_{Con} = 2.5 \text{ A}; I_{Bon} = -I_{Boff} = 0.5 \text{ A}$	0.6 3.4 0.6	1.0 4.0 0.8	μs μs μs
t _s	Switching times (inductive load) Turn-off storage time Turn-off fall time	$I_{Con} = 2.5 \text{ A}; I_{Bon} = 0.5 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}$	1.1 100	1.4 150	μs ns
t _s	Switching times (inductive load) Turn-off storage time Turn-off fall time	$I_{Con} = 2.5 \text{ A}; I_{Bon} = 0.5 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}; T_{j} = 100 ^{\circ}\text{C}$	1.2 140	1.5 300	μs ns

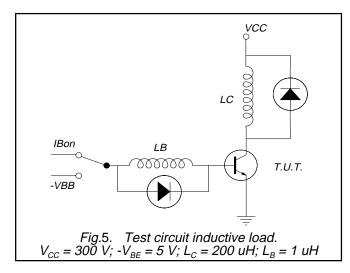
¹ Measured with half sine-wave voltage (curve tracer).

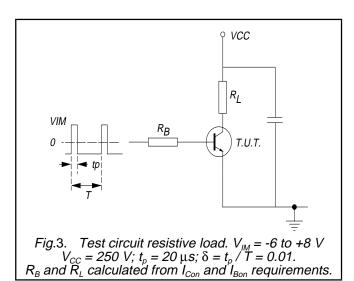
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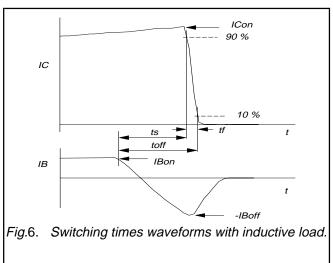












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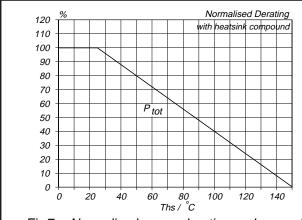


Fig.7. Normalised power derating and second breakdown curves.

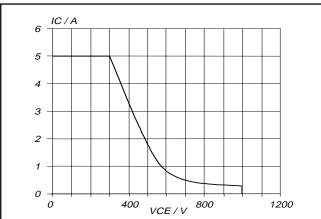
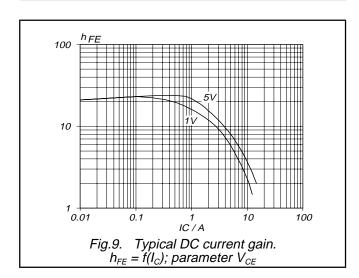


Fig.8. Reverse bias safe operating area. $T_i \le T_{i \max}$



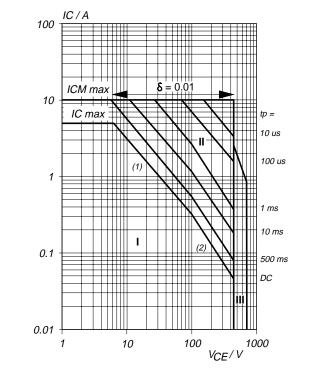
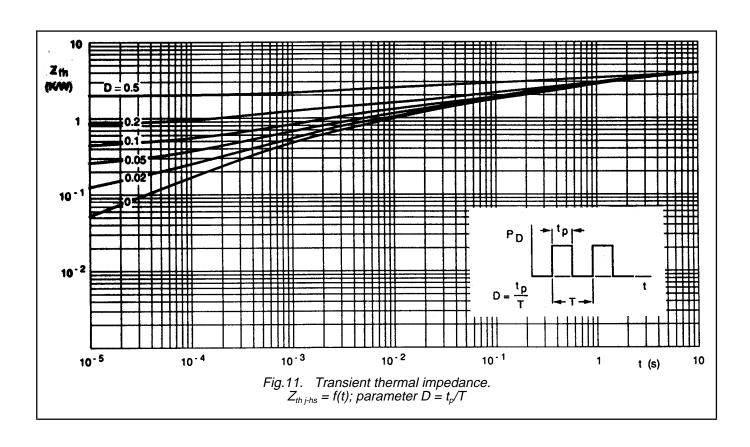


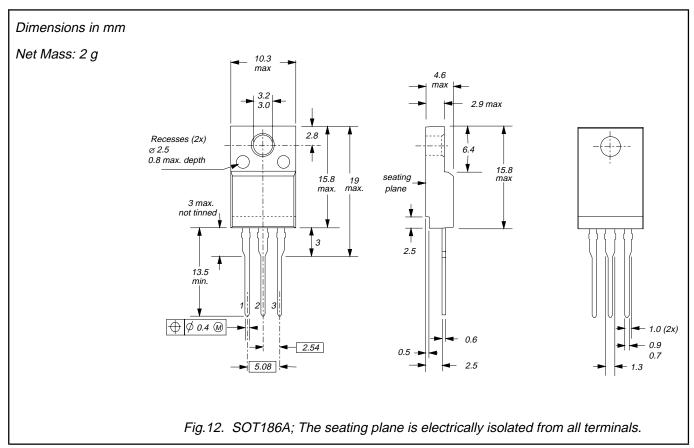
Fig.10. Forward bias safe operating area. $T_{hs} \le 25$ °C

- (1) (2)
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- P_{tot} max and P_{tot} peak max lines. Second breakdown limits. Region of permissible DC operation. Extension for repetitive pulse operation. Extension during turn-on in single transistor converters provided that $R_{BE} \le 100 \, \Omega$ and $t_p \le 0.6 \, \mu s$. NB: 30 ± 5 newton force on the centre of the
 - envelope.



BUT11XI

MECHANICAL DATA



- Refer to mounting instructions for F-pack envelopes.
 Epoxy meets UL94 V0 at 1/8".

Philips Semiconductors Product specification

Silicon Diffused Power Transistor

BUT11XI

DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limitin or conferen				

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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