## BU4522DX

### **GENERAL DESCRIPTION**

Enhanced performance, new generation, high-voltage, high-speed switching npn transistor with an integrated damper diode in a plastic full-pack envelope intended for use in horizontal deflection circuits of colour television receivers and p.c monitors. Features exceptional tolerance to base drive and collector current load variations resulting in a very low worst case dissipation.

#### QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V <sub>CESM</sub>	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	1500	V
V <sub>CEO</sub>	Collector-emitter voltage (open base)		-	800	V
I <sub>c</sub>	Collector current (DC)		-	10	Α
1.1	Collector current peak value		-	25	Α
I <sub>CM</sub> P <sub>tot</sub> V <sub>CEsat</sub>	Total power dissipation	T <sub>hs</sub> ≤ 25 °C	-	45	W
V <sub>CEsat</sub>	Collector-emitter saturation voltage	$I_{\rm C} = 7 \text{ A}; I_{\rm B} = 1.75 \text{ A}$	-	3.0	V
I <sub>Csat</sub>	Collector saturation current (Fig 17)	f = 16  kHz	7	-	Α
oodi		f = 64 kHz	6	-	Α
V <sub>F</sub>	Diode forward voltage	$I_{\rm F} = 7.0  {\rm A}$	-	2.2	V
t,	Fall time	$I_{Csat} = 7 \text{ A}; f = 16 \text{ kHz}$	285	400	ns
-		f = 64  kHz	t.b.f	t.b.f	ns

#### **PINNING - SOT399**

base

collector

emitter

isolated

DESCRIPTION

PIN

1

2

3

case

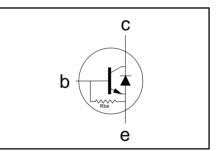
#### **PIN CONFIGURATION**

0 °

213

case

#### SYMBOL



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CESM</sub>	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	1500	V
V <sub>CEO</sub>	Collector-emitter voltage (open base)			800	V
	Collector current (DC)		-	10	A
I <sub>CM</sub>	Collector current peak value		-	25	A
I <sub>B</sub>	Base current (DC)			6	A
I <sub>BM</sub>	Base current peak value		-	9	A
-I <sub>BM</sub>	Reverse base current peak value <sup>1</sup>			6	A
P <sub>tot</sub>	Total power dissipation	$T_{hs} \leq 25 \degree C$	-	45	W
T <sub>stq</sub>	Storage temperature	113	-55	150	°C
	Junction temperature		-	150	°C

<sup>1</sup> Turn-off current.

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## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R <sub>th j-hs</sub>	Junction to heatsink	with heatsink compound	-	2.8	K/W
R <sub>th j-a</sub>	Junction to ambient	in free air	35	-	K/W

#### **ISOLATION LIMITING VALUE & CHARACTERISTIC**

 $T_{hs} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>isol</sub>	Repetitive peak voltage from all three terminals to external heatsink	$R.H. \leq 65~\%$ ; clean and dustfree	-	-	2500	V
C <sub>isol</sub>	Capacitance from T2 to external heatsink	f = 1 MHz	-	22	-	рF

### STATIC CHARACTERISTICS

 $T_{hs} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CES</sub>	Collector cut-off current <sup>2</sup>	$V_{BE} = 0 V; V_{CE} = V_{CESMmax}$	-	-	1.0	mA
I <sub>CES</sub>		$V_{BE} = 0 V; V_{CE} = V_{CESMmax}$ $V_{BE} = 0 V; V_{CE} = V_{CESMmax};$	-	-	2.0	mA
BV <sub>EBO</sub>	Emitter-base breakdown voltage	T <sub>j</sub> = 125 °C I <sub>B</sub> = 600 mA	7.5	13.5	-	V
	Base-emitter resistance	$V_{EB} = 7.5 V$	-	50	-	Ω
V <sub>CEOsust</sub>	Collector-emitter sustaining voltage	I <sub>B</sub> = 0 A; I <sub>C</sub> = 100 mA; L = 25 mH	800	-	-	V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 7 A; I <sub>B</sub> = 1.75 A	-	-	3.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	Ι <sub>C</sub> = 7 A; Ι <sub>B</sub> = 1.75 A	0.85	0.94	1.03	V
h <sub>FE</sub>	DC current gain	$I_{c} = 1 A; V_{cE} = 5 V$	-	10	-	
h <sub>FE</sub>		$I_{\rm C} = 7$ A; $V_{\rm CE} = 5$ V	4.2	5.8	7.3	V
V <sub>F</sub>	Diode forward voltage	$I_F = 7 A$	-	-	2.2	V

## **DYNAMIC CHARACTERISTICS**

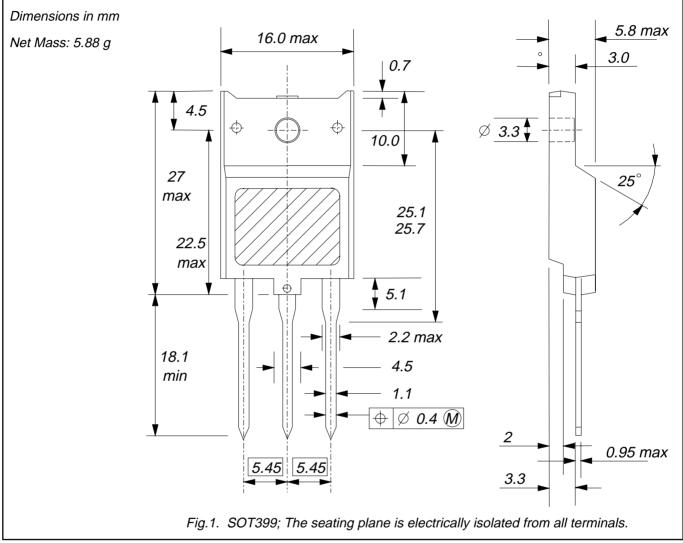
 $T_{hs} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
t <sub>s</sub> t <sub>f</sub>	Switching times (16 kHz line deflection circuit) Turn-off storage time Turn-off fall time	f = 16 kHz; $I_{Csat}$ = 7 A; $I_{B1}$ = 1.4 A; $(I_{B2}$ = -3.5 A)	3.5 285	4.3 400	μs ns
t <sub>s</sub> t <sub>f</sub>	Switching times (64 kHz line deflection circuit) Turn-off storage time Turn-off fall time	$I_{Csat} = t.b.f$	t.b.f t.b.f	t.b.f t.b.f	μs ns

<sup>2</sup> Measured with half sine-wave voltage (curve tracer).

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## **MECHANICAL DATA**



#### Notes

Refer to mounting instructions for F-pack envelopes.
Epoxy meets UL94 V0 at 1/8".

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#### DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	reliminary specification This data sheet contains preliminary data; supplementary data may be published later			
Product specification	This data sheet contains final product specifications.			
Limiting values				
or more of the limiting val operation of the device at	in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one ues may cause permanent damage to the device. These are stress ratings only and these or at any other conditions above those given in the Characteristics sections of plied. Exposure to limiting values for extended periods may affect device reliability.			
Application information				
Where application information is given, it is advisory and does not form part of the specification.				
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## LIFE SUPPORT APPLICATIONS

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