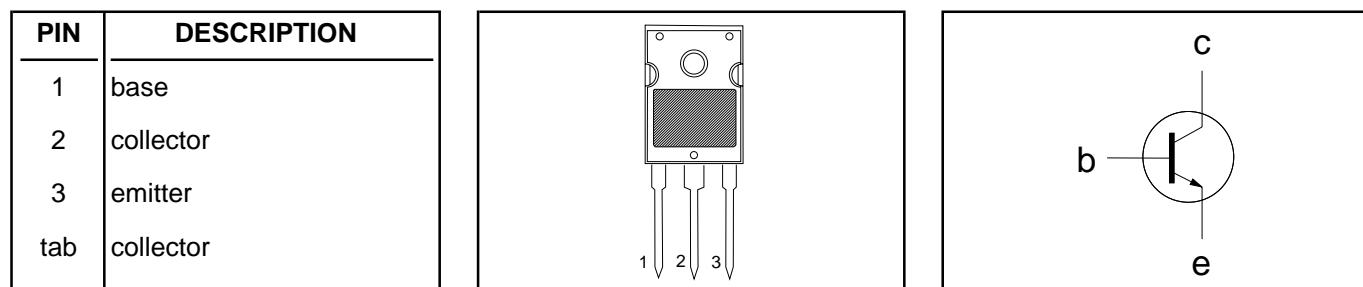


Silicon Diffused Power Transistor**BU2527AW****GENERAL DESCRIPTION**

New generation, high-voltage, high-speed switching npn transistor in a plastic envelope intended for use in horizontal deflection circuits of high resolution monitors. Features improved RBSOA performance and is suitable for operation up to 64 kHz.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	1500	V
V_{CEO}	Collector-emitter voltage (open base)		-	800	V
I_C	Collector current (DC)		-	12	A
I_{CM}	Collector current peak value		-	30	A
P_{tot}	Total power dissipation	$T_{mb} \leq 25 \text{ }^{\circ}\text{C}$	-	125	W
V_{CEsat}	Collector-emitter saturation voltage	$I_C = 6.0 \text{ A}; I_B = 1.2 \text{ A}$	-	5.0	V
I_{Csat}	Collector saturation current		6.0	-	A
t_s	Storage time	$I_{Csat} = 6.0 \text{ A}; I_{B(end)} = 0.55 \text{ A}$	1.7	2.0	μs

PINNING - SOT429**PIN CONFIGURATION****SYMBOL****LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	1500	V
V_{CEO}	Collector-emitter voltage (open base)		-	800	V
I_C	Collector current (DC)		-	12	A
I_{CM}	Collector current peak value		-	30	A
I_B	Base current (DC)		-	8	A
I_{BM}	Base current peak value		-	12	A
$-I_{B(AV)}$	Reverse base current	average over any 20 ms period	-	200	mA
$-I_{BM}$	Reverse base current peak value ¹		-	7	A
P_{tot}	Total power dissipation	$T_{mb} \leq 25 \text{ }^{\circ}\text{C}$	-	125	W
T_{stg}	Storage temperature		-65	150	$^{\circ}\text{C}$
T_j	Junction temperature		-	150	$^{\circ}\text{C}$

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{th j-mb}$	Junction to mounting base		-	1.0	K/W
$R_{th j-a}$	Junction to ambient	in free air	45	-	K/W

¹ Turn-off current.

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STATIC CHARACTERISTICS $T_{mb} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CES}	Collector cut-off current ²	$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	0.25	mA
I_{CES}		$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	2.0	mA
I_{EBO}	Emitter cut-off current	$T_j = 125^\circ\text{C}$	-	-	0.25	mA
BV_{EBO}	Emitter-base breakdown voltage	$V_{EB} = 7.5 \text{ V}; I_c = 0 \text{ A}$	7.5	13.5	-	V
$V_{CEO}sust$	Collector-emitter sustaining voltage	$I_B = 1 \text{ mA}$	800	-	-	V
V_{CEsat}	Collector-emitter saturation voltage	$I_B = 0 \text{ A}; I_c = 100 \text{ mA}; L = 25 \text{ mH}$	-	-	5.0	V
V_{BEsat}	Base-emitter saturation voltage	$I_c = 6.0 \text{ A}; I_B = 1.2 \text{ A}$	-	-	1.3	V
h_{FE}	DC current gain	$I_c = 6.0 \text{ A}; I_B = 1.2 \text{ A}$	-	10	-	
h_{FE}		$I_c = 1 \text{ A}; V_{CE} = 5 \text{ V}$	5	7	9	
		$I_c = 6 \text{ A}; V_{CE} = 5 \text{ V}$				

DYNAMIC CHARACTERISTICS $T_{mb} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
C_c	Collector capacitance	$I_E = 0 \text{ A}; V_{CB} = 10 \text{ V}; f = 1 \text{ MHz}$	145	-	pF
t_s	Switching times (64 kHz line deflection circuit)	$I_{CM} = 6.0 \text{ A}; L_C = 170 \mu\text{H}; C_{fb} = 5.4 \text{ nF}$ $I_{B(end)} = 0.55 \text{ A}; L_B = 0.6 \mu\text{H}; -V_{BB} = 2 \text{ V}; (-dI_B/dt = 3.33 \text{ A}/\mu\text{s})$			
t_f	Turn-off storage time Turn-off fall time		1.7 0.1	2.0 0.2	μs

² Measured with half sine-wave voltage (curve tracer).

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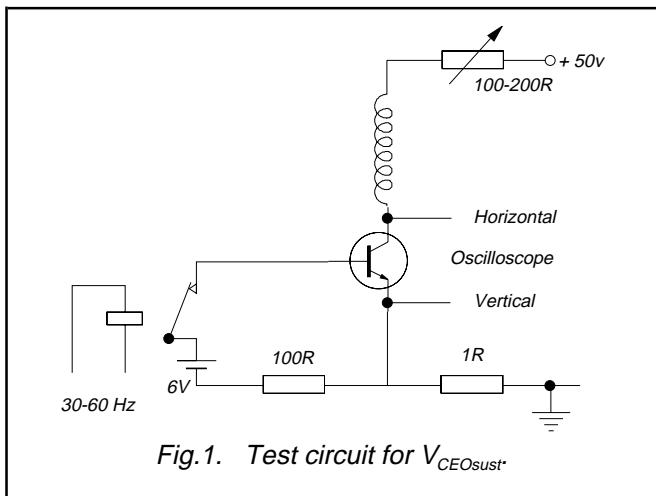


Fig. 1. Test circuit for V_{CEO} sust.

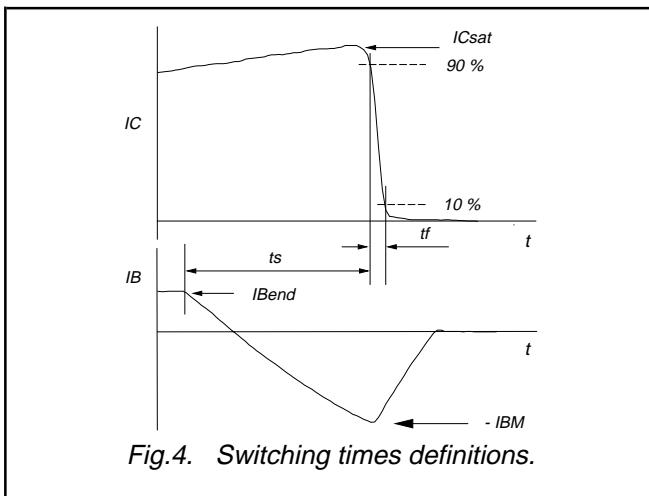


Fig.4. Switching times definitions.

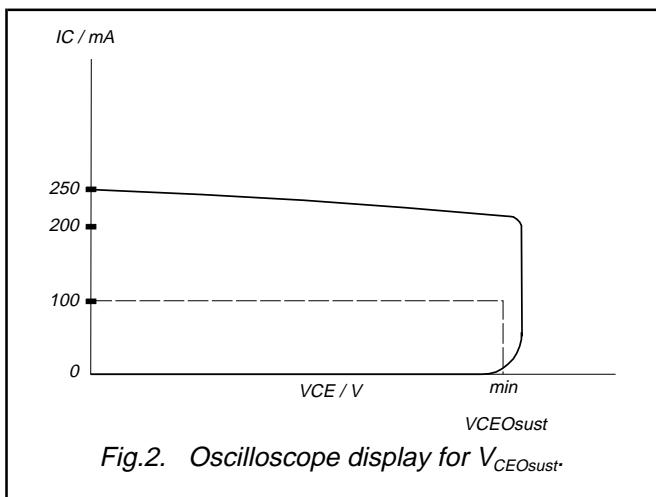


Fig.2. Oscilloscope display for V_{CEO} sust.

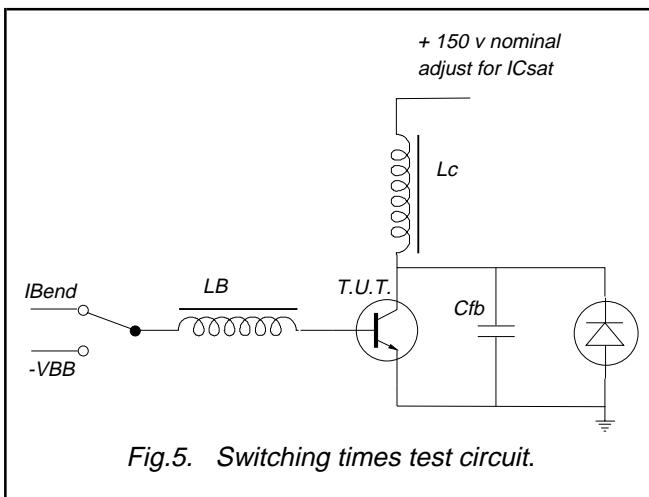


Fig.5. Switching times test circuit.

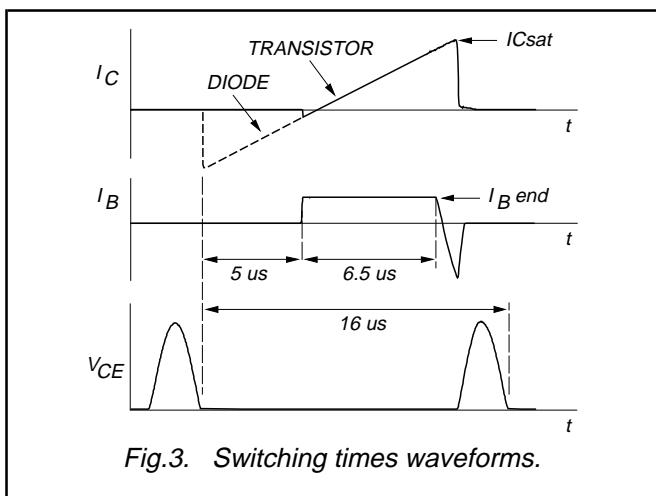


Fig.3. Switching times waveforms.

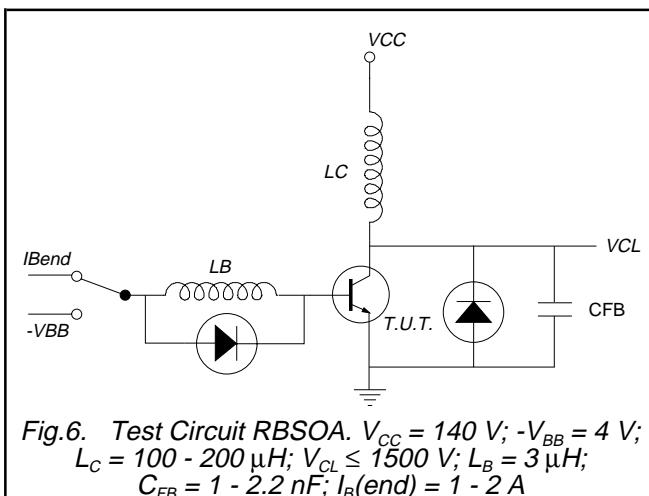


Fig.6. Test Circuit RBSOA. $V_{CC} = 140$ V; $-V_{BB} = 4$ V;
 $L_C = 100 - 200 \mu H$; $V_{CL} \leq 1500$ V; $L_B = 3 \mu H$;
 $C_{FB} = 1 - 2.2 nF$; $I_B(\text{end}) = 1 - 2$ A

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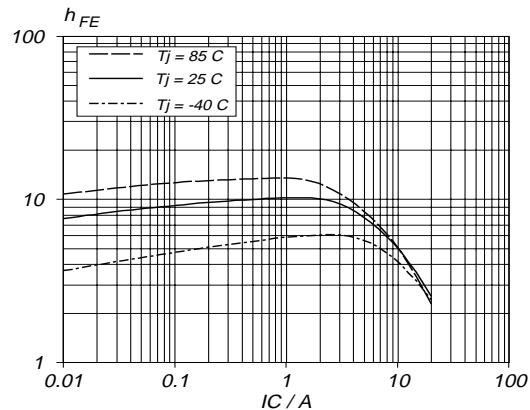


Fig.7. Typical DC current gain. $h_{FE} = f(I_C)$
 $V_{CE} = 5 V$

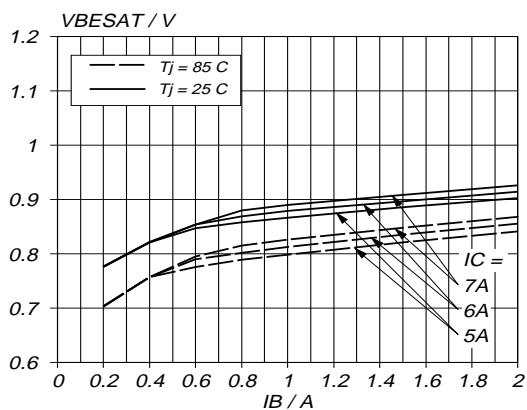


Fig.10. Typical base-emitter saturation voltage.
 $V_{BE\text{sat}} = f(I_B)$; parameter I_C

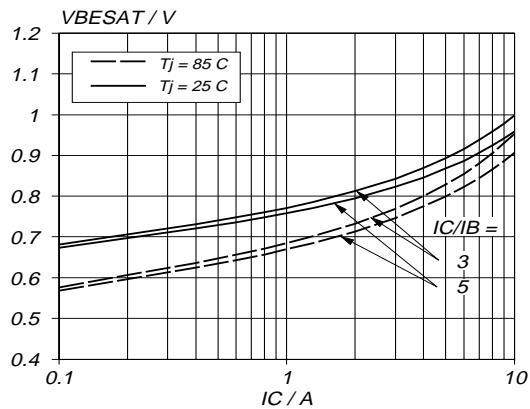


Fig.8. Typical base-emitter saturation voltage.
 $V_{BE\text{sat}} = f(I_C)$; parameter I_C/I_B

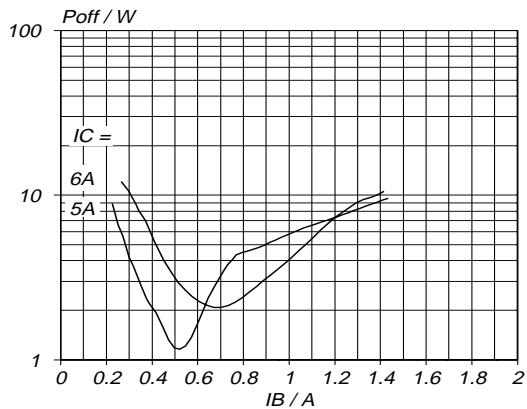


Fig.11. Typical turn-off losses. $T_j = 85^\circ C$
 $P_{off} = f(I_B)$; parameter I_C ; $f = 64 \text{ kHz}$

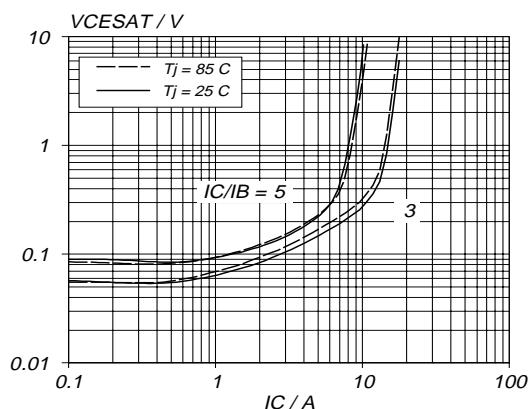


Fig.9. Typical collector-emitter saturation voltage.
 $V_{CE\text{sat}} = f(I_C)$; parameter I_C/I_B

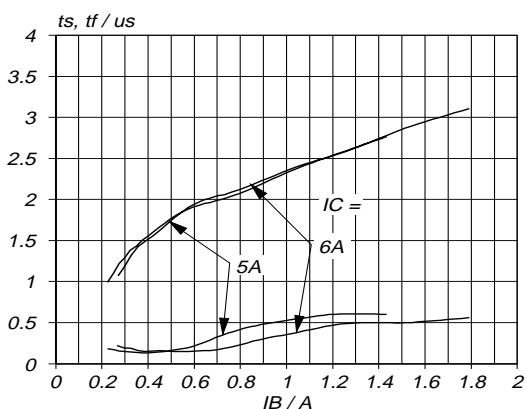


Fig.12. Typical collector storage and fall time.
 $ts = f(I_B)$; $tf = f(I_B)$; parameter I_C ; $T_j = 85^\circ C$; $f = 64 \text{ kHz}$

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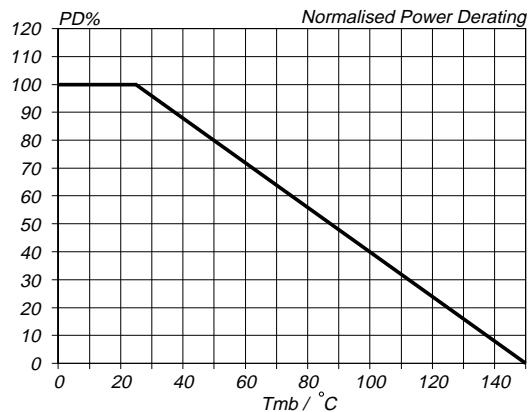


Fig.13. Normalised power dissipation.
 $PD\% = 100 \cdot P_D / P_{D,25^\circ C} = f(T_{mb})$

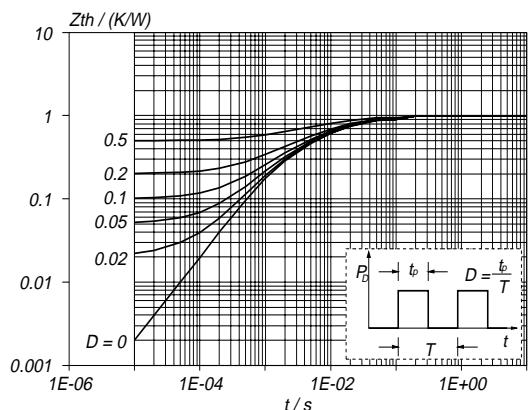


Fig.14. Transient thermal impedance.
 $Z_{th,j-mb} = f(t); \text{ parameter } D = t_p/T$

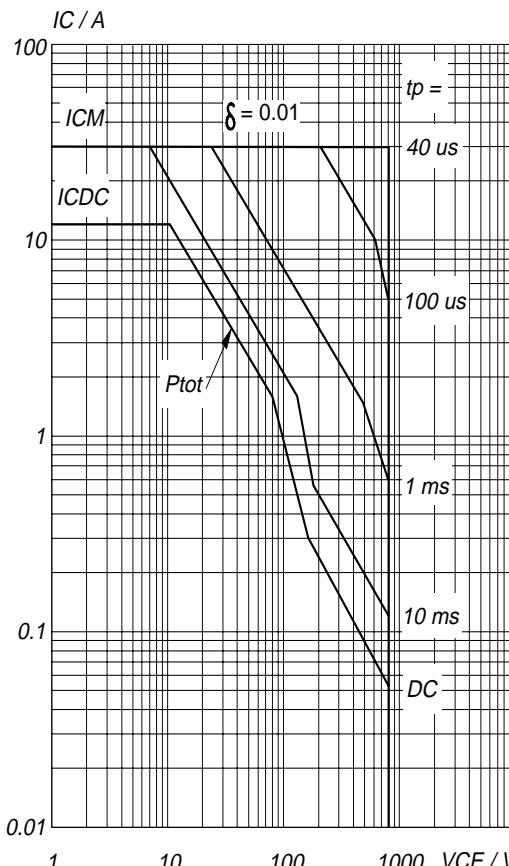


Fig.15. Forward bias safe operating area. $T_{mb} = 25^\circ C$
 $I_{CDC} \& I_{CM} = f(V_{CE}); I_{CM}$ single pulse; parameter t_p .
Second-breakdown limits independant of temperature.

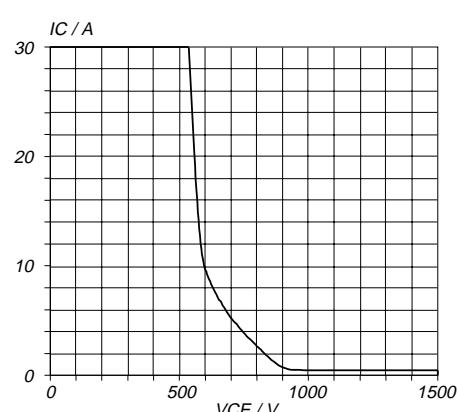


Fig.16. Reverse bias safe operating area. $T_j \leq T_{jmax}$

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MECHANICAL DATA

Dimensions in mm

Net Mass: 5 g

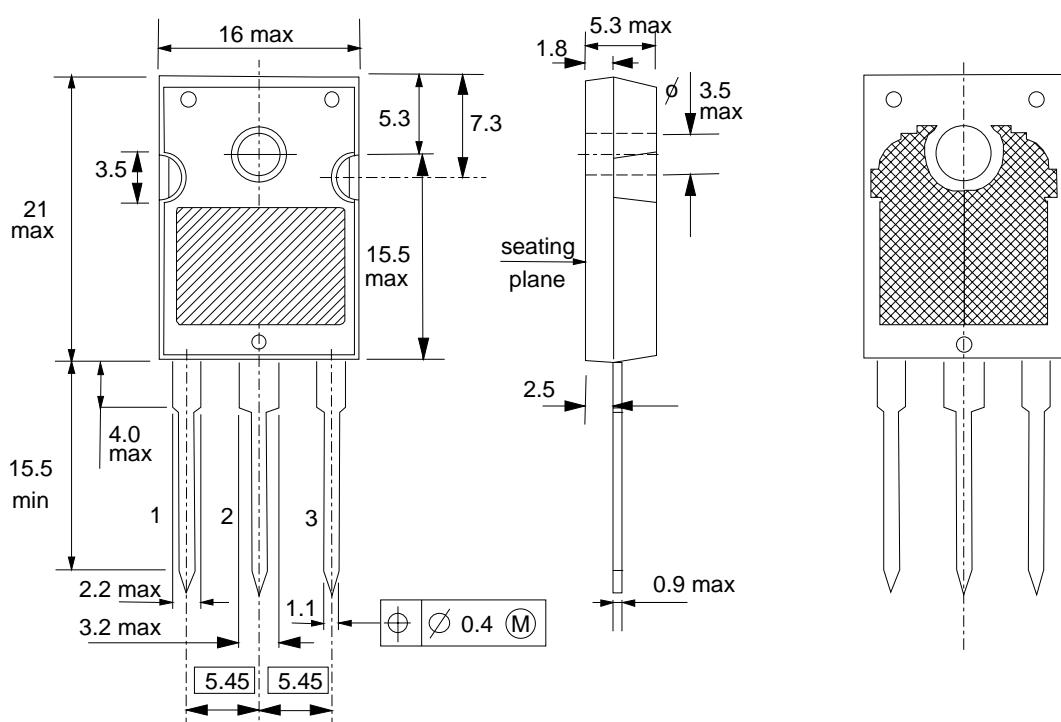


Fig.17. SOT429; pin 2 connected to mounting base.

Notes

1. Refer to mounting instructions for SOT429 envelope.
2. Epoxy meets UL94 V0 at 1/8".

Silicon Diffused Power Transistor**BU2527AW****DEFINITIONS**

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	
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