BTA208S series B

BTA208M series B

GENERAL DESCRIPTION

Glass passivated high commutation triacs in a plastic envelope, suitable for surface mounting, intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. These devices will commutate the full rated rms current at the maximum rated junction temperature,

QUICK REFERENCE DATA

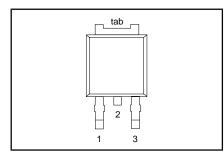
SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V _{DRM} I _{T(RMS)} I _{TSM}	BTA208S (or BTA208M)- Repetitive peak off-state voltages RMS on-state current Non-repetitive peak on-state current	500B 500 8 65	600B 600 8 65	800B 800 8 65	V A A

PINNING - SOT428

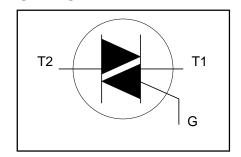
without the aid of a snubber.

PIN NUMBER	Standard S	Alternative M
1	MT1	gate
2	MT2	MT2
3	gate	MT1
tab	MT2	MT2

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT	
V_{DRM}	Repetitive peak off-state voltages		-	-500 500 ¹	-600 600 ¹	-800 800	V
I _{T(RMS)}	RMS on-state current	full sine wave;	-		8		Α
I _{TSM}	Non-repetitive peak on-state current	$T_{mb} \le 102 ^{\circ}\text{C}$ full sine wave; $T_{j} = 25 ^{\circ}\text{C}$ prior to surge $t = 20 \text{ms}$	-		65		A
l ² t	I ² t for fusing	t = 16.7 ms t = 10 ms	-		71 21		A A ² s
dl _⊤ /dt	Repetitive rate of rise of on-state current after	$I_{TM} = 12 \text{ A}; I_{G} = 0.2 \text{ A};$ $dI_{G}/dt = 0.2 \text{ A}/\mu\text{s}$			100		A/μs
${\sf I}_{\sf GM} \ {\sf V}_{\sf GM}$	triggering Peak gate current		-		2		A
P_{GM}	Peak gate voltage Peak gate power		-		2 5 5		V W
P _{G(AV)}	Average gate power	over any 20 ms	-		0.5		W
$\frac{T_{stg}}{T_{j}}$	Storage temperature Operating junction temperature	period	-40 -		150 125		ိုင

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¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/µs.

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{\text{th j-mb}}$ $R_{\text{th j-a}}$	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle pcb (FR4) mounted; footprint as in Fig.14		- - 75	2.0 2.4 -	K/W K/W K/W

STATIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current ²	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$				
		T2+ G+	2	18	50	mΑ
		T2+ G-	2	21	50	mΑ
		T2- G-	2 2	34	50	mΑ
I _L	Latching current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$				
-		T2+ G+	-	31	60	mA
		T2+ G-	-	34	90	mA
		T2- G-	-	30	60	mA
l _H	Holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	31	60	mA
V _T	On-state voltage	$I_{T} = 10 \text{ A}$	-	1.3	1.65	V
V _{GT}	Gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$	-	0.7	1.5	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_L = 125 ^{\circ}\text{C}$	0.25	0.4	-	V
I _D	Off-state leakage current	$V_D = V_{DRM(max)}$; $T_j = 125$ °C	-	0.1	0.5	mA

DYNAMIC CHARACTERISTICS

 $T_j = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	V _{DM} = 67% V _{DRM(max)} ; T _j = 125 °C; exponential waveform; gate open circuit	1000	4000	-	V/μs
dl _{com} /dt	Critical rate of change of	$V_{DM} = 400 \text{ V}; T_i = 125 \text{ °C}; I_{T(RMS)} = 8 \text{ A};$	-	14	-	A/ms
t _{gt}	commutating current Gate controlled turn-on time	without snubber; gate open circuit $I_{TM} = 12 \text{ A}$; $V_D = V_{DRM(max)}$; $I_G = 0.1 \text{ A}$; $dI_G/dt = 5 \text{ A}/\mu\text{s}$	-	2	-	μs

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² Device does not trigger in the T2-, G+ quadrant.

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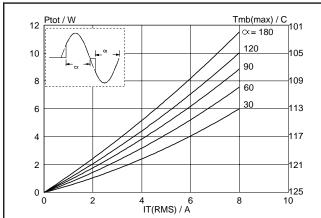


Fig.1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where $\alpha =$ conduction angle.

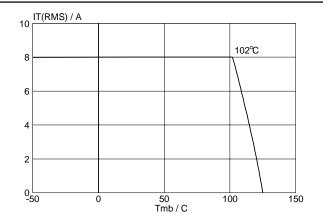


Fig.4. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

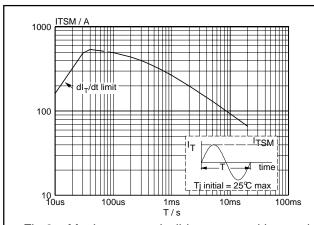


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \le 20$ ms.

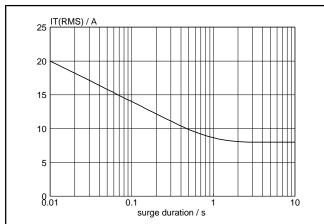


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50 Hz; $T_{mb} \le 102$ °C.

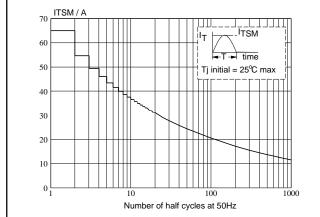


Fig.3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, f = 50 Hz.

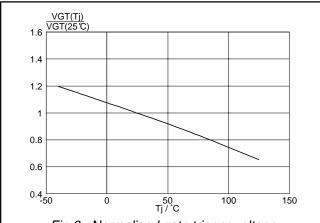
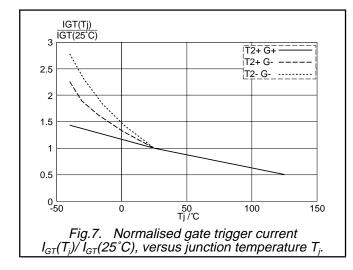
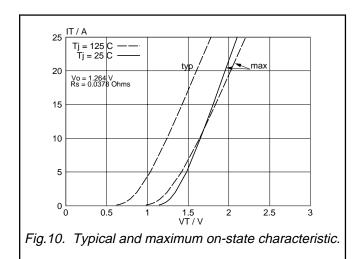
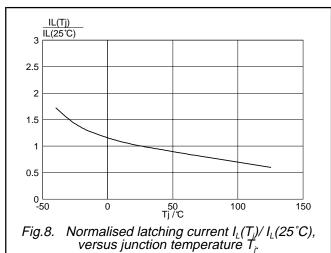


Fig.6. Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(25\,^{\circ}C)$, versus junction temperature T_j .

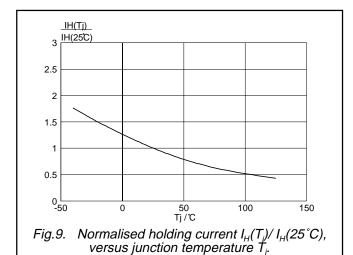
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 0.01_{00} 0.1ms 1ms 10ms 0.1s 1s 10s tp/s Fig.11. Transient thermal impedance $Z_{th j-mb}$, versus pulse width t_p .



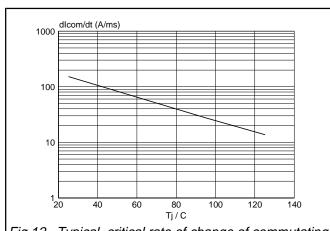
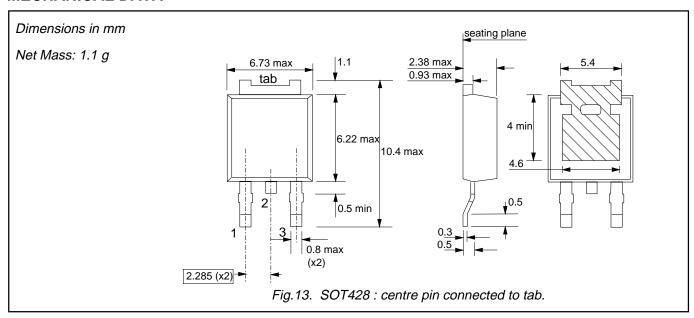


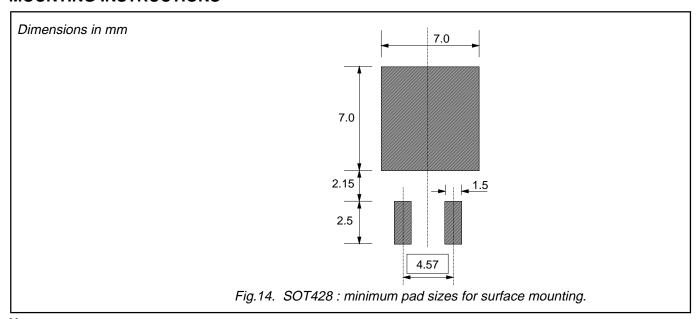
Fig.12. Typical, critical rate of change of commutating current, dl_{com}/dt versus junction temperature.

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MECHANICAL DATA



MOUNTING INSTRUCTIONS



Notes

1. Plastic meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status				
Objective specification This data sheet contains target or goal specifications for product development.				
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification This data sheet contains final product specifications.				

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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