# BTA204 series D, E and F

## **GENERAL DESCRIPTION**

# Passivated guaranteed commutation triacs in a plastic envelope, intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series and "logic level" D series are intended for interfacing with low power drivers, including micro controllers.

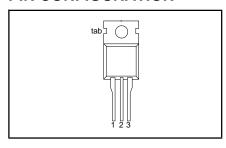
## **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
	BTA204- BTA204- BTA204-	500D 500E 500F	600D 600E 600F	- 800E 800F	
$V_{DRM}$	Repetitive peak	500	600	800	V
I <sub>T(RMS)</sub> I <sub>TSM</sub>	off-state voltages RMS on-state current Non-repetitive peak on-state current	4 25	4 25	4 25	A A

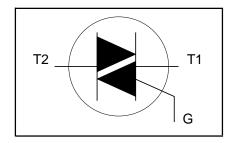
## **PINNING - TO220AB**

PIN	DESCRIPTION			
1	main terminal 1			
2	main terminal 2			
3	gate			
tab	main terminal 2			

# **PIN CONFIGURATION**



## **SYMBOL**



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
$V_{DRM}$	Repetitive peak off-state voltages			<b>-500</b> 500 <sup>1</sup>	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave;	-		4		Α
I <sub>TSM</sub>	Non-repetitive peak on-state current	$T_{mb} \le 107 ^{\circ}\text{C}$ full sine wave; $T_{j} = 25 ^{\circ}\text{C}$ prior to surge $t = 20 \text{ms}$	-		25		A
124	124 for five in a	t = 16.7 ms	-		27		A A <sup>2</sup> s
l²t dl <sub>⊤</sub> /dt	l <sup>2</sup> t for fusing Repetitive rate of rise of on-state current after	$I_{TM} = 6 \text{ A}; I_{G} = 0.2 \text{ A}; \\ dI_{G}/dt = 0.2 \text{ A}/\mu\text{s}$	-		3.1 100		A-s A/μs
${\sf I}_{\sf GM} \ {\sf V}_{\sf GM}$	triggering Peak gate current		-		2		A
$\begin{array}{c} V_{GM} \\ P_{GM} \\ P_{G(AV)} \end{array}$	Peak gate voltage Peak gate power Average gate power	over any 20 ms	- - -		2 5 5 0.5		W W
${f T}_{ m stg} \ {f T}_{ m j}$	Storage temperature Operating junction temperature	period	-40 -		150 125		ိုင

December 1998 1 Rev 1.000

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/µs.

BTA204 series D, E and F

# THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{\text{th j-mb}}$ $R_{\text{th j-a}}$	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle in free air		- - 60	3.0 3.7 -	K/W K/W

# STATIC CHARACTERISTICS

T<sub>i</sub> = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.		MAX.		UNIT
		BTA204-			D	Е	F	
I <sub>GT</sub>	Gate trigger current <sup>2</sup>	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ T2+ G+ T2+ G-	-	-	5	10 10	25 25	mA mA
   I <sub>L</sub>	Latching current	T2 + G T2 - G $V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	-	5 5	10	25	mA
	-	T2+ G+ T2+ G- T2- G-	- -	- -	6 9 6	12 18 12	20 30 20	mA mA mA
I <sub>H</sub>	Holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	-	6	12	20	mA
$V_{T}$	On-state voltage Gate trigger voltage	$I_T = 5 \text{ A}$ $V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ $V_D = 400 \text{ V}; I_T = 0.1 \text{ A};$	- - 0.25	1.4 0.7 0.4		1.7 1.5 -		V V V
I <sub>D</sub>	Off-state leakage current	$ T_j = 125 °C $ $ V_D = V_{DRM(max)}; $ $ T_j = 125 °C $	-	0.1		0.5		mA

# **DYNAMIC CHARACTERISTICS**

T<sub>i</sub> = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS		MIN.		TYP.	MAX.	UNIT
		BTA204-	D	E	F			
dV <sub>D</sub> /dt	Critical rate of rise of off-state voltage	V <sub>DM</sub> = 67% V <sub>DRM(max)</sub> ; T <sub>j</sub> = 125 °C; exponential waveform; gate open circuit	20	30	50	-	-	V/μs
dI <sub>com</sub> /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 125 ^{\circ}\text{C};$ $I_{T(RMS)} = 4 \text{ A};$ $dV_{com}/dt = 20 \text{V}/\mu\text{s}; \text{ gate}$ open circuit	1.0	2.0	2.5	-	-	A/ms
dl <sub>com</sub> /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 125 ^{\circ}\text{C};$ $I_{T(RMS)} = 4 \text{ A};$ $dV_{com}/dt = 0.1 \text{V}/\mu\text{s}; \text{ gate}$ open circuit	5.0	-	-	-	-	A/ms
$\mathbf{t}_{gt}$	Gate controlled turn-on time	$I_{TM} = 12 \text{ A}; V_D = V_{DRM(max)}; I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu s$	-	-	-	2	-	μs

<sup>2</sup> Device does not trigger in the T2-, G+ quadrant.

# BTA204 series D, E and F

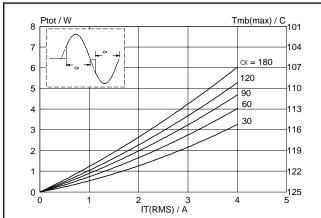


Fig.1. Maximum on-state dissipation,  $P_{tot}$ , versus rms on-state current,  $I_{T(RMS)}$ , where  $\alpha =$  conduction angle.

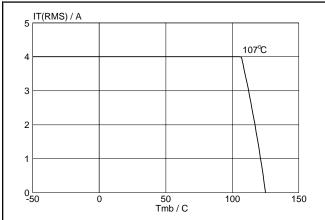


Fig.4. Maximum permissible rms current  $I_{T(RMS)}$ , versus mounting base temperature  $T_{mb}$ .

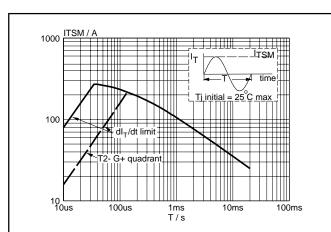


Fig.2. Maximum permissible non-repetitive peak on-state current  $I_{TSM}$ , versus pulse width  $t_p$ , for sinusoidal currents,  $t_p \le 20$ ms.

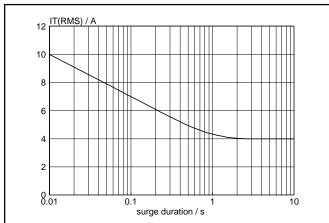


Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents, f = 50 Hz;  $T_{mb} \le 107$  °C.

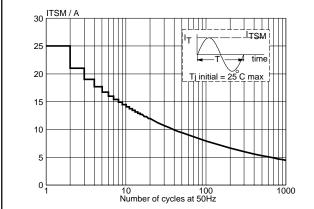


Fig.3. Maximum permissible non-repetitive peak on-state current  $I_{TSM}$ , versus number of cycles, for sinusoidal currents, f = 50 Hz.

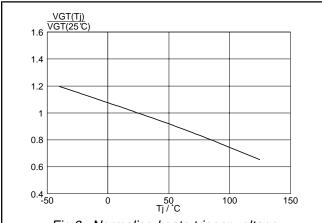
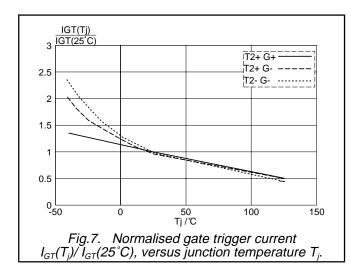
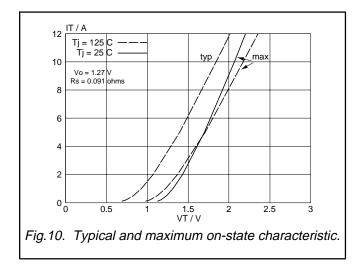
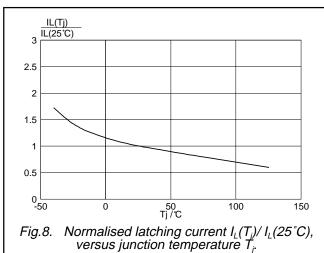


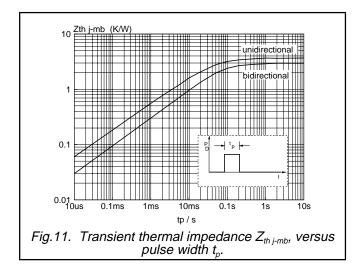
Fig.6. Normalised gate trigger voltage  $V_{GT}(T_j)/V_{GT}(25\,^{\circ}C)$ , versus junction temperature  $T_j$ .

# BTA204 series D, E and F









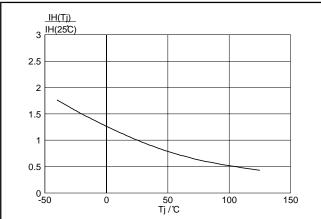
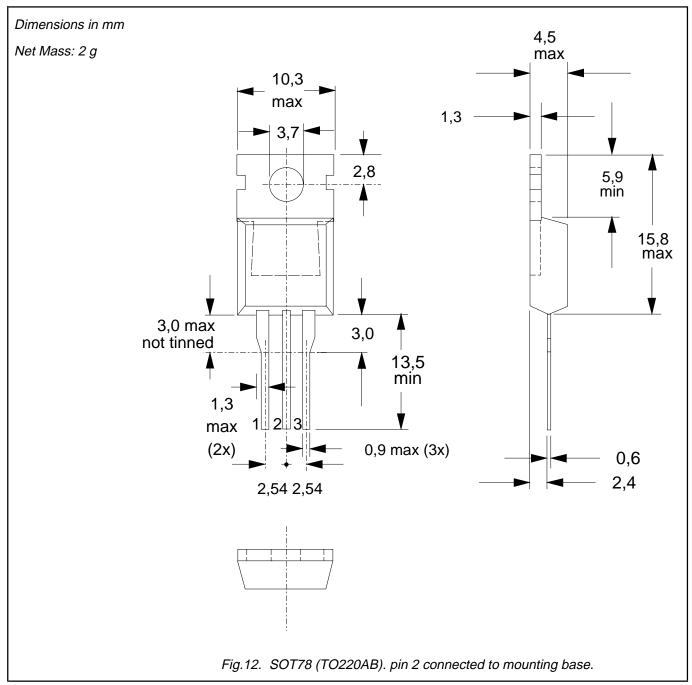


Fig.9. Normalised holding current  $I_H(T_i)/I_H(25^{\circ}C)$ , versus junction temperature  $T_j$ .

# BTA204 series D, E and F

# **MECHANICAL DATA**



- Refer to mounting instructions for SOT78 (TO220) envelopes.
   Epoxy meets UL94 V0 at 1/8".

BTA204 series D, E and F

## **DEFINITIONS**

Data sheet status						
Objective specification	This data sheet contains target or goal specifications for product development.					
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.					
Product specification	This data sheet contains final product specifications.					
Product specification	This data sheet contains final product specifications.					

#### Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### © Philips Electronics N.V. 1998

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, it is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent or other industrial or intellectual property rights.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.