

# Triacs

## logic level

# BT137F series D

## GENERAL DESCRIPTION

Glass passivated, sensitive gate triacs in a full pack plastic envelope, intended for use in general purpose bidirectional switching and phase control applications. These devices are intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

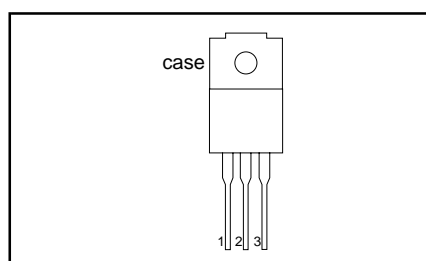
## QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	UNIT
$V_{\text{DRM}}$	<b>BT137F-</b> Repetitive peak off-state voltages	<b>500D</b> 500	<b>600D</b> 600	V
$I_{\text{T(RMS)}}$	RMS on-state current	8	8	A
$I_{\text{TSM}}$	Non-repetitive peak on-state current	55	55	A

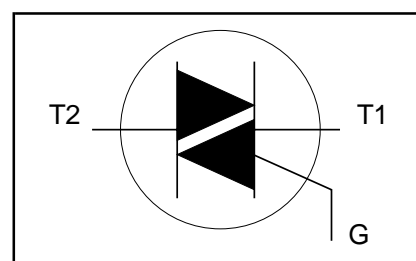
## PINNING - SOT186

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
case	isolated

## PIN CONFIGURATION



## SYMBOL



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
$V_{\text{DRM}}$	Repetitive peak off-state voltages		-	-500 500 <sup>1</sup>	-600 600 <sup>1</sup>	V
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_{\text{hs}} \leq 73^\circ\text{C}$	-	8		A
$I_{\text{TSM}}$	Non-repetitive peak on-state current	full sine wave; $T_{\text{j}} = 125^\circ\text{C}$ prior to surge; with reapplied $V_{\text{DRM(max)}}$	-	55		A
		$t = 20\text{ ms}$	-	60		A
		$t = 16.7\text{ ms}$	-	15		A <sup>2</sup> s
$I^2t$	$I^2t$ for fusing	$t = 10\text{ ms}$	-	15		A <sup>2</sup> s
$di_{\text{T}}/dt$	Repetitive rate of rise of on-state current after triggering	$I_{\text{TM}} = 12\text{ A}$ ; $I_{\text{G}} = 0.2\text{ A}$ ; $di_{\text{G}}/dt = 0.2\text{ A}/\mu\text{s}$	-	50		A/ $\mu\text{s}$
		T2+ G+	-	50		A/ $\mu\text{s}$
		T2+ G-	-	50		A/ $\mu\text{s}$
		T2- G-	-	50		A/ $\mu\text{s}$
		T2- G+	-	10		A/ $\mu\text{s}$
$I_{\text{GM}}$	Peak gate current		-	2		A
$V_{\text{GM}}$	Peak gate voltage		-	5		V
$P_{\text{GM}}$	Peak gate power		-	5		W
$P_{\text{G(AV)}}$	Average gate power	over any 20 ms period	-	0.5		W
$T_{\text{stg}}$	Storage temperature		-40	150		$^\circ\text{C}$
$T_{\text{j}}$	Operating junction temperature		-	125		$^\circ\text{C}$

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/ $\mu\text{s}$ .

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# BT137F series D

## ISOLATION LIMITING VALUE & CHARACTERISTIC

$T_{hs} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{isol}$	Repetitive peak voltage from all three terminals to external heatsink	R.H. $\leq 65\%$ ; clean and dustfree	-		1500	V
$C_{isol}$	Capacitance from T2 to external heatsink	$f = 1\text{ MHz}$	-	12	-	pF

## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-hs}$	Thermal resistance junction to heatsink	full or half cycle with heatsink compound	-	-	4.5	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	without heatsink compound in free air	-	55	6.5	K/W

## STATIC CHARACTERISTICS

$T_j = 25\text{ }^{\circ}\text{C}$  unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$I_{GT}$	Gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$				
		T2+ G+	-	2.5	5	mA
		T2+ G-	-	3.5	5	mA
		T2- G-	-	3.5	5	mA
		T2- G+	-	6.5	10	mA
$I_L$	Latching current	$V_D = 12\text{ V}$ ; $I_{GT} = 0.1\text{ A}$				
		T2+ G+	-	1.6	15	mA
		T2+ G-	-	8.5	20	mA
		T2- G-	-	1.2	15	mA
		T2- G+	-	2.5	20	mA
$I_H$	Holding current	$V_D = 12\text{ V}$ ; $I_{GT} = 0.1\text{ A}$	-	1.5	10	mA
$V_T$	On-state voltage	$I_T = 10\text{ A}$	-	1.3	1.65	V
$V_{GT}$	Gate trigger voltage	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$	-	0.7	1.5	V
		$V_D = 400\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 125\text{ }^{\circ}\text{C}$	0.25	0.4	-	V
$I_D$	Off-state leakage current	$V_D = V_{DRM(max)}$ ; $T_j = 125\text{ }^{\circ}\text{C}$	-	0.1	0.5	mA

## DYNAMIC CHARACTERISTICS

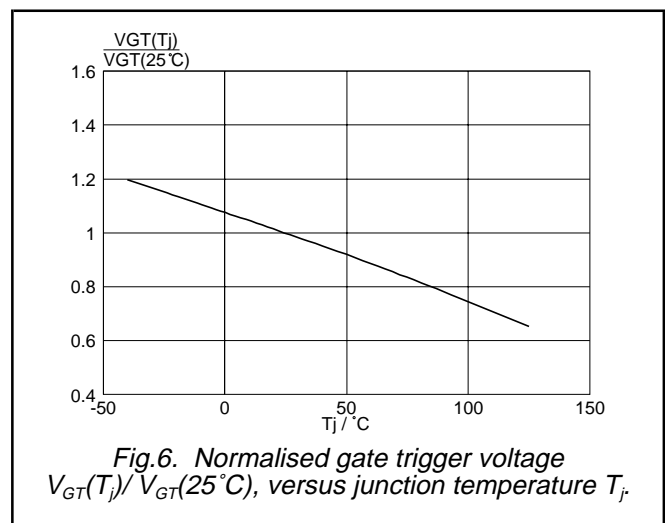
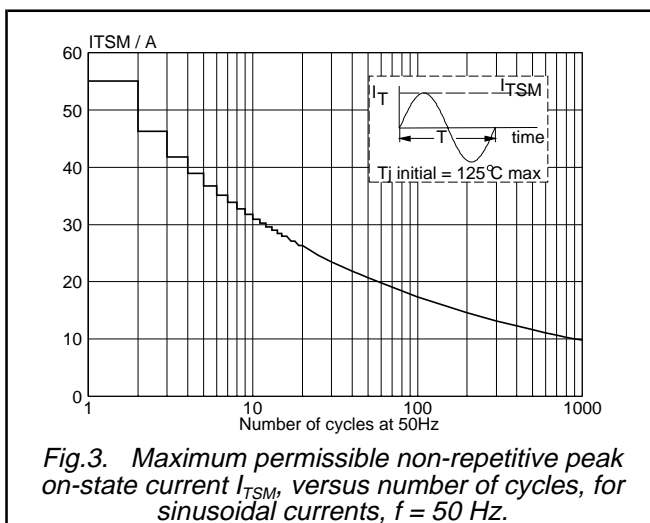
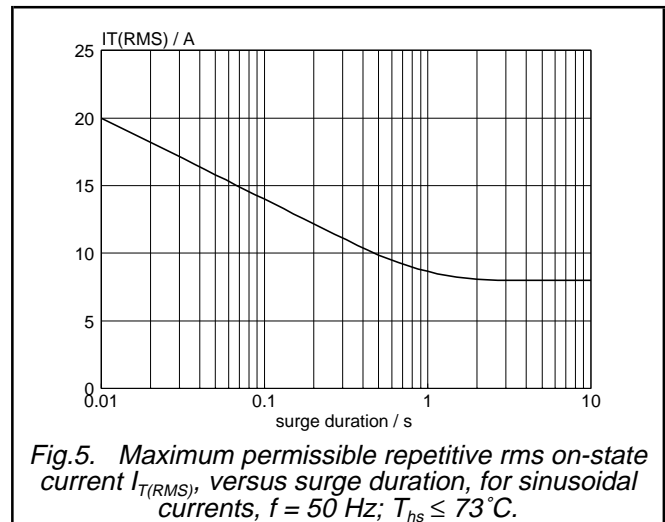
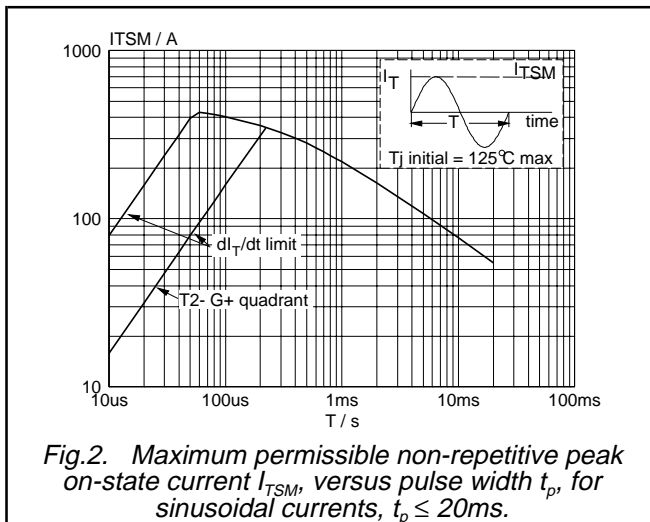
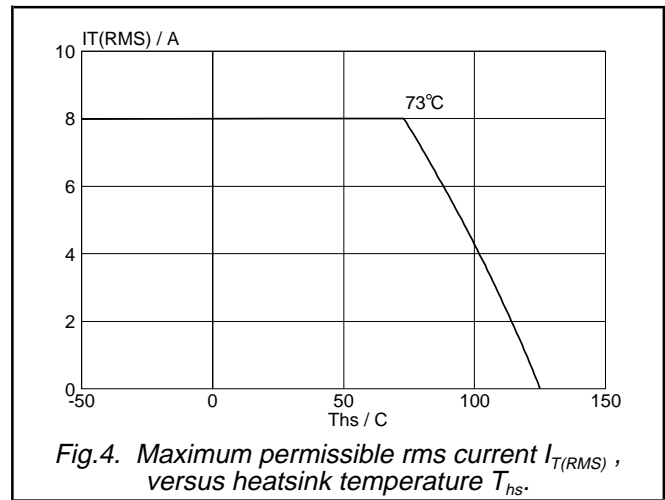
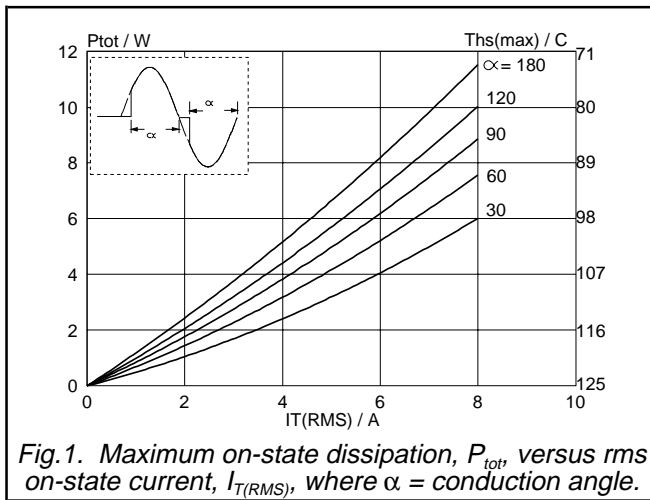
$T_j = 25\text{ }^{\circ}\text{C}$  unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$dV_D/dt$	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}$ ; $T_j = 125\text{ }^{\circ}\text{C}$ ; exponential waveform; $R_{GK} = 1\text{ k}\Omega$	-	5	-	V/ $\mu\text{s}$
$t_{gt}$	Gate controlled turn-on time	$I_{TM} = 12\text{ A}$ ; $V_D = V_{DRM(max)}$ ; $I_G = 0.1\text{ A}$ ; $dI_G/dt = 5\text{ A}/\mu\text{s}$	-	2	-	$\mu\text{s}$

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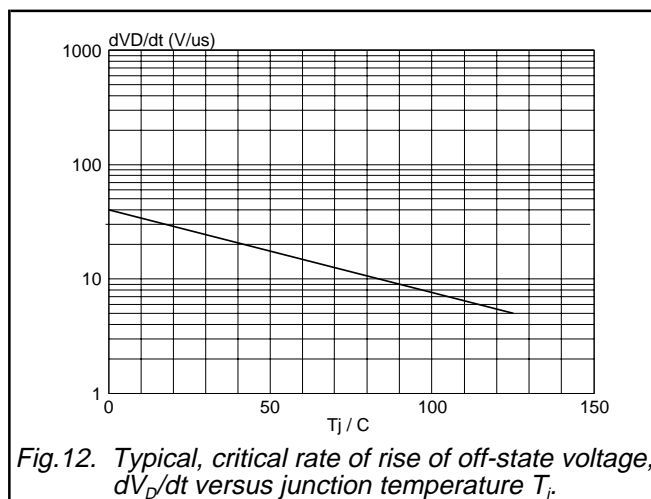
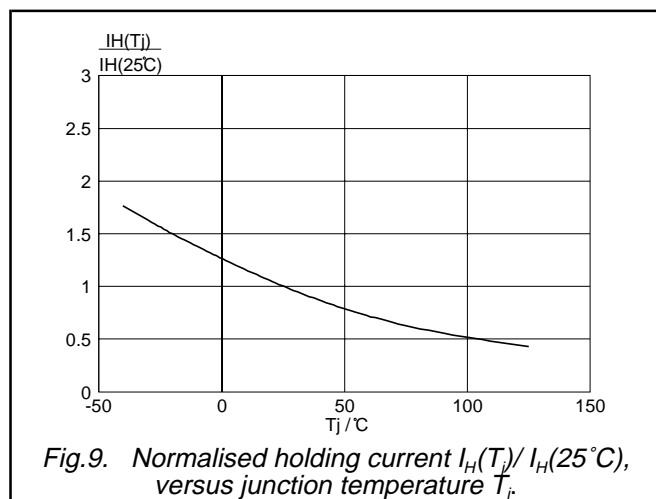
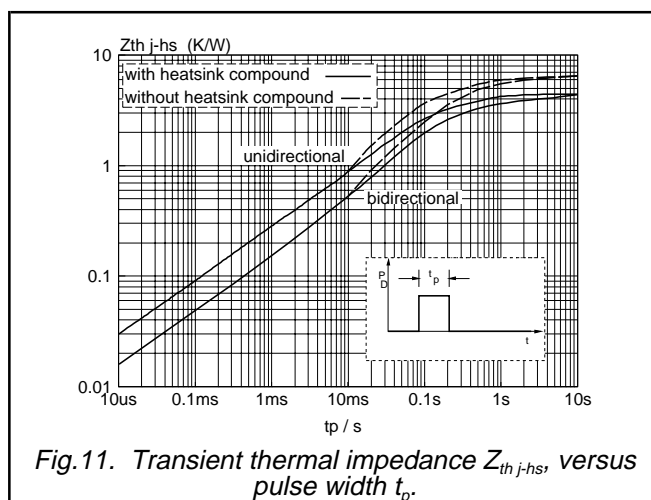
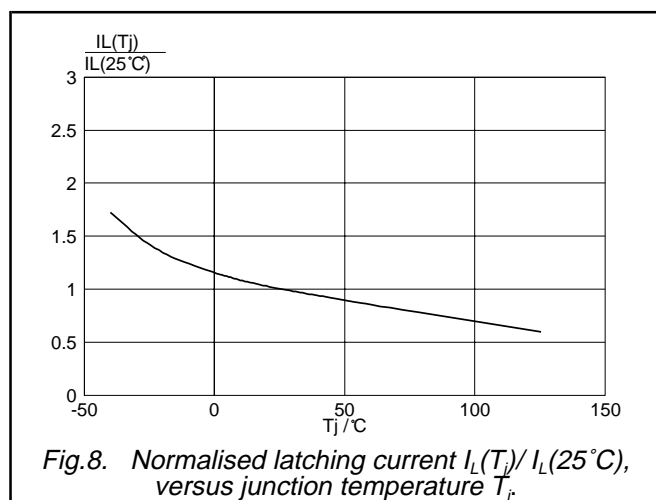
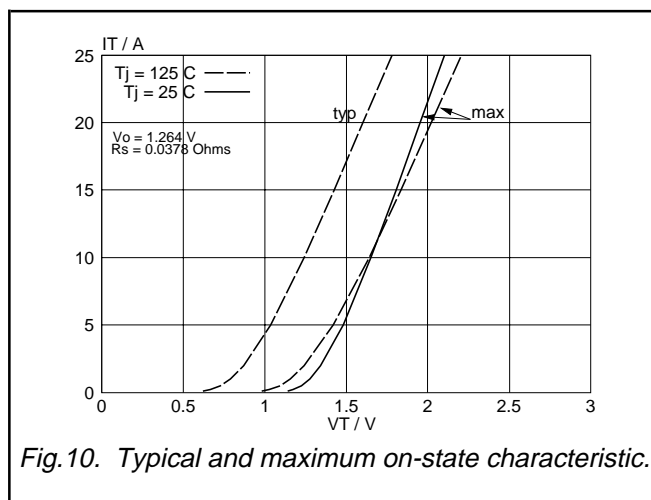
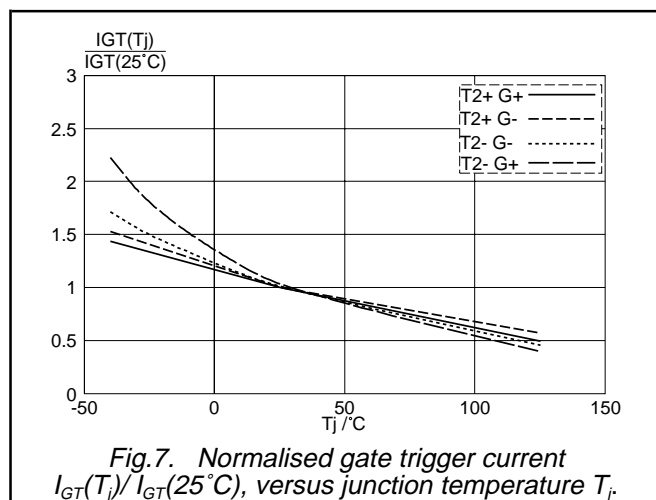
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## MECHANICAL DATA

Dimensions in mm

Net Mass: 2 g

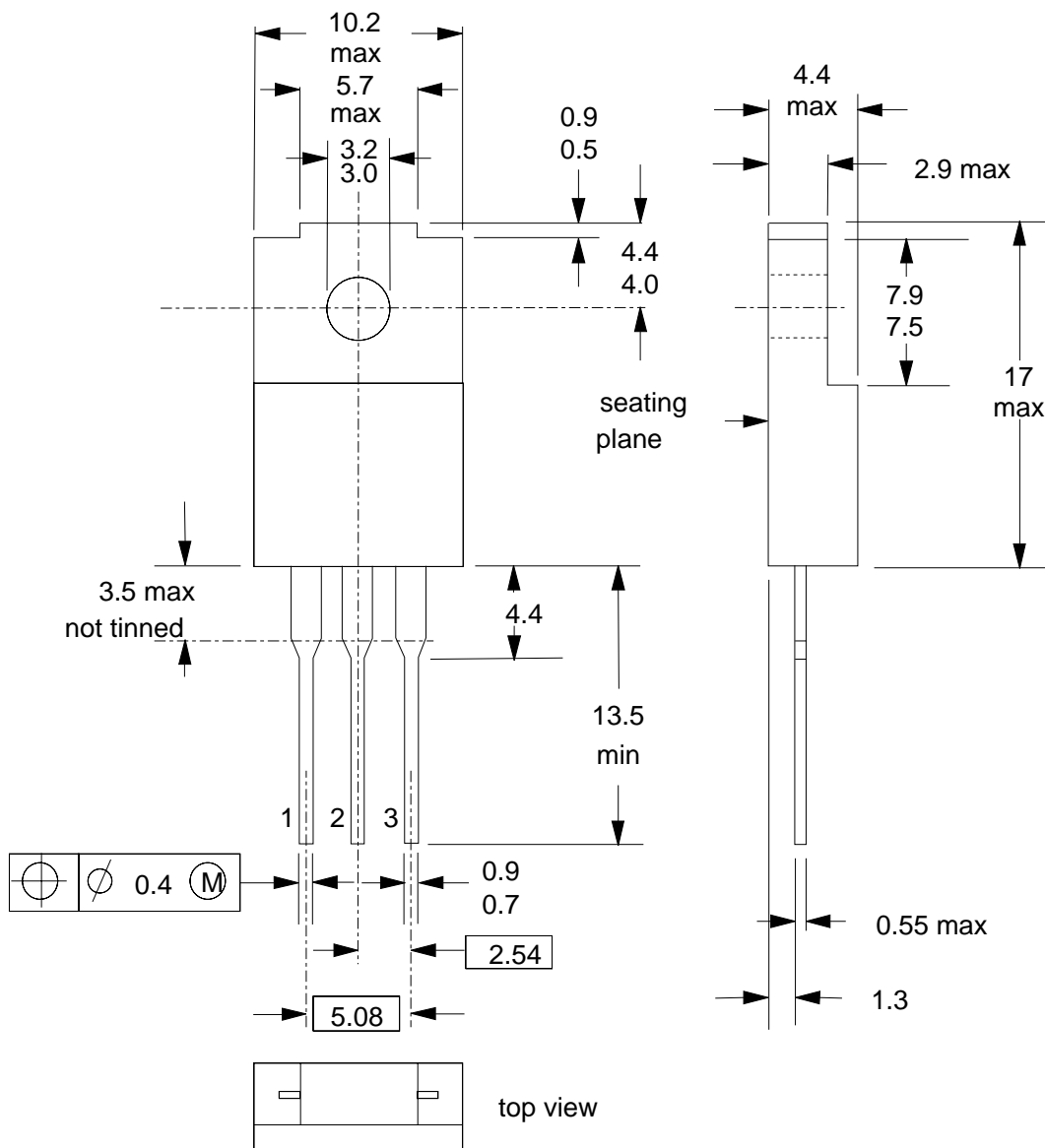


Fig.13. SOT186; The seating plane is electrically isolated from all terminals.

### Notes

- Accessories supplied on request: refer to mounting instructions for F-pack envelopes.
- Epoxy meets UL94 V0 at 1/8".

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## DEFINITIONS

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	
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