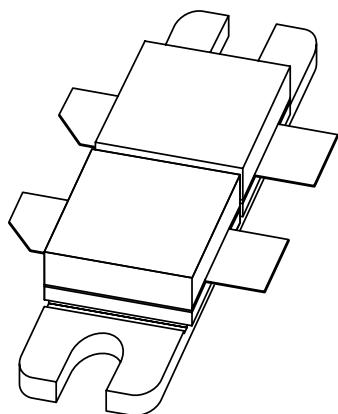


DATA SHEET



BLF378

VHF push-pull power MOS transistor

Product specification
Supersedes data of 1996 Oct 17

1998 Jul 29

VHF push-pull power MOS transistor**BLF378****FEATURES**

- High power gain
- Easy power control
- Good thermal stability
- Gold metallization ensures excellent reliability.

APPLICATIONS

- Broadcast transmitter applications in the VHF frequency range.

DESCRIPTION

Dual push-pull silicon N-channel enhancement mode vertical D-MOS transistor encapsulated in a 4-lead, SOT262A1 balanced flange package with two ceramic caps. The mounting flange provides the common source connection for the transistors.

CAUTION

This product is supplied in anti-static packing to prevent damage caused by electrostatic discharge during transport and handling. For further information, refer to Philips specs.: SNW-EQ-608, SNW-FQ-302A, and SNW-FQ-302B.

PINNING - SOT262A1

PIN	SYMBOL	DESCRIPTION
1	d_1	drain 1
2	d_2	drain 2
3	g_1	gate 1
4	g_2	gate 2
5	s	source

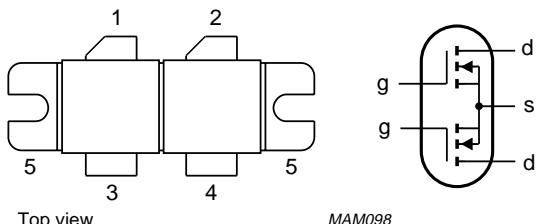


Fig.1 Simplified outline and symbol.

QUICK REFERENCE DATA

RF performance at $T_h = 25^\circ\text{C}$ in a push-pull common source test circuit.

MODE OF OPERATION	f (MHz)	V_{DS} (V)	P_L (W)	G_p (dB)	ΔG_p (dB) ⁽¹⁾	η_D (%)
CW, class-AB	225	50	250	>14 typ. 16	<1 typ. 0.6	>50 typ. 55

Note

1. Assuming a 3rd order amplitude transfer characteristic, 1 dB gain compression corresponds with 30% synchronized input / 25% synchronized output compression in television service (negative modulation, CCIR system).

WARNING**Product and environmental safety - toxic materials**

This product contains beryllium oxide. The product is entirely safe provided that the BeO discs are not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

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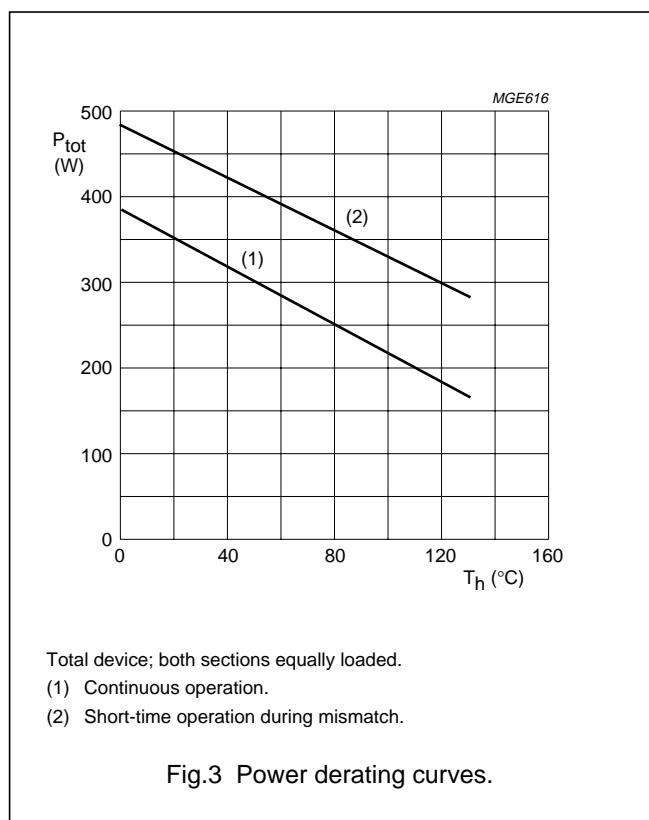
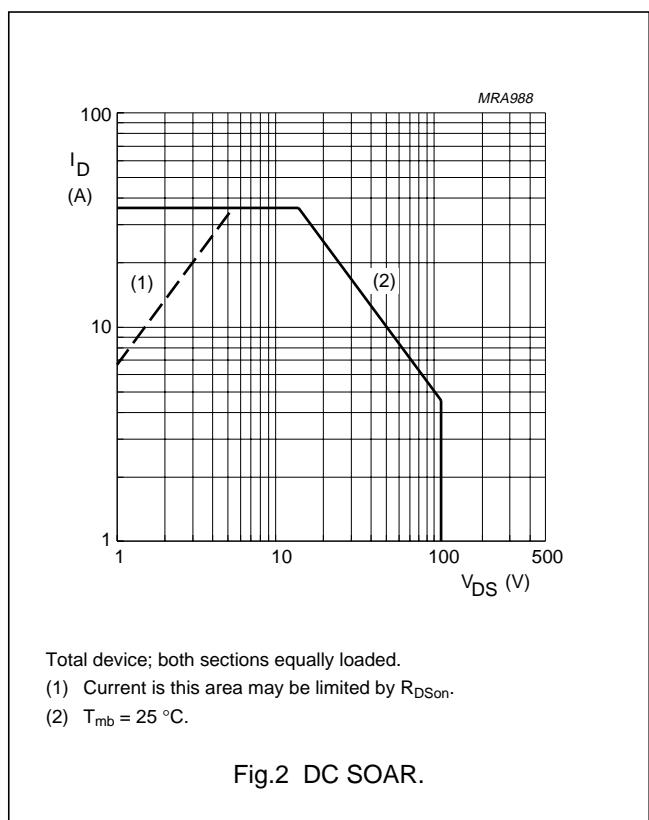
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transistor section unless otherwise specified					
V_{DSS}	drain-source voltage		–	110	V
V_{GSS}	gate-source voltage		–	± 20	V
I_D	drain current (DC)		–	18	A
P_{tot}	total power dissipation	$T_{mb} \leq 25^\circ\text{C}$ total device; both sections equally loaded	–	500	W
T_{stg}	storage temperature		–65	150	$^\circ\text{C}$
T_j	junction temperature		–	200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-mb}$	thermal resistance from junction to mounting base	total device; both sections equally loaded	0.35	K/W
$R_{th\ mb-h}$	thermal resistance from mounting base to heatsink	total device; both sections equally loaded	0.15	K/W

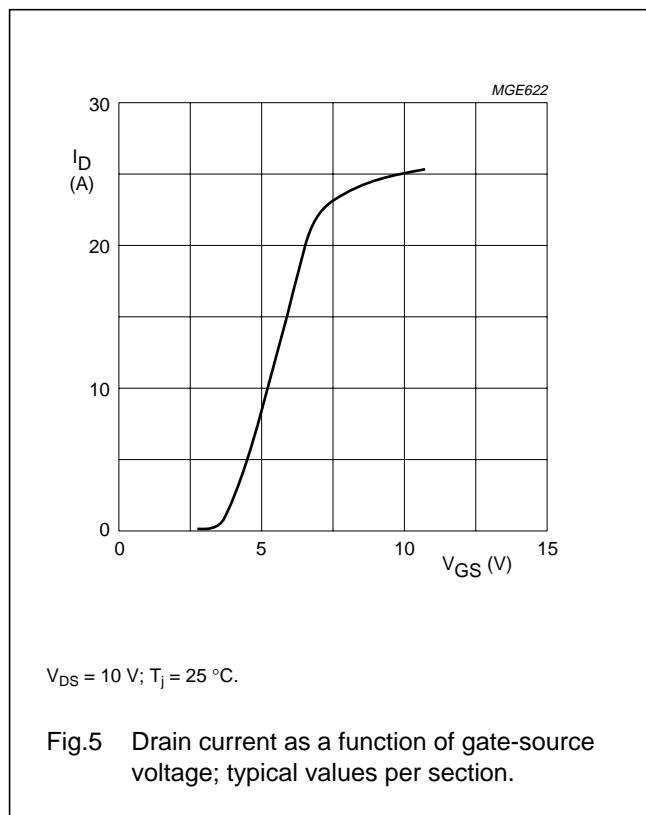
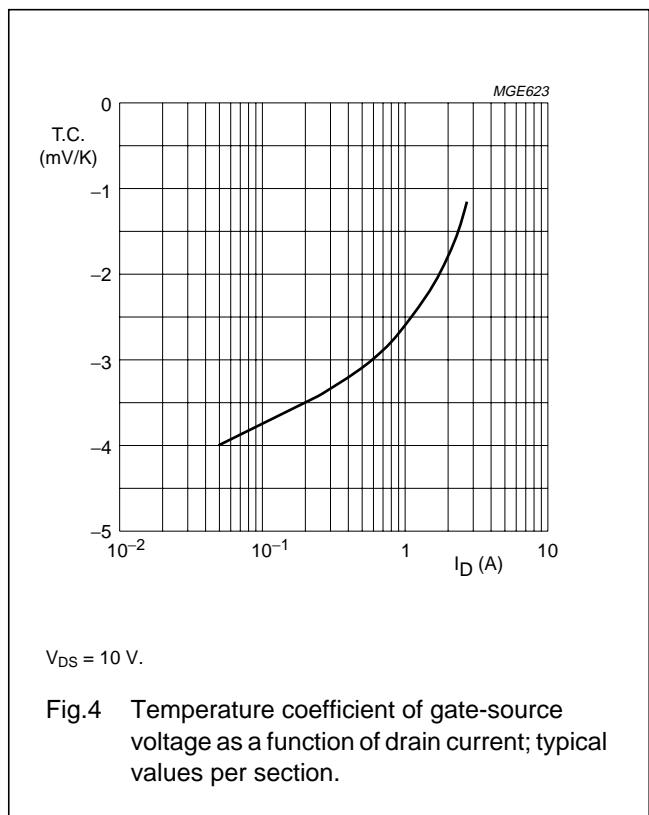


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CHARACTERISTICS $T_j = 25^\circ\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per transistor section						
$V_{(\text{BR})\text{DSS}}$	drain-source breakdown voltage	$V_{\text{GS}} = 0$; $I_D = 50 \text{ mA}$	110	—	—	V
I_{DSS}	drain-source leakage current	$V_{\text{GS}} = 0$; $V_{\text{DS}} = 50 \text{ V}$	—	—	2.5	mA
I_{GSS}	gate-source leakage current	$V_{\text{GS}} = \pm 20 \text{ V}$; $V_{\text{DS}} = 0$	—	—	1	μA
$V_{\text{GS}\text{th}}$	gate-source threshold voltage	$I_D = 50 \text{ mA}$; $V_{\text{DS}} = 10 \text{ V}$	2.0	—	4.5	V
ΔV_{GS}	gate-source voltage difference of both transistor sections	$I_D = 50 \text{ mA}$; $V_{\text{DS}} = 10 \text{ V}$	—	—	100	mV
g_{fs}	forward transconductance	$I_D = 5 \text{ A}$; $V_{\text{DS}} = 10 \text{ V}$	4.5	6.2	—	S
$g_{\text{fs}1}/g_{\text{fs}2}$	forward transconductance ratio of both transistor sections	$I_D = 5 \text{ A}$; $V_{\text{DS}} = 10 \text{ V}$	0.9	—	1.1	
$R_{\text{DS}\text{on}}$	drain-source on-state resistance	$I_D = 5 \text{ A}$; $V_{\text{GS}} = 10 \text{ V}$	—	0.2	0.3	Ω
I_{DSX}	on-state drain current	$V_{\text{GS}} = 10 \text{ V}$; $V_{\text{DS}} = 10 \text{ V}$	—	25	—	A
C_{is}	input capacitance	$V_{\text{GS}} = 0$; $V_{\text{DS}} = 50 \text{ V}$; $f = 1 \text{ MHz}$	—	480	—	pF
C_{os}	output capacitance	$V_{\text{GS}} = 0$; $V_{\text{DS}} = 50 \text{ V}$; $f = 1 \text{ MHz}$	—	190	—	pF
C_{rs}	feedback capacitance	$V_{\text{GS}} = 0$; $V_{\text{DS}} = 50 \text{ V}$; $f = 1 \text{ MHz}$	—	14	—	pF
$C_{\text{d-f}}$	drain-flange capacitance		—	5.4	—	pF



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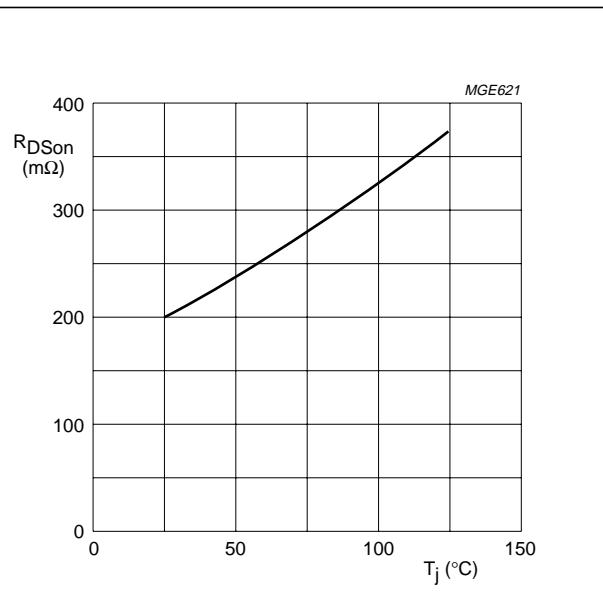
 $I_D = 5 \text{ A}; V_{GS} = 10 \text{ V}.$

Fig.6 Drain-source on-state resistance as a function of junction temperature; typical values per section.

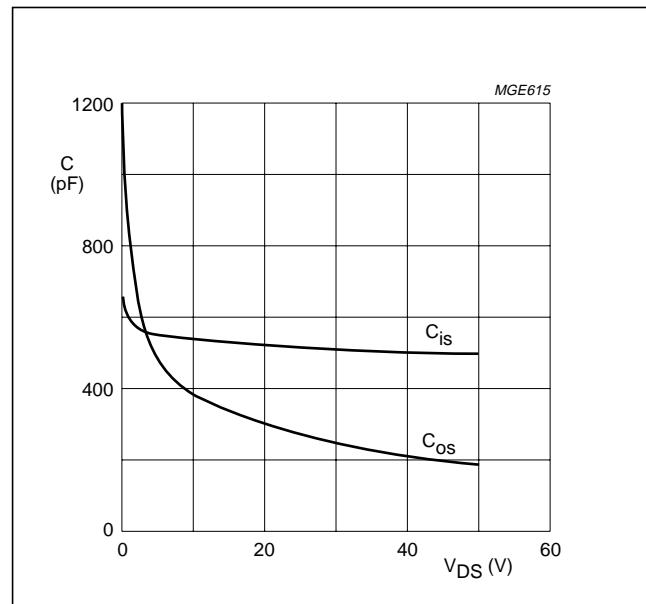
 $V_{GS} = 0; f = 1 \text{ MHz}.$

Fig.7 Input and output capacitance as functions of drain-source voltage; typical values per section.

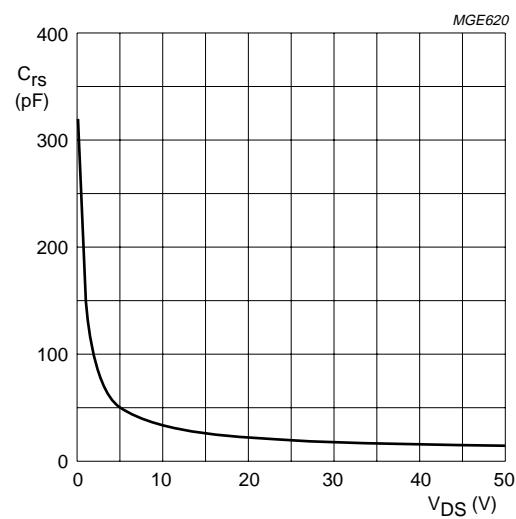
 $V_{GS} = 0; f = 1 \text{ MHz}.$

Fig.8 Feedback capacitance as a function of drain-source voltage; typical values per section.

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APPLICATION INFORMATION**Class-AB operation**

RF performance in CW operation in a common source class-AB circuit. $T_h = 25^\circ\text{C}$; $R_{th\text{ mb-h}} = 0.15 \text{ K/W}$ unless otherwise specified. $R_{GS} = 2.8 \Omega$ per section; optimum load impedance per section = $0.74 + j2 \Omega$ ($V_{DS} = 50 \text{ V}$).

MODE OF OPERATION	f (MHz)	V_{DS} (V)	I_{DQ} (A)	P_L (W)	G_p (dB)	ΔG_p (dB) ⁽¹⁾	η_D (%)
CW, class-AB	225	50	2×0.5	250	>14 typ. 16	<1 typ. 0.6	>50 typ. 55
	225	45	2×0.5	250	typ. 15	typ. 1	typ. 60

Note

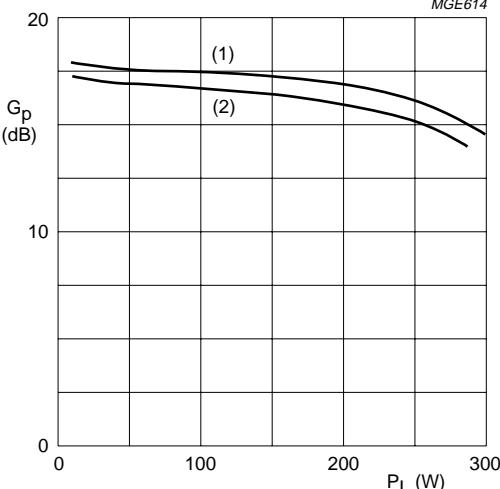
- Assuming a 3rd order amplitude transfer characteristic, 1 dB gain compression corresponds with 30% synchronized input / 25% synchronized output compression in television service (negative modulation, CCIR system).

Ruggedness in class-AB operation

The BLF378 is capable of withstanding a load mismatch corresponding to $VSWR = 7 : 1$ through all phases under the conditions: $V_{DS} = 50 \text{ V}$; $f = 225 \text{ MHz}$ at rated output power.

VHF push-pull power MOS transistor

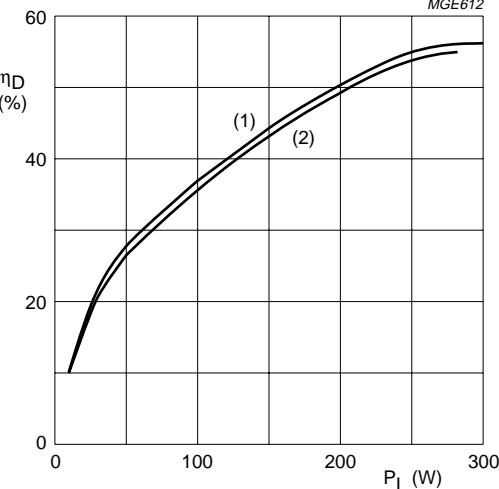
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Class-AB operation; $V_{DS} = 50$ V; $I_{DQ} = 2 \times 0.5$ A; $f = 225$ MHz;
 $Z_L = 0.74 + j2$ Ω (per section); $R_{GS} = 2.8$ Ω (per section).

- (1) $T_h = 25$ °C.
- (2) $T_h = 70$ °C.

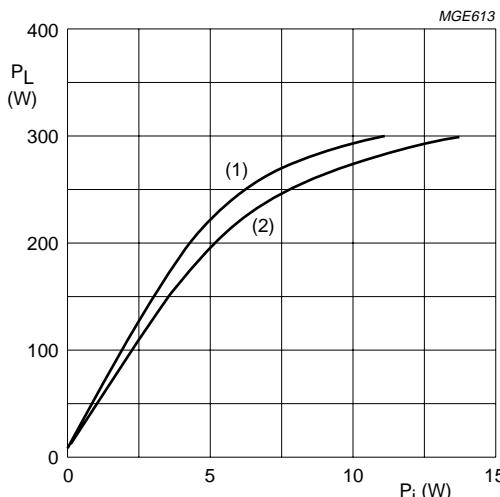
Fig.9 Power gain as a function of load power;
typical values per section.



Class-AB operation; $V_{DS} = 50$ V; $I_{DQ} = 2 \times 0.5$ A; $f = 225$ MHz;
 $Z_L = 0.74 + j2$ Ω (per section); $R_{GS} = 2.8$ Ω (per section).

- (1) $T_h = 25$ °C.
- (2) $T_h = 70$ °C.

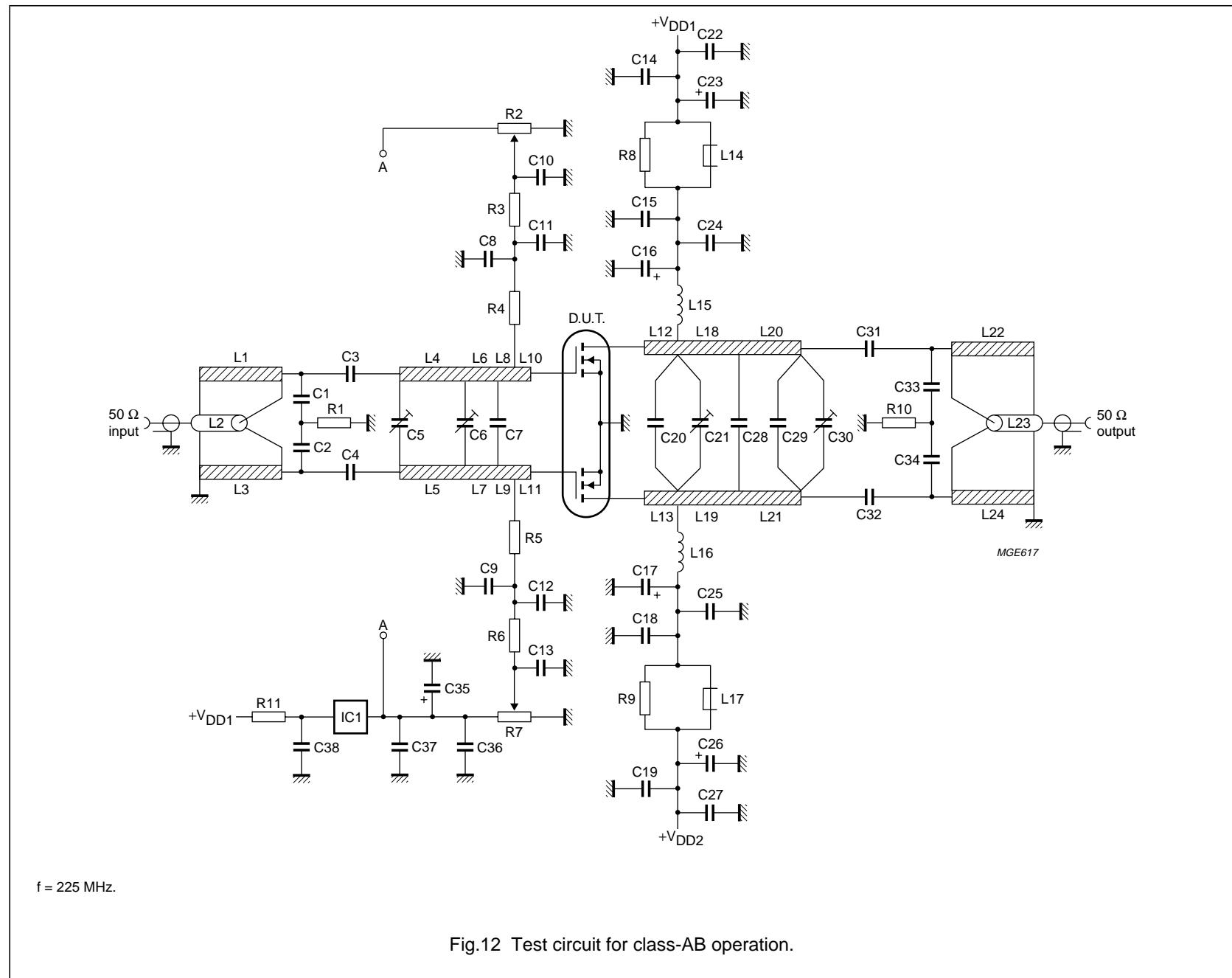
Fig.10 Efficiency as a function of load power;
typical values per section.



Class-AB operation; $V_{DS} = 50$ V; $I_{DQ} = 2 \times 0.5$ A; $f = 225$ MHz;
 $Z_L = 0.74 + j2$ Ω (per section); $R_{GS} = 2.8$ Ω (per section).

- (1) $T_h = 25$ °C.
- (2) $T_h = 70$ °C.

Fig.11 Load power as a function of input power;
typical values per section.



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List of components class-AB test circuit (see Figs 12 and 13).

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE No.
C1, C2	multilayer ceramic chip capacitor; note 1	27 pF, 500 V		
C3, C4, C31, C32	multilayer ceramic chip capacitor; note 1	3 × 18 pF in parallel, 500 V		
C5	film dielectric trimmer	4 to 40 pF		2222 809 08002
C6, C30	film dielectric trimmer	2 to 18 pF		2222 809 09006
C7	multilayer ceramic chip capacitor; note 1	100 pF, 500 V		
C8, C9, C15, C18	MKT film capacitor	1 µF, 63 V		2222 371 11105
C10, C13, C14, C19, C36	multilayer ceramic chip capacitor	100 nF, 50 V		2222 852 47104
C11, C12	multilayer ceramic chip capacitor; note 1	2 × 1 nF in parallel, 500 V		
C16, C17	electrolytic capacitor	220 µF, 63 V		
C20	multilayer ceramic chip capacitor; note 1	3 × 33 pF in parallel, 500 V		
C21	film dielectric trimmer	2 to 9 pF		2222 809 09005
C22, C27, C37, C38	multilayer ceramic chip capacitor; note 1	1 nF, 500 V		
C23, C26, C35	electrolytic capacitor	10 µF, 63 V		
C24, C25	multilayer ceramic chip capacitor; note 1	2 × 470 pF in parallel, 500 V		
C28	multilayer ceramic chip capacitor; note 1	2 × 10 pF in parallel + 18 pF, 500 V		
C29	multilayer ceramic chip capacitor; note 1	2 × 5.6 pF in parallel, 500 V		
C33, C34	multilayer ceramic chip capacitor; note 1	5.6 pF, 500 V		
L1, L3, L22, L24	stripline; note 2	50 Ω	4.8 × 80 mm	
L2, L23	semi-rigid cable; note 3	50 Ω	ext. conductor length 80 mm ext. dia 3.6 mm	
L4, L5	stripline; note 2	43 Ω	6 × 24 mm	
L6, L7	stripline; note 2	43 Ω	6 × 14.5 mm	
L8, L9	stripline; note 2	43 Ω	6 × 4.4 mm	
L10, L11	stripline; note 2	43 Ω	6 × 3.2 mm	
L12, L13	stripline; note 2	43 Ω	6 × 15 mm	
L14, L17	grade 3B Ferroxcube wideband HF choke	2 in parallel		4312 020 36642
L15, L16	1¾ turns enamelled 2 mm copper wire	40 nH	space 1 mm int. dia. 10 mm leads 2 × 7 mm	

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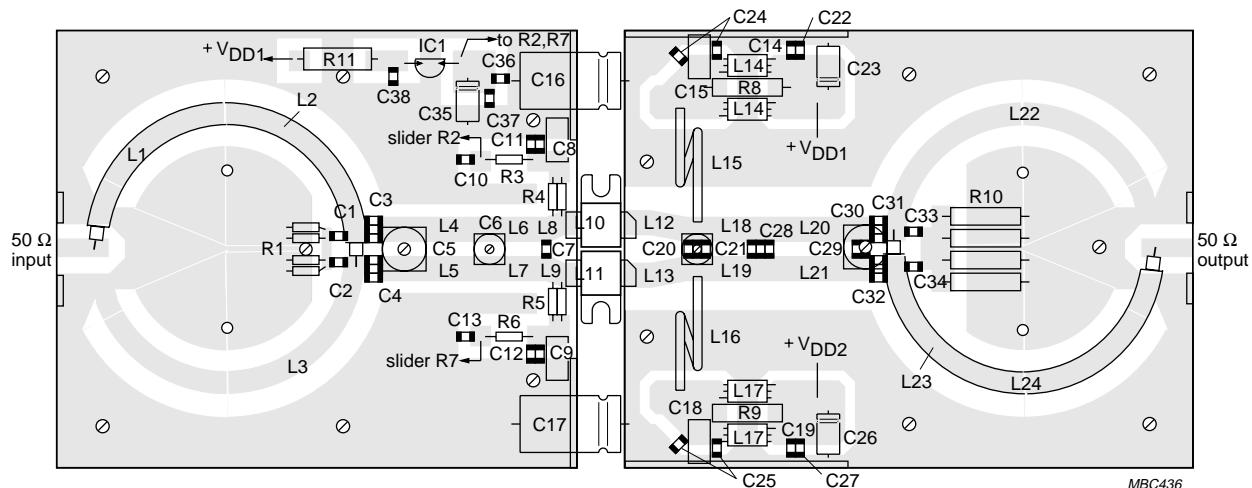
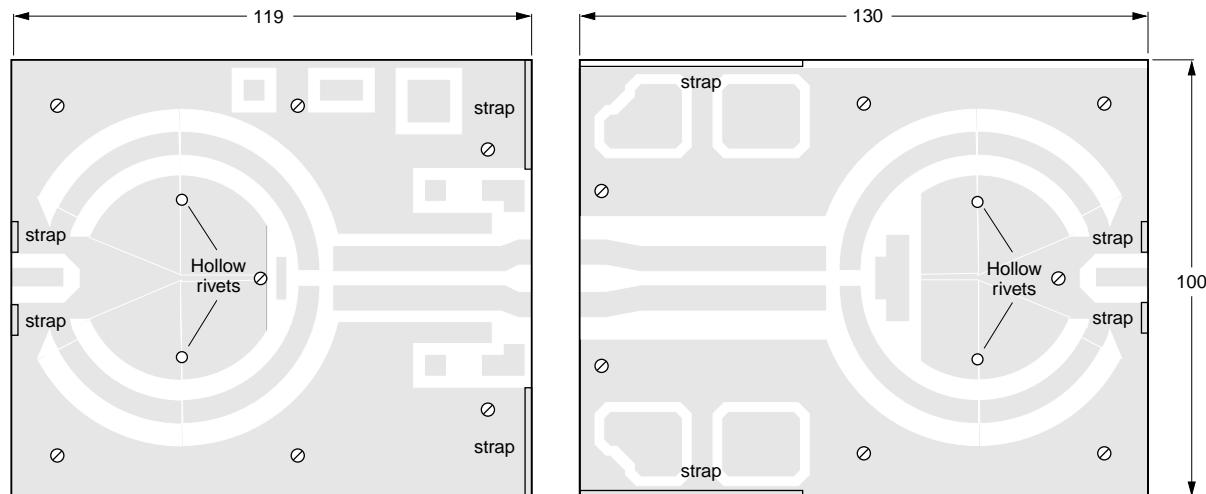
COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE No.
L18, L19	stripline; note 2	43 Ω	6 × 13 mm	
L20, L21	stripline; note 2	43 Ω	6 × 29.5 mm	
R1	metal film resistor	4 × 0.4 W, 10 Ω		
R2, R7	10 turns potentiometer	50 kΩ		
R3, R6	metal film resistor	0.4 W, 1 kΩ		
R4, R5	metal film resistor	2 × 0.4 W, 5.62 Ω in parallel		
R8, R9	metal film resistor	1 W, 10 Ω, ±5%		
R10	metal film resistor	4 × 1 W, 10 Ω in parallel		
R11	metal film resistor	1 W, 5.11 kΩ		
IC1	voltage regulator 78L05			

Notes

1. American Technical Ceramics (ATC) capacitor, type 100B or other capacitor of the same quality.
2. The striplines L1, L3 to L13, L18 to L22 and L24 are on a double copper-clad printed-circuit board with glass microfibre PTFE dielectric ($\epsilon_r = 2.2$); thickness $1/16$ inch; thickness of copper sheet $2 \times 35 \mu\text{m}$.
3. Semi-rigid cables L2 and L23 are soldered on to striplines L1 and L24.

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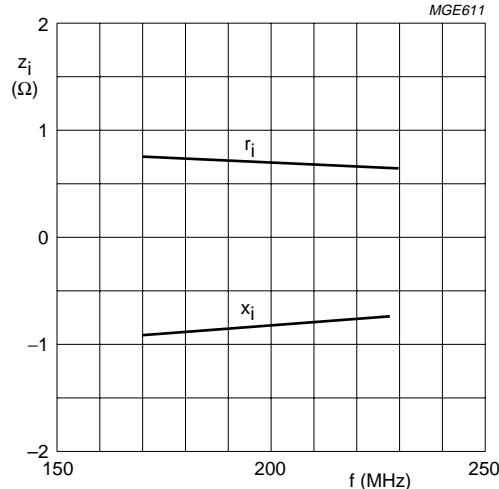
Dimensions in mm.

The circuit and components are situated on one side of the PTFE fibre-glass board, the other side being fully metallized to serve as an earth. Earth connections are made by means of copper straps for a direct contact between upper and lower sheets.

Fig.13 Printed-circuit board and component layout for 225 MHz class-AB test circuit.

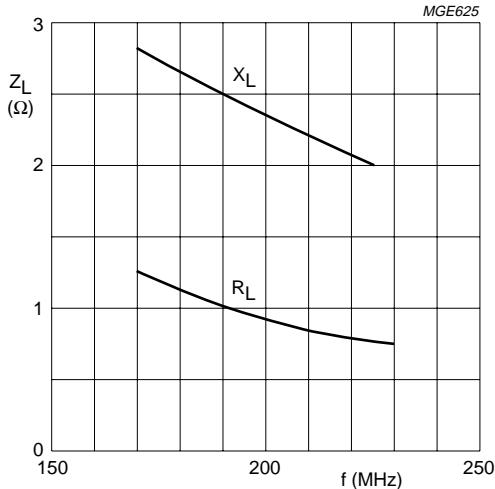
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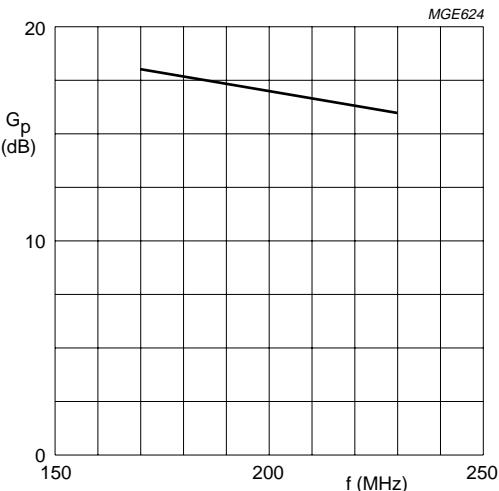
Class-AB operation; $V_{DS} = 50$ V; $I_{DQ} = 2 \times 0.5$ A;
 $R_{GS} = 2.8 \Omega$ (per section); $P_L = 250$ W.

Fig.14 Input impedance as a function of frequency (series components); typical values per section.



Class-AB operation; $V_{DS} = 50$ V; $I_{DQ} = 2 \times 0.5$ A;
 $R_{GS} = 2.8 \Omega$ (per section); $P_L = 250$ W.

Fig.15 Load impedance as a function of frequency (series components); typical values per section.



Class-AB operation; $V_{DS} = 50$ V; $I_{DQ} = 2 \times 0.5$ A;
 $R_{GS} = 2.8 \Omega$ (per section); $P_L = 250$ W.

Fig.16 Power gain as a function of frequency; typical values per section.

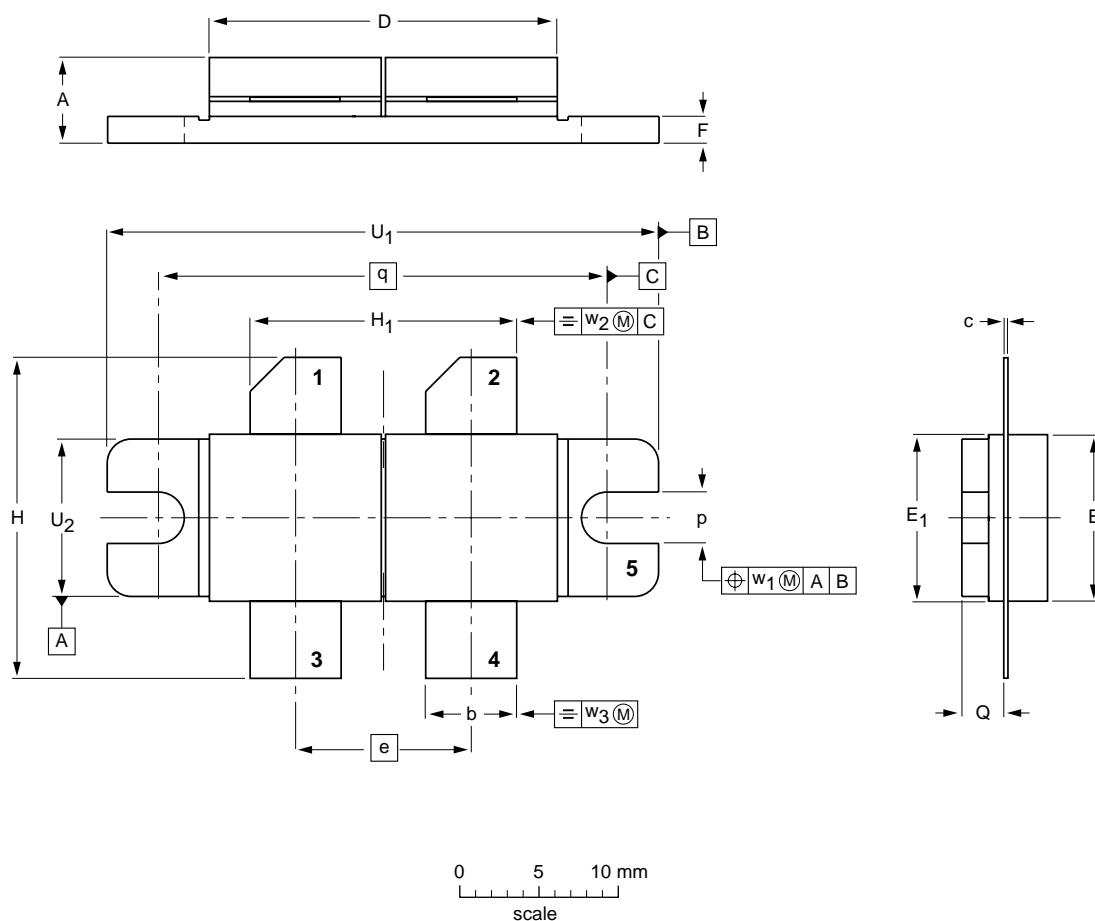
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PACKAGE OUTLINE

Flanged double-ended ceramic package; 2 mounting holes; 4 leads

SOT262A1



DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A	b	c	D	e	E	E ₁	F	H	H ₁	p	Q	q	U ₁	U ₂	w ₁	w ₂	w ₃
mm	5.77 5.00	5.85 5.58	0.16 0.10	21.98 21.71	11.05	10.27 10.05	10.29 10.03	1.78 1.52	20.58 20.06	17.02 16.51	3.28 3.02	2.85 2.59	27.94	34.17 33.90	9.91 9.65	0.51	1.02	0.25
inches	0.227 0.197	0.230 0.220	0.006 0.004	0.865 0.855	0.435	0.404 0.396	0.405 0.395	0.070 0.060	0.81 0.79	0.67 0.65	0.129 0.119	0.112 0.102	1.100	1.345 1.335	0.390 0.380	0.02	0.04	0.01

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT262A1						97-06-28

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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NOTES

Philips Semiconductors – a worldwide company

Argentina: see South America

Australia: 34 Waterloo Road, NORTH RYDE, NSW 2113,
Tel. +61 2 9805 4455, Fax. +61 2 9805 4466

Austria: Computerstr. 6, A-1101 WIEN, P.O. Box 213, Tel. +43 1 60 1010,
Fax. +43 1 60 101 1210

Belarus: Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,
220050 MINSK, Tel. +375 172 200 733, Fax. +375 172 200 773

Belgium: see The Netherlands

Brazil: see South America

Bulgaria: Philips Bulgaria Ltd., Energoproject, 15th floor,
51 James Bourchier Blvd., 1407 SOFIA,
Tel. +359 2 689 211, Fax. +359 2 689 102

Canada: PHILIPS SEMICONDUCTORS/COMPONENTS,
Tel. +1 800 234 7381

China/Hong Kong: 501 Hong Kong Industrial Technology Centre,
72 Tat Chee Avenue, Kowloon Tong, HONG KONG,
Tel. +852 2319 7888, Fax. +852 2319 7700

Colombia: see South America

Czech Republic: see Austria

Denmark: Prags Boulevard 80, PB 1919, DK-2300 COPENHAGEN S,
Tel. +45 32 88 2636, Fax. +45 31 57 0044

Finland: Sinikalliontie 3, FIN-02630 ESPOO,
Tel. +358 9 615800, Fax. +358 9 61580920

France: 51 Rue Carnot, BP317, 92156 SURESNES Cedex,
Tel. +33 1 40 99 6161, Fax. +33 1 40 99 6427

Germany: Hammerbrookstraße 69, D-20097 HAMBURG,
Tel. +49 40 23 53 60, Fax. +49 40 23 536 300

Greece: No. 15, 25th March Street, GR 17778 TAVROS/ATHENS,
Tel. +30 1 4894 339/239, Fax. +30 1 4814 240

Hungary: see Austria

India: Philips INDIA Ltd, Band Box Building, 2nd floor,
254-D, Dr. Annie Besant Road, Worli, MUMBAI 400 025,
Tel. +91 22 493 8541, Fax. +91 22 493 0966

Indonesia: PT Philips Development Corporation, Semiconductors Division,
Gedung Philips, Jl. Buncit Raya Kav.99-100, JAKARTA 12510,
Tel. +62 21 794 0040 ext. 2501, Fax. +62 21 794 0080

Ireland: Newstead, Clonskeagh, DUBLIN 14,
Tel. +353 1 7640 000, Fax. +353 1 7640 200

Israel: RAPAC Electronics, 7 Kehilat Saloniki St, PO Box 18053,
TEL AVIV 61180, Tel. +972 3 645 0444, Fax. +972 3 649 1007

Italy: PHILIPS SEMICONDUCTORS, Piazza IV Novembre 3,
20124 MILANO, Tel. +39 2 6752 2531, Fax. +39 2 6752 2557

Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku,
TOKYO 108-8507, Tel. +81 3 3740 5130, Fax. +81 3 3740 5077

Korea: Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL,
Tel. +82 2 709 1412, Fax. +82 2 709 1415

Malaysia: No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,
Tel. +60 3 750 5214, Fax. +60 3 757 4880

Mexico: 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905,
Tel. +9-5 800 234 7381

Middle East: see Italy

Netherlands: Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,
Tel. +31 40 27 82785, Fax. +31 40 27 88399

New Zealand: 2 Wagener Place, C.P.O. Box 1041, AUCKLAND,
Tel. +64 9 849 4160, Fax. +64 9 849 7811

Norway: Box 1, Manglerud 0612, OSLO,
Tel. +47 22 74 8000, Fax. +47 22 74 8341

Pakistan: see Singapore

Philippines: Philips Semiconductors Philippines Inc.,
106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI,
Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

Poland: Ul. Lukiska 10, PL 04-123 WARSZAWA,
Tel. +48 22 612 2831, Fax. +48 22 612 2327

Portugal: see Spain

Romania: see Italy

Russia: Philips Russia, Ul. Usatcheva 35A, 119048 MOSCOW,
Tel. +7 095 755 6918, Fax. +7 095 755 6919

Singapore: Lorong 1, Toa Payoh, SINGAPORE 319762,
Tel. +65 350 2538, Fax. +65 251 6500

Slovakia: see Austria

Slovenia: see Italy

South Africa: S.A. PHILIPS Pty Ltd., 195-215 Main Road Martindale,
2092 JOHANNESBURG, P.O. Box 7430 Johannesburg 2000,
Tel. +27 11 470 5911, Fax. +27 11 470 5494

South America: Al. Vicente Pinzon, 173, 6th floor,
04547-130 SÃO PAULO, SP, Brazil,
Tel. +55 11 821 2333, Fax. +55 11 821 2382

Spain: Balmes 22, 08007 BARCELONA,
Tel. +34 93 301 6312, Fax. +34 93 301 4107

Sweden: Kottbygatan 7, Akalla, S-16485 STOCKHOLM,
Tel. +46 8 5985 2000, Fax. +46 8 5985 2745

Switzerland: Allmendstrasse 140, CH-8027 ZÜRICH,
Tel. +41 1 488 2741 Fax. +41 1 488 3263

Taiwan: Philips Semiconductors, 6F, No. 96, Chien Kuo N. Rd., Sec. 1,
TAIPEI, Taiwan Tel. +886 2 2134 2865, Fax. +886 2 2134 2874

Thailand: PHILIPS ELECTRONICS (THAILAND) Ltd.,
209/2 Sanpavuth-Bangna Road Prakanong, BANGKOK 10260,
Tel. +66 2 745 4090, Fax. +66 2 398 0793

Turkey: Talatpasa Cad. No. 5, 80640 GÜLTEPE/İSTANBUL,
Tel. +90 212 279 2770, Fax. +90 212 282 6707

Ukraine: PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,
252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

United Kingdom: Philips Semiconductors Ltd., 276 Bath Road, Hayes,
MIDDLESEX UB3 5BX, Tel. +44 181 730 5000, Fax. +44 181 754 8421

United States: 811 East Arques Avenue, SUNNYVALE, CA 94088-3409,
Tel. +1 800 234 7381

Uruguay: see South America

Vietnam: see Singapore

Yugoslavia: PHILIPS, Trg N. Pasica 5/v, 11000 BEOGRAD,
Tel. +381 11 625 344, Fax.+381 11 635 777

For all other countries apply to: Philips Semiconductors,
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5600 MD EINDHOVEN, The Netherlands, Fax. +31 40 27 24825

Internet: <http://www.semiconductors.philips.com>

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Printed in The Netherlands

125108/00/03/PP16

Date of release: 1998 Jul 29

Document order number: 9397 750 04189

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