4-channel BTL driver for CD players BA6397FP

CD/CD-ROM Drivers (4 channels)

The BA6397FP is a 4-channel BTL driver for CD player motors and actuators. The 5V regulator and internal standard operational amplifier make this IC suited to a broad range of applications.

Applications

CD players and CD-ROM drives

Features

- 1) HSOP 28-pin package allows for miniaturization of applications.
- 2) Low number of external components.
- Driver gain is adjustable with a single attached resistor.
- Internal 5V regulator. (requires attached PNP transistor)
- 5) Internal standard operational amplifier.
- 6) Internal thermal shutdown circuit.

Block diagram



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Pin description

Pin No.	Pin name	Description		
1	CH1-OUT A	Driver CH1 negative output		
2	CH1-OUT B	Driver CH1 Positive output		
3	CH1-IN A	Driver CH1 input		
4	CH1-IN B	Driver CH1 input, gain adjustment pin		
5	Tr - B	Connect to external transistor base		
6	Vreg OUT	Constant voltage output, connects to external transistor collector		
7	MUTE	Driver mute control input		
8	GND	Ground		
9	CH2-IN B	Driver CH2 input, gain adjustment pin		
10	CH2-IN A	Driver CH2 input		
11	CH2-OUT B	Driver CH2 positive output		
12	CH2-OUT A	Driver CH2 negative output		
13	GND	Substrate ground		
14	OP OUT	Operational amplifier output		
15		Operational amplifier input, negative		
16	OP IN (+)	Operational amplifier output, positive		
17	CH3-OUT A	Driver CH3 negative output		
18	CH3-OUT B	Driver CH3 Positive output		
19	CH3-IN A	Driver CH3 input		
20	СНЗ-ІМ В	Driver CH3 input, gain adjustment pin		
21	Vcc	Power supply		
22	Vcc	Power supply		
23	BIAS IN	Bias amplifier input		
24	CH4-IN B	Driver CH4 input, gain adjustment pin		
25	CH4-IN A	Driver CH4 input		
26	CH4-OUT B	Driver CH4 positive output		
27	CH4-OUT A	Driver CH4 negative output		
28	GND	Substrate ground		

Note: "Positive output" and "negative output" indicate polarity relative to input.

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Input/output circuits











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Fig. 1

●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	18	V
Power dissipation	Pd	1.7 * 1	W
Operating temperature range	Topr		°°
Storage temperature range	Tstg	-55~150	<u>°C</u>

* 1. When mounted to a 50 mm x 50 mm x 1.0 mm paper phenol board Reduced by 13.6 mW for each Increase in Ta of 1°C over 25°C.

●Recommended operating conditions (Ta=25℃)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	6~9 *2	V

*2. The driver can operate as low as 4.5 V





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Optical Disc ICs

For CDs/CD-ROMs

CD/CD-ROM Drivers (4 channels)

•Electrical characteristics (Unless otherwise noted, $Ta=25^{\circ}C$, Vcc=8V, f=1kHz, $R_L=8\Omega$)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current	lcc	6.0	10.0	14.0	mA	No load
Output voltage, offset	Voo	40	-	40	mV	
Output voltage, HIGH	VOHD	5.2	5.6		ν –	
Output voltage, LOW	Vold		1.3	1.55	V	
Voltage gain (closed circuit)	Gvc	7.0	8.0	9.0	dB	Vin=0.1Vrms, 1kHz
Ripple rejection ratio	RR	-	60	-	dB	Vin=0.1Vrms, 100Hz
Slew rate	SR		2.0	-	V/uS	100 kHz square wave, 3 Vp-p output
MUTE OFF voltage	VMOFF	2.0			V	
[5V regulator]		£			.l	
Output voltage	Vreg	4.75	5.00	5.25	V	∟=100mA
Output load differential	ΔVAL	-50	0	10	mV	L=0~200mA
Power supply voltage differential	∆ Vvcc	-10	0	25	mV	(Vcc=6~9V) L=100mA
(Operational amplifier)	·	<u> </u>	. <u> </u>		I	
Offset voltage	VOFOP	-5	0	5	mV	
Input bias current	VBOP	_	—	300	nA	
High level output voltage	VOHOP	6.0	_	_	v	×
Low level output voltage	VOLOP	-	-	1.8	v	
Output drive current (sink)	Isink	10	50	_	mA	Vcc at 50 Ω
Output drive current (source)	ISOURCE	10	40		mA	50Ω at ground
Voltage gain (open circuit)	Gvo		78		dB	Vin=-75dBV, 1kHz
Slew rate	SRo₽		1	·	V/uS	100 kHz square wave, 4 Vp-p output
Ripple rejection ratio	RROP	_	65	_	dB	Vin=-20dBV, 100Hz
Common mode rejection ratio	CMRR	70	84	_	dB	Vin=-20dBV, 1kHz

Circuit operation

1. Driver

Inputs to the IC are the focus tracking error signal from the servo preamplifier and the control signal from the motor. The input signals, which normally center on 2.5V, are V/I converted by the preamplifier, generating a current corresponding to the input voltage. This current is passed through a resistor and into the internal reference voltage component, the preamplifier output being a signal centering on the internal reference voltage. Two systems (positive phase and negative phase) are created during V/I conversion, generating BTL output via the driver buffer.



Fig. 2

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Optical Disc ICs

CD/CD-ROM Drivers (4 channels)

For CDs/CD-ROMs

2. Regulator

This is a typical series regulator that generates a reference voltage internally. A PNP low saturation type transistor must be connected.



3. Operational amplifier

A standard 4558 type.



Fig. 4



Operation notes

- The BA6397FP has an internal thermal shutdown circuit. Output current is muted when the chip temperature exceeds 175°C (typically).
- If the mute pin (7 pin) voltage is opened or lowered below 0.5V, the output current will be muted. Pin 7 should be pulled up above 2.0V during normal use.
- The bias pin (23 pin) is muted when lowered below 1.4V (typically). Make sure it stays above 1.6V during normal use.
- 4. Muting occurs during thermal shutdown, mute-on operations or a drop in the bias pin voltage or supply voltage. In each case, only the drivers are muted. During muting, the output pins remain at the internal bias voltage, roughly (Vcc-VF)/2.
- 5. The internal input resistor has a positive temperature coefficient of roughly 2000ppm/degree, and so when changing the gain using an attached resistor, gain will also change at a rate of roughly 2000ppm/degree. There is virtually no gain variation due to temperature when using the internal input resistor.
- Be sure to connect the IC to a 0.1 μ F bypass capacitor to the power supply, at the base of the IC.
- 7. The radiating fin is connected to the package's internal GND, but should also be connected to an external ground.
- The capacitor between regulator output (6 pin) and GND also serves to prevent oscillation of the IC, so select one with good temperature characteristics.



Thermal reduction curve

Electrical characteristic curves





Fig. 6 Driver I/O characteristics (variable load)



INPUT VOLTAGE VIN (V)

Fig. 7 Drive I/O characteristics (variable power supply)

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