Preset graphic equalizers

Preset equalizer sound control (P.S.C) IC

BA3842F

The BA3842F is a preset graphic equalizer IC with dynamic bass boost which is designed for audio equipment.

Applications

Mini-component stereos

Micro-component stereos

Radio cassette recorders with CD

Features

- 1) Includes preset equalizer function with five equalizer positions and a flat mode setting.
- Built-in dynamic bass boost with gain settings from 0 to 20dB in steps of 5dB.
- 3) Low distortion and low noise.
- 4) Built-in dynamic bass boost and interlocked ALC (Auto Level Control).

●Absolute maximum ratings (Ta = 25℃)

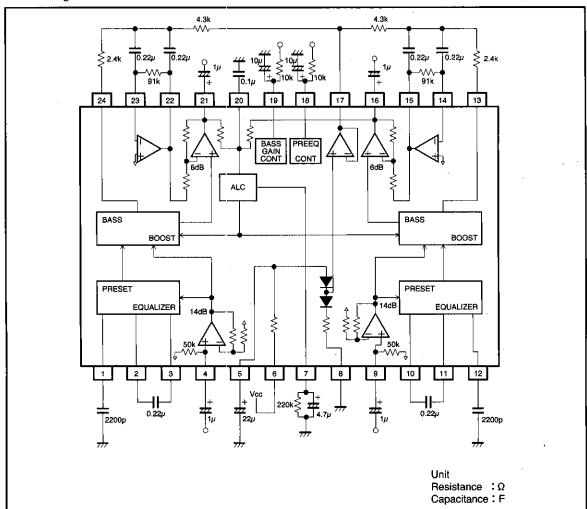
Parameter	Symbol	Limits	Unit
Applied voltage	Vcc	11.0	٧
Power dissipation	Pd	550*	mW
Operating temperature	Topr	−40~+85	°C
Storage temperature	Tstg	-55~+125	°C

^{*} When used above Ta = 25°C decreases 5.5 mW per degree.

Recommended operating conditions

Parameter	Symbol	Limits	Unit
Supply voltage	Vcc	8.0~10.5	V

Block diagram



●Pin description

Pin No.	Name	Function
1	HPFL	Left channel preset equalizer high band filter
2	LPFL2	Left channel preset equalizer low band filter
3	LPFL1	Left channel preset equalizer low band filter
4	LIN	Left channel input
5	FILTER	Filter
6	Vcc	Voltage supply
7	ALCC	ALC time constant
8	GND	Ground
9	RIN	Right channel input
10	LRFR1	Right channel preset equalizer low band filter
11	LRFR2	Right channel preset equalizer low band filter
12	HPFR	Right channel preset equalizer high band filter

Pin No.	Name	Function
13	BAR1	Right channel dynamic bass boost filter
14	BAR2	Right channel dynamic bass boost filter
15	BAR3	Right channel dynamic bass boost filter
16	ROUT	Right channel output
17	BIAS	Bias
18	SWGE	preset equalizer mode switch
19	SWTB	Bass boost mode switch
20	CAP	ALC band setting filter
21	LOUT	Left channel output
22	BAL3	Left channel dynamic bass boost filter
23	BAL2	Left channel dynamic bass boost filter
24	BAL1	Left channel dynamic bass boost filter

●Input/output circuit

Symbol	Number	Equivalent circuit	Description
HPFL HPFR	1 pin 12pin	Voc	Pin for setting the high cutoff frequency of the preset graphic equalizer. fC is set by adding a capacitor between the pin and a bias or ground.
LPFL2 LPFR2	2 pin 11pln	Voc Voc	Pin for setting the low cutoff frequency of the preset graphic equalizer. fC is set by adding a capacitor between this pin and LPFL1/LPFR1.
LPFL1 LPFR1	3 pin 10pin	Voc Voc	Pin for setting the low cutoff frequency of the preset graphic equalizer. fC is set by adding a capacitor between this pin and LPFL2/LPFR2.
LIN RIN	4 pin 9 pin	Vcc X	Signal input pin. Design the circuit for an input impedance of 50 kxxx (typ.).
FILTER	5 pin	Voc Vcc Voc	Filter input pin. This will be approximately 1/2VCC + 0.7 V. Connect a 10 µF capacitor to this pin. Discharge and precharge circuits are built in, however, they will not be affected by the external capacitor.

Symbol	Number	Equivalent circuit	Pin description
ALCC	7pin	Vcc 2*	Pin for setting the ALC time constant. Attack time and release time are set with an external resistor and capacitor.
BAR1 BAL1	13pin 24pin	Voc Voc	Pin for connecting the dynamic bass boost filter.
BAR2 BAL2	14pin 23pin	Vcc Vcc	Pin for connecting the dynamic bass boost filter. This pin has a high impedance, therefore, pattern wire should be short and direct.
BAR3 BAL3	15pin 22pin	Voc Voc	Pin for connecting the dynamic bass boost filter.
ROUT LOUT	16pin 21pin	Vcc Voc	Signal output pin.

Audio ICs BA3842F

Symbol	Number	Equivalent circuit	Pin description
BIAS	. 17pin 。	Voc Voc	Bias output pin. When using a bias, the pattern wire should be close to the pin to reduce the impedance of the wire.
SWGE	18pin	VCC	Mode switch for preset graphic equalizer.
SWTB	19pin	Voce The state of	Gain switch for dynamic bass boost.
CAP	20pin	30k	Pin for setting the ALC band. Set the impedance of this pin at 10 k Ω (typ.).
vcc	6pin		Supply voltage pin.
GND	8pin		Ground pin.

• Electrical characteristics (unless otherwise indicated, Ta = 25°C, Vcc = 10V, f = 1kHZ, Vin = 200mVrms, preset equalizer--normal: Rg = 620 Ω, during dynamic bass boost: $R_L = 10k\Omega$)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current	IQ	_	7	17	mA	Not designed to be radiation resistant.
consumption	Vom	2.4	2.8	_	Vrms	THD=1%
Maximum output voltage	Gv	18	20	22	dB	
Output voltage gain	THD		0.01	0.05	%	f=1KHz, BW:400Hz~300KHz
Total harmonic distortion	Vno	_	17	35	μ Vrms	Rg=0, DIN AUDIO*
Output noise voltage	СТ		-70	60	dB	f=1KHz
Crosstalk	Rin	35	50	65	kΩ	
Input impedance	RR	_	-41	-35	dB	fr=100Hz、Vr _{IN} =100mVrms
Ripple rejection	СВ	-1.5	0	1.5	dB	
Channel balance						
ODynamic bass boost						
Boost level 1	8B1	3	5	7	dB	f=60Hz V _{IN} =56mVrms
Boost level 2	BB2	8	10	12	dB	f=60Hz, TH1 V _{IN} =30mVrms
Boost level 3	BB3	13	15	17	dB	f=60Hz, TH2 V _{IN} =18mVrms
Boost level 4	BB4	18	20	22	dB	f=60Hz, TH3 V _{IN} =10mVrms
Switch control voltage 0	THO	_	0	0.3	V	No bass boost
Switch control voltage 1	TH1	1.0	1.3	1.6	V	Boost level 1 position
Switch control voltage 2	TH2	2.5	3.0	3.5	V	Boost level 2 position
Switch control voltage 3	TH3	4.5	5.0	5.5	V	Boost level 3 position
Switch control voltage 4	TH4	6.5	7.0	10	V	Boost level 4 position
©Preset equalizer	<u> </u>					<u>'</u>
Switch control voltage F	TH⊧	_	0	0.3	V	FLAT
Switch control voltage R	THe	1.0	1.3	1.6	V	ROCK
Switch control voltage P	TH _P	2.5	3.0	3.5	V	POP
Switch control voltage J	THJ	4.2	5.0	5.5	V	JAZZ
Switch control voltage C	THc	6.5	7.0	7.5	V	CLASSIC
Switch control voltage B	TH _B	8.5	9.0	10	V	BGM

^{*} Measurement performed using Matsushita Communication IndustrialVP-9690A DIN Audio filter (average value wave detection, effective value display).

Operating specifications

Input and output signal pins are in phase.

ONot designed for radiation resistance.

●Reference values for application circuit example (unless otherwise indicated, $Ta = 25^{\circ}C$, Vcc = 10V, Vin = 200mVrms, preset equalizer--normal: $Rg = 620 \Omega$, during dynamic bass boost: $RL= 10k \Omega$)

Parameter		Symbol	Тур.	Unit	Conditions
©Preset equali:	zer				
ROCK	Low range	GVAL	25	dB	f=100Hz TH _B
ROCK	High range	Gvrh	25	dB	f=10Hz TH _R
POP	Low range	GvPL	19	dB	f=100Hz TH _P
POP	Mid-range	G _{VPM}	23	dB	f=1kHz TH _P
POP	High range	Gvpн	20	dB	f=10kHz TH _P
JAZZ	Low range	GvJL	23	dB	f=100Hz TH _J
JAZZ	High range	Gvjh	23	dB	f=10kHz TH _J
CLASSIC	Low range	GvcL	23	dB	f=100Hz THc
CLASSIC	High range	GvcH	19	dB	f=10kHz THc
BGM	Low range	Gybl	19	dB	f=100Hz TH _B
BGM	High range	Gveн	18	dB	f=10kHz TH ₈
Output noise vo	Itage ROCK	V _{NOR}	32	μVrms	Rg=0 TH _B DIO AUDIO*
 	POP	V _{NOP}	18	μ Vrms	Rg=0 TH _R DIO AUDIO*
	JAZZ	V _{NOJ}	25	μVrms	Rg=0 TH _J DIO AUDIO*
	CLASSIC	V _{NOC}	21	μ Vrms	Rg=0 TH _C DIO AUDIO*
	BGM	V _{NOB}	17	μVrms	Rg=0 TH _B DIO AUDIO*
Output level when ALC on		Voagc	1.3	Vrms	During dynamic bass boost, f=60Hz VIN=130mVrms

^{*} Measurement performed using Matsushita Communication IndustriatVP-9690A DIN Audio filter (average value wave detection, effective value display).

Operating specifications

Input and output signal pins are in phase.

Measurement circuit

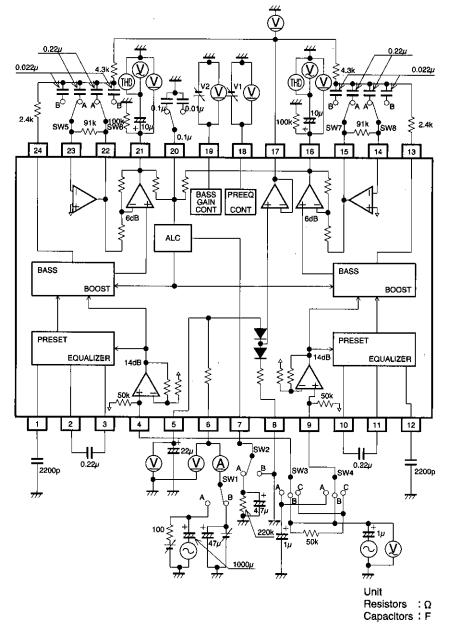


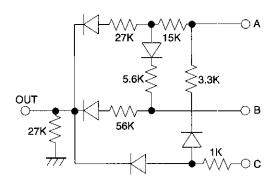
Fig.1

L: 0V

H:Vcc

Circuit operation

- Operating supply voltage range
 Within the operating supply voltage range, circuit
 functioning is guaranteed as long as the operating
 temperature range is not exceeded. However,
 verify carefully that the voltage, temperature, and
 component values are appropriate.
- 2. Control pins and control voltage settings
- 2-1. The DC control range is 0 V to Vcc for the preset equalizer and the dynamic bass boost control pins (pins 18 and 19). Make sure that the voltage applied to these control pins does not exceed Vcc.
- 2-2. Mode switch threshold values are determined by dividing resistors between Vcc and GND of both the preset equalizer and bass boost. If the control voltages are divided from the supply voltage of the IC, they will have greater tolerance with respect to Vcc fluctuations.
- 2-3. During mode switching, an abrupt change in the level of the DC output may occur, causing a sound. In this case, add the capacitor and resistor indicated in the application, or only the capacitor as needed.
- 2-4. Here is an example of determining the control pin voltage setting with the input of three values.



Connect OUT to the control pin.

Regarding the values of the various constants, it is important to consider current dissipation and other such problems. If such a problem should occur, change the constants and redesign the diode logic.

	OUT電圧	С	В	Α
•	0 V	L	L	L
	1.3 V	L	L	Ι
	3 V	L	Н	٦
	5 V	L	н	Н
•	7 V	Н	L	L
	9 V	Н	L	Н

3. Input coupling capacitors

Note that the polarity of the input coupling capacitors will change depending on the DC voltage to which they are connected. Set capacitors based on the frequency band to be used, taking into consideration the fact that the input impedance is $50k\Omega$.

4. Load resistances

If the values of the load resistors are too small, the output gain and total harmonic distortion may fluctuate slightly. Take this into consideration when connecting the subsequent stage.

5. The dynamic bass boost filter is a multi feedback active filter which forms the BPF.

Fo can be changed with the C value.

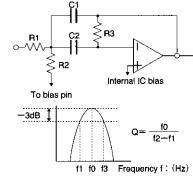
$$f_0=1/[2\pi \times \{(R_1//R_2)\times R_3\times C_1\times C_2\}^{1/2}]$$

$$Q=1/2\times\{(R_3/(R_1//R_2))^{1/2}$$

$$H_0=R_3/\{R_1\times(1+C_1/C_2)^{1/2}\}$$

When $R_1=2.4k\,\Omega$, $R_2=4.3k\,\Omega$, $R_3=91k\,\Omega$, and

$$C_1 = C_2 = 0.22 \,\mu\,F$$



If R_1 and R_2 are too small, the bass boost characteristics such as boost gain and crosstalk may change. Keep this in mind when setting filter values. Furthermore, design the application so that the bass boost level increments by 5dB from 5 to 20dB when $H_0=19$.

6 .Filter for preset equalizer

6-1. Low band

The low cutoff frequency FC of each mode is given by the following equation:

$$fc = 1/2 \pi CR = 1/(2 \pi \times C \times 15.6 k\Omega)$$

When $C = 0.22 \mu F$,

fc ≒ 46 (HZ)

The fC value can be changed to make slight changes in the boost and cutoff gain of each mode.

6-2. High band

The high cutoff frequencies FC of each mode are given by the following equation:

$$fc = 1/2\pi CR$$

When C = 2200pF,

 $\begin{array}{lll} \text{ROCK: R=8.0k}\,\Omega & \text{fc} \div 9.0 \text{ (kHz)} \\ \text{JAZZ: R=10.7k}\,\Omega & \text{fc} \div 6.8 \text{ (kHz)} \\ \text{CLASSIC: R=14.9k}\,\Omega & \text{fc} \div 4.8 \text{ (kHz)} \end{array}$

BGM. POP: $R=17.4k\Omega$ fc=4.2 (kHz)

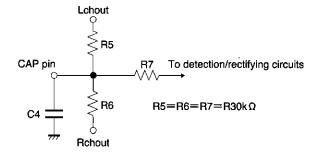
The fC values can be changed to make slight changes in the boost and cutoff gain of each mode.

Note:

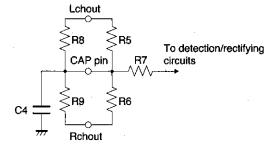
If a high or low fc value is changed, the gains of all modes will change. The gain cannot be changed for only one mode.

7. ALC

The band of ALC detection can be changed with the value of C₄. It must be adjusted appropriately for the dynamic bass boost fo. In the example application circuit C₄ = 0.1 μ F, thus fc = 1/2 π C₄ (R₅//R₇)

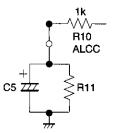


The detection wave is dually rectified. In the example application circuit, it is 1.3Vrms, but if the resistors R_0 and R_0 are connected as shown below, it will lower the ALC level. Adjust the ALC level as needed. The detection band at this time is : fc = $1/2 \pi C_4 (R_0//R_0//R_0//R_0)$



8. Attack time and release time

Adjust the attack and release times with the resistors and capacitors connected to the ALCC pin. The attack time is determined by R₁₀ (1k Ω) and C₅, and the release time is determined by R₁₁ and C₅. If the value set for C₅ is below 4.7 μ F, the ALC level may become unstable.



●Electrical characteristic curves (reference values)

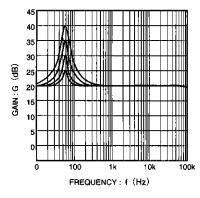


Fig. 2 Dynamic bass boost frequency characteristics

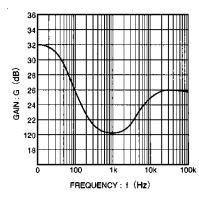


Fig. 3 PRE-G-EQ frequency characteristics (rock)

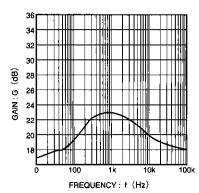


Fig. 4 PRE-G-EQ frequency characteristics (pop)

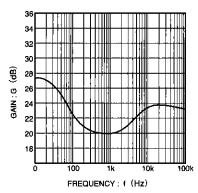


Fig. 5 PRE-G-EQ frequency characteristics (jazz)

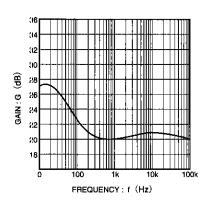


Fig. 6 PRE-G-EQ frequency characteristics (classic)

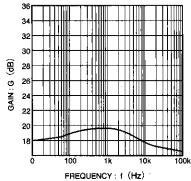


Fig. 7 PRE-G-EQ frequency characteristics (BGM)

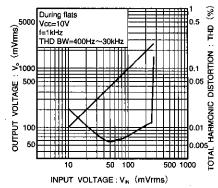


Fig.8 Input/output characteristics

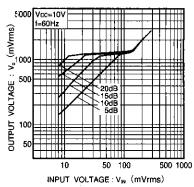
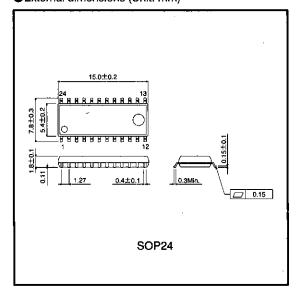


Fig.9 Bass boost ALC input/output characteristics

●External dimensions (Unit: mm)



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