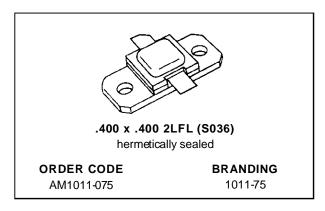


AM1011-075

RF & MICROWAVE TRANSISTORS L-BAND AVIONICS APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- 10:1 VSWR CAPABILITY
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 75 W MIN. WITH 9.2 dB GAIN

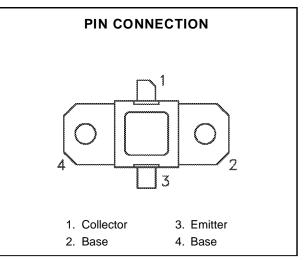


DESCRIPTION

The AM1011-075 device is a high power Class C transistor specifically designed for L-Band Avionics transponder/interrogator pulsed output and driver applications.

This device is capable of operation over a wide range of pulse widths, duty cycles, and temperatures and is capable of withstanding 10:1 output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

The AM1011-075 is supplied in the AMPAC™ Hermetic Metal/Ceramic package with internal Input/Output matching structures.



ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C)

Symbol	Parameter	Value	Unit	
P _{DISS}	Power Dissipation* (T _C ≤ 100°C)	175	W	
Ic	Device Current*	5.4	А	
V _C C	Collector-Supply Voltage*	55	V	
TJ	Junction Temperature (Pulsed RF Operation)	250	°C	
T _{STG}	Storage Temperature	- 65 to +200	°C	

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	0.86	°C/W

^{*}Applies only to rated RF amplifier operation

September 1992

ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

Symbol	Test Conditions	Value			11:4:4		
		Min.	Тур.	Max.	Unit		
BV _{CBO}	I _C = 10mA	$I_E = 0mA$		65	_		V
BVEBO	I _E = 4mA	$I_C = 0mA$		3.5	_	_	V
BVcer	IC = 20mA	$R_{BE} = 10\Omega$		65	_	_	V
ICES	Vce = 50V			_	_	6	mA
h _{FE}	V _{CE} = 5V	I _C = 1mA		10	_	_	_

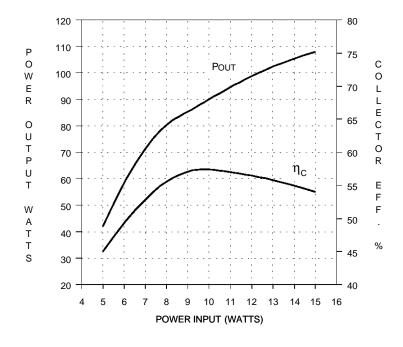
DYNAMIC

Symbol	Test Conditions			Value			Unit
Symbol	rest Conditions		Min.	Тур.	Max.	Unit	
Pout	f = 1090MHz	$P_{IN} = 9W Peak$	$V_{CC} = 50V$	75	84	_	W
ης	f = 1090MHz	P _{IN} = 9W Peak	$V_{CC} = 50V$	48	56	_	%
G _P	f = 1090MHz	P _{IN} = 9W Peak	$V_{CC} = 50V$	9.2	9.7	_	dB

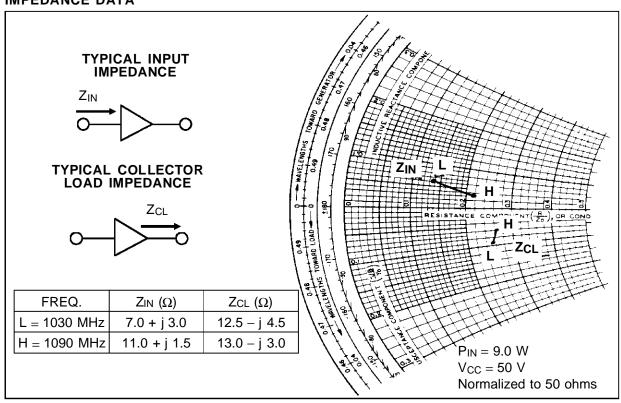
Note: Pulse Width = $32\mu Sec$ Duty Cycle = 2%

TYPICAL PERFORMANCE

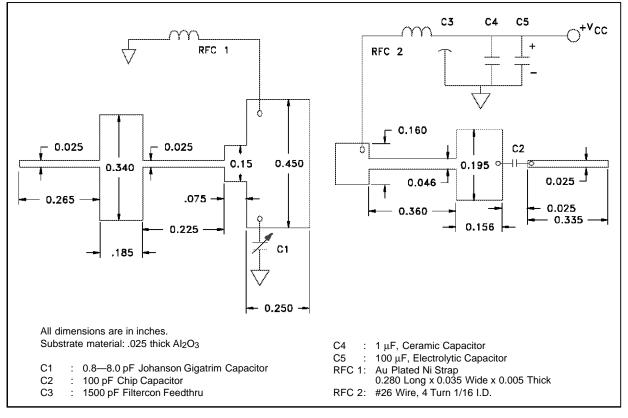
TYPPICAQWPOWER/TOUPPHEFT&R COEFECINOR VEPFICEINOY VS POWER INPUT



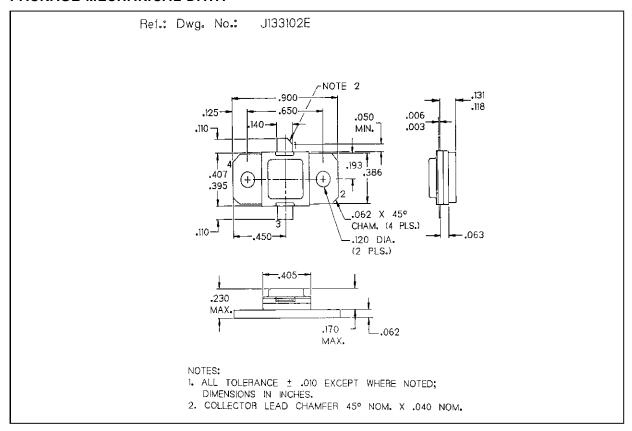
IMPEDANCE DATA



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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