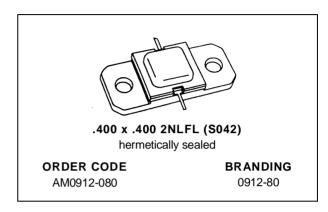


AM0912-080

RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 90 W MIN. WITH 13 dB GAIN
- BANDWIDTH 225 MHz



DESCRIPTION

The AM0912-080 Avionics power transistor is a broadband, high peak pulse power device specifically designed for avionics applications requiring broad bandwidth with moderate duty cycle and pulse width constraints such as ground/ship based DME/TACAN.

This device is also designed for specialized applications including JTIDS where reduced power provided under pulse formats utilizing short pulse widths and high burst or overall duty cycles.

The AM0912-080 is housed in the unique AMPAC™ Hermetic Metal/Ceramic package with internal Input/Output matching structures.

PIN CONNECTION 1. Collector 3. Emitter 2. Base 4. Base

ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
P _{DISS}	Power Dissipation* (T _C ≤100°C)	220	W
Ic	Device Current*	7.0	А
Vcc	Collector-Supply Voltage*	50	V
TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	0.80	°C/W

^{*}Applies only to rated RF amplifier operation

September 1992

ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

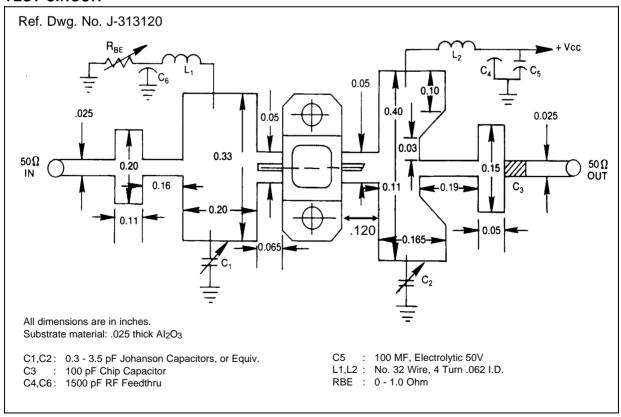
Symbol	Took Conditions	Value			11		
	Test Conditions		Min.	Тур.	Max.	Unit	
ВУсво	$I_C = 40mA$	$I_E = 0mA$		65	_		V
BV _{EBO}	I _E = 10mA	I _C = 0mA		3.0	_	_	V
BVCER	IC = 40mA	$R_{BE} = 10\Omega$		65	_	_	V
Ісво	V _{CB} = 50V			_	_	12	mA
hFE	V _{CE} = 5V	I _C = 2A		20	_	120	_

DYNAMIC

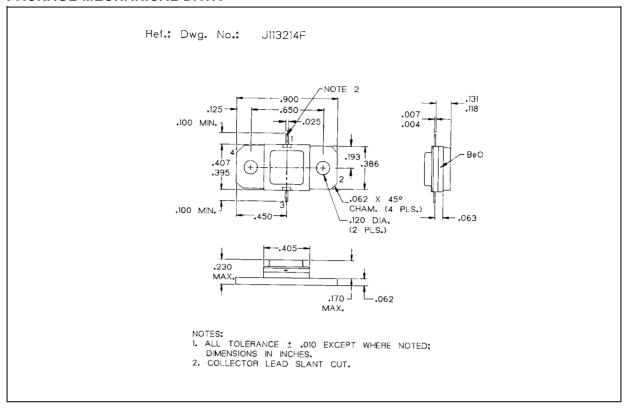
Symbol	Test Conditions		Value		Unit		
Symbol	rest Conditions			Min.	Тур.	Max.	Unit
Pout	f = 960 — 1215MHz	$P_{IN} = 13W$	$V_{CC} = 50V$	90	100	_	W
ης	f = 960 — 1215MHz	$P_{\text{IN}}=13W$	$V_{CC} = 50V$	38	44	_	%
G _P	f = 960 — 1215MHz	$P_{IN} = 13W$	V _{CC} = 50V	8.4	_	_	dB

Note: Pulse Width = $10\mu Sec$ Duty Cycle = 10%

TEST CIRCUIT



PACKAGE MECHANICAL DATA



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