

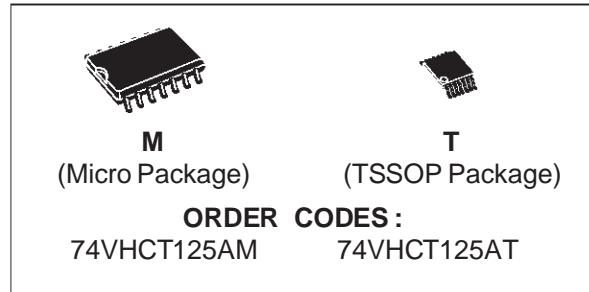
QUAD BUS BUFFERS (3-STATE)

PRELIMINARY DATA

- HIGH SPEED: $t_{PD} = 3.8 \text{ ns}$ (TYP.) at $V_{CC} = 5\text{V}$
- LOW POWER DISSIPATION:
 $I_{CC} = 4 \mu\text{A}$ (MAX.) at $T_A = 25^\circ\text{C}$
- COMPATIBLE WITH TTL OUTPUTS:
 $V_{IH} = 2\text{V}$ (MIN), $V_{IL} = 0.8\text{V}$ (MAX)
- POWER DOWN PROTECTION ON INPUTS & OUTPUTS
- SYMMETRICAL OUTPUT IMPEDANCE:
 $|I_{OH}| = |I_{OL}| = 8 \text{ mA}$ (MIN)
- BALANCED PROPAGATION DELAYS:
 $t_{PLH} \approx t_{PHL}$
- OPERATING VOLTAGE RANGE:
 V_{CC} (OPR) = 4.5V to 5.5V
- PIN AND FUNCTION COMPATIBLE WITH 74 SERIES 125
- IMPROVED LATCH-UP IMMUNITY
- LOW NOISE: $V_{OLP} = 0.8\text{V}$ (Max.)

DESCRIPTION

The 74VHCT125A is an advanced high-speed CMOS QUAD BUS BUFFERS fabricated with sub-micron silicon gate and double-layer metal wiring C²MOS technology. It has similar high speed performance of equivalent Bipolar



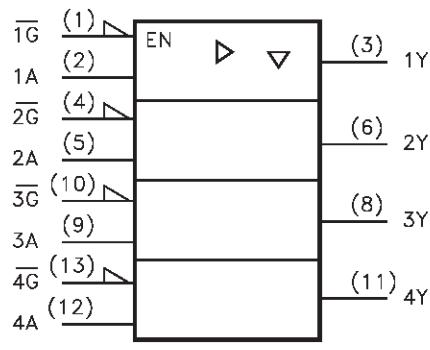
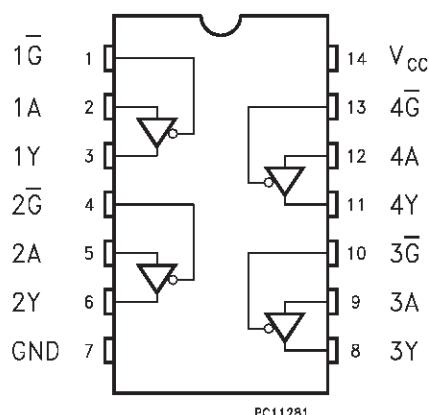
Schottky TTL combined with true CMOS low power dissipation.

This device requires the 3-STATE control input \bar{G} to be set high to place the output into the high impedance state.

Power down protection is provided on all inputs and outputs and 0 to 7V can be accepted on inputs with no regard to the supply voltage. This device can be used to interface 5V to 3V.

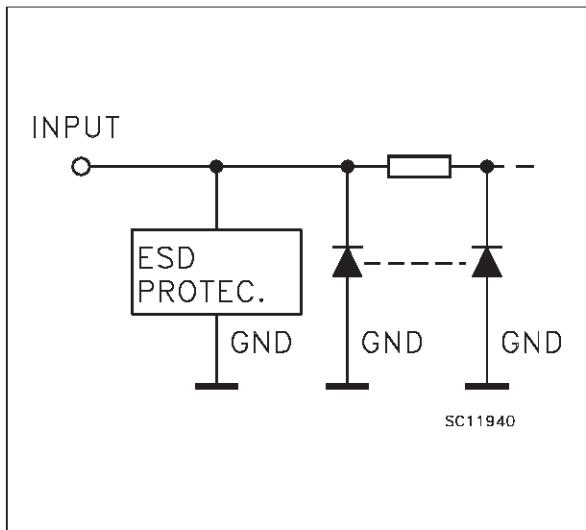
All inputs and outputs are equipped with protection circuits against static discharge, giving them 2KV ESD immunity and transient excess voltage.

PIN CONNECTION AND IEC LOGIC SYMBOLS



74VHCT125A

INPUT EQUIVALENT CIRCUIT



PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1, 4, 10, 13	$\overline{1G}$ to $\overline{4G}$	Output Enable Inputs
2, 5, 9, 12	1A to 4A	Data Inputs
3, 6, 8, 11	1Y to 4Y	Data Outputs
7	GND	Ground (0V)
14	Vcc	Positive Supply Voltage

TRUTH TABLE

A	\overline{G}	Y
X	H	Z
L	L	L
H	L	H

X: "H" or "L"

Z: High Impedance

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	-0.5 to +7.0	V
V_I	DC Input Voltage	-0.5 to +7.0	V
V_O	DC Output Voltage (see note 1)	-0.5 to +7.0	V
V_O	DC Output Voltage (see note 2)	-0.5 to $V_{CC} + 0.5$	V
I_{IK}	DC Input Diode Current	- 20	mA
I_{OK}	DC Output Diode Current	± 20	mA
I_o	DC Output Current	± 25	mA
I_{CC} or I_{GND}	DC V_{CC} or Ground Current	± 50	mA
T_{stg}	Storage Temperature	-65 to +150	°C
T_L	Lead Temperature (10 sec)	300	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

1) Output in OFF State

2) High or Low State

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	4.5 to 5.5	V
V_I	Input Voltage	0 to 5.5	V
V_O	Output Voltage (see note 1)	0 to 5.5	V
V_O	Output Voltage (see note 2)	0 to V_{CC}	V
T_{op}	Operating Temperature	-40 to +85	°C
dt/dv	Input Rise and Fall Time (see note 3) ($V_{CC} = 5.0 \pm 0.5V$)	0 to 20	ns/V

1) Output in OFF State

2) High or Low State

3) V_{IN} from 0.8V to 2 V

DC SPECIFICATIONS

Symbol	Parameter	Test Conditions		Value					Unit	
		V _{CC} (V)		T _A = 25 °C			-40 to 85 °C			
				Min.	Typ.	Max.	Min.	Max.		
V _{IH}	High Level Input Voltage	4.5 to 5.5		2			2		V	
V _{IL}	Low Level Input Voltage	4.5 to 5.5				0.8		0.8	V	
V _{OH}	High Level Output Voltage	4.5	V _I ^(*) = V _{IH}	I _O =-50 µA	4.4	4.5	4.4		V	
		4.5	V _I ^(*) = V _{IL}	I _O =-8 mA	3.94		3.8			
V _{OL}	Low Level Output Voltage	4.5	V _I ^(*) = V _{IH} or V _{IL}	I _O =50 µA		0.0	0.1	0.1	V	
		4.5		I _O =8 mA			0.36	0.44		
I _{OZ}	3 State Output Leakage Current	4.5 to 5.5	V _I = V _{IH} or V _{IL} V _O = 0V to 5.5V				±0.25		±2.5 µA	
I _I	Input Leakage Current	0 to 5.5	V _I = 5.5V or GND				±0.1		±1.0 µA	
I _{CC}	Quiescent Supply Current	5.5	V _I = V _{CC} or GND				2		20 µA	
ΔI _{CC}	Additional Worst Case Supply Current	5.5	One Input at 3.4V, other input at V _{CC} or GND				1.35		1.5 mA	
I _{OPD}	Output Leakage Current	0	V _{OUT} = 5.5V				0.5		5.0 µA	

(*) All outputs loaded

AC ELECTRICAL CHARACTERISTICS (Input t_r = t_f = 3 ns)

Symbol	Parameter	Test Condition		Value					Unit	
		V _{CC} (V)		T _A = 25 °C			-40 to 85 °C			
				Min.	Typ.	Max.	Min.	Max.		
t _{PLH} t _{PHL}	Propagation Delay Time	5.0 ^(*)	C _L = 15 pF		3.8	5.5	1.0	6.5	ns	
		5.0 ^(*)	C _L = 50 pF		5.3	7.5	1.0	8.5		
t _{PLZ} t _{PHZ}	Output Disable Time	5.0 ^(*)	C _L = 15 pF (**)		3.6	5.1	1.0	6.0	ns	
		5.0 ^(*)	C _L = 50 pF (**)		5.1	7.1	1.0	8.0		
t _{PZL} t _{PZH}	Output Enable Time	5.0 ^(*)	C _L = 50 pF (**)		6.1	8.8	1.0	10.0	ns	

(*) Voltage range is 5.0V ± 0.5V

(**) R_L = 1KΩ

74VHCT125A

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Conditions		Value					Unit
				TA = 25 °C			-40 to 85 °C		
		Min.	Typ.	Max.	Min.	Max.	Min.	Max.	
C _{IN}	Input Capacitance			4	10			10	pF
C _{OUT}	Output Capacitance			10					pF
C _{PD}	Power Dissipation Capacitance (note 1)			14					pF

1) C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation. I_{CC(OPT)} = C_{PD} • V_{CC} • f_{IN} + I_{CC}/4 (per circuit)

DYNAMIC SWITCHING CHARACTERISTICS

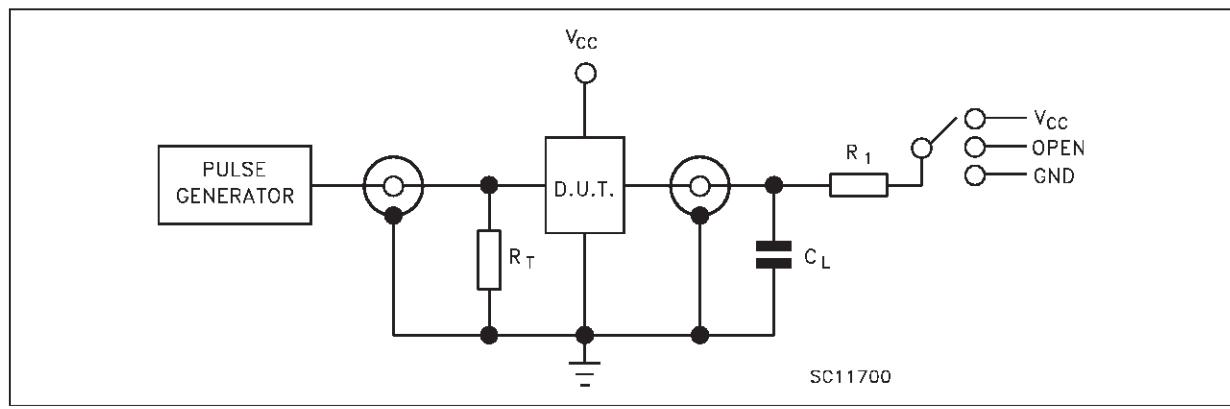
Symbol	Parameter	Test Conditions		Value					Unit
				TA = 25 °C			-40 to 85 °C		
		V _{CC} (V)	Min.	Typ.	Max.	Min.	Max.	Min.	
V _{OLP}	Dynamic Low Voltage Quiet Output (note 1, 2)	5.0	C _L = 50 pF		0.3	0.8			V
V _{OLV}				-0.8	-0.3				
V _{IHD}				2					
V _{ILD}						0.8			

1) Worst case package.

2) Max number of outputs defined as (n). Data inputs are driven 0V to 3.0V, (n-1) outputs switching and one output at GND.

3) Max number of data inputs (n) switching, (n-1) switching 0V to 3.0V. Inputs under test switching: 3.0V to threshold (V_{ILD}), 0V to threshold (V_{IHD}), f=1MHz.

TEST CIRCUIT



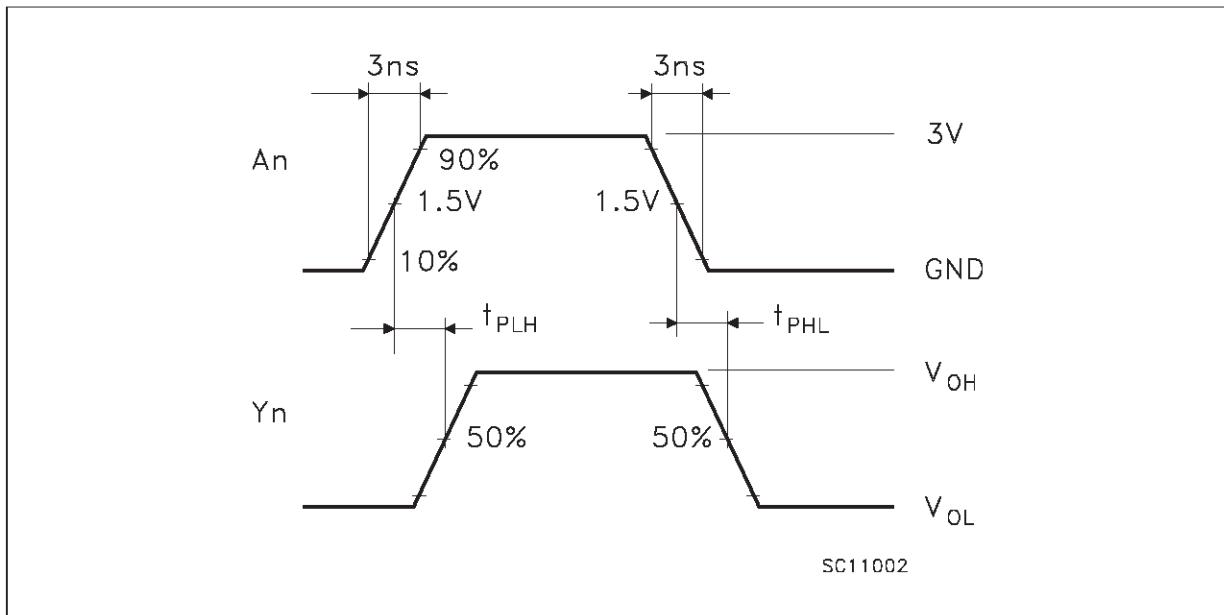
TEST	SWITCH
t _{PLH} , t _{PHL}	Open
t _{PZL} , t _{PLZ}	V _{CC}
t _{PZH} , t _{PHZ}	GND

C_L = 15/50 pF or equivalent (includes jig and probe capacitance)

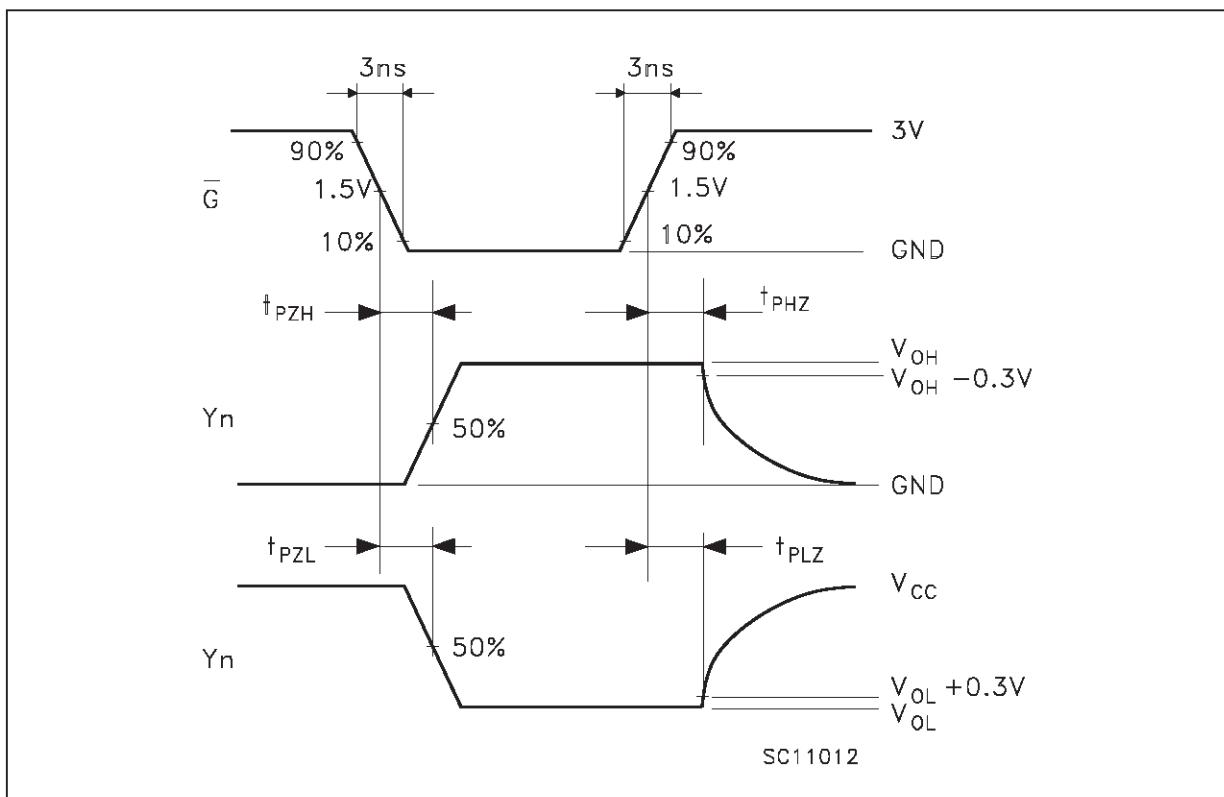
R_L = R₁ = 1kΩ or equivalent

R_T = Z_{out} of pulse generator (typically 50Ω)

WAVEFORM 1: PROPAGATION DELAYS (f=1MHz; 50% duty cycle)

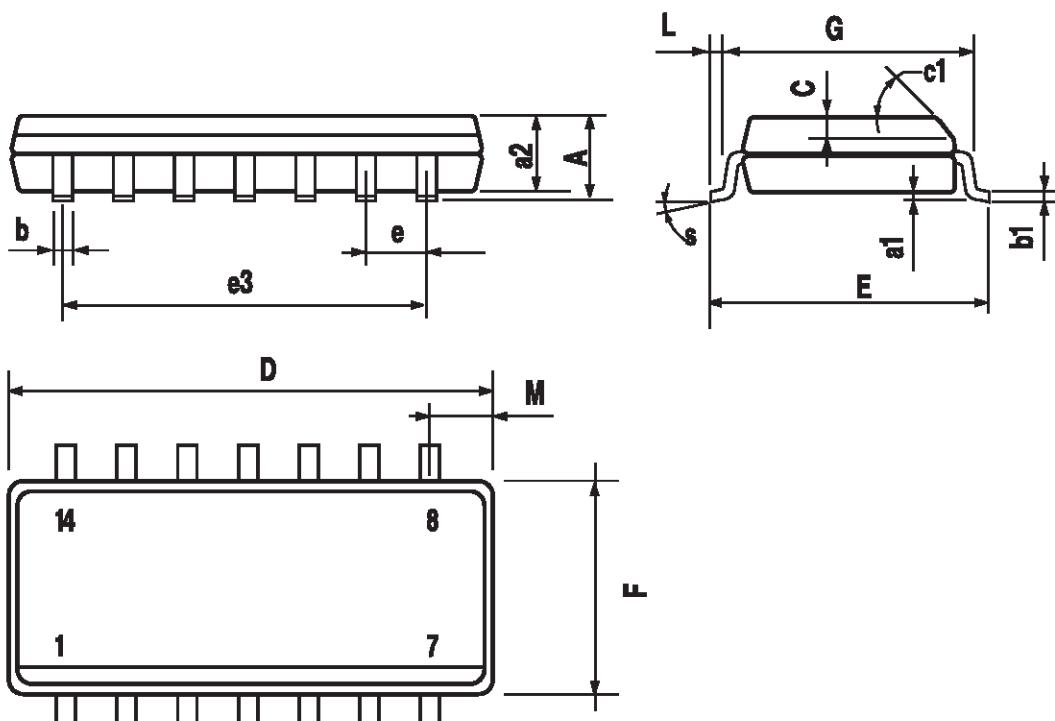


WAVEFORM 2: OUTPUT ENABLE AND DISABLE TIME (f=1MHz; 50% duty cycle)



SO-14 MECHANICAL DATA

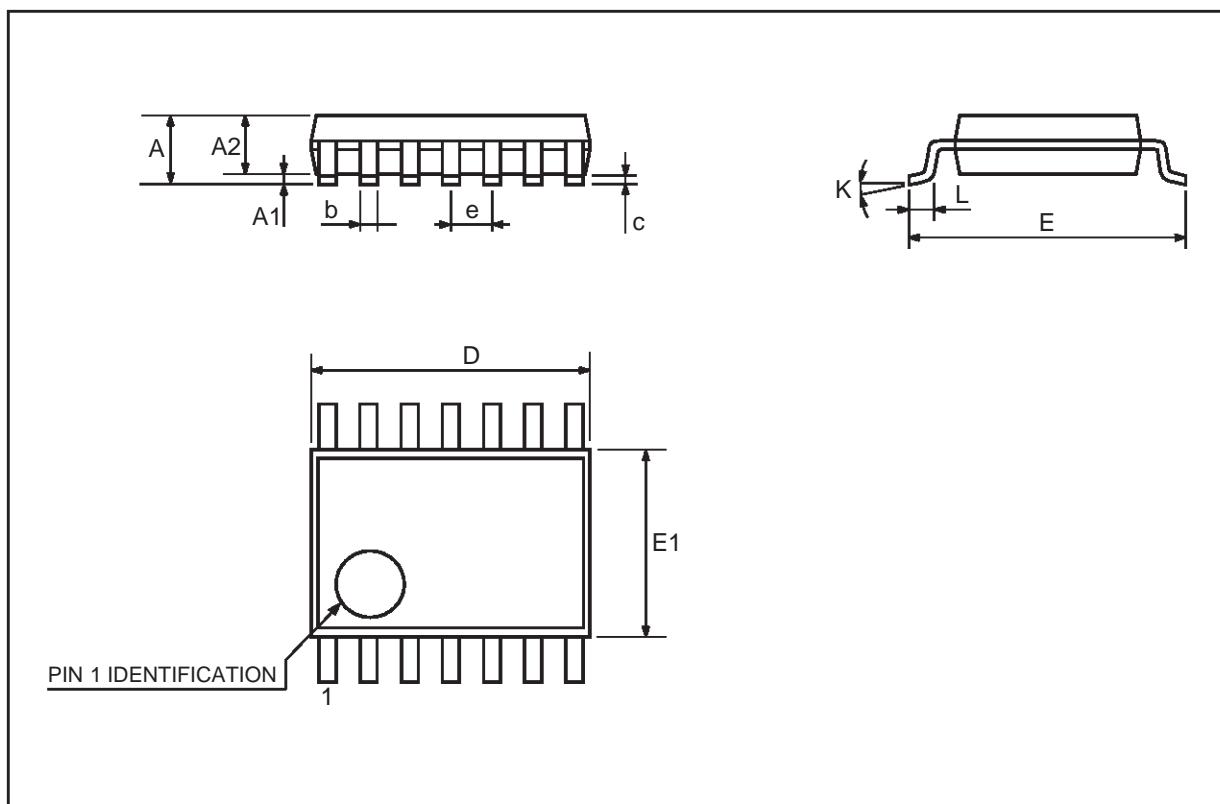
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.75			0.068
a1	0.1		0.2	0.003		0.007
a2			1.65			0.064
b	0.35		0.46	0.013		0.018
b1	0.19		0.25	0.007		0.010
C		0.5			0.019	
c1		45 (typ.)				
D	8.55		8.75	0.336		0.344
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		7.62			0.300	
F	3.8		4.0	0.149		0.157
G	4.6		5.3	0.181		0.208
L	0.5		1.27	0.019		0.050
M			0.68			0.026
S		8 (max.)				



P013G

TSSOP14 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.1			0.433
A1	0.05	0.10	0.15	0.002	0.004	0.006
A2	0.85	0.9	0.95	0.335	0.354	0.374
b	0.19		0.30	0.0075		0.0118
c	0.09		0.20	0.0035		0.0079
D	4.9	5	5.1	0.193	0.197	0.201
E	6.25	6.4	6.5	0.246	0.252	0.256
E1	4.3	4.4	4.48	0.169	0.173	0.176
e		0.65 BSC			0.0256 BSC	
K	0°	4°	8°	0°	4°	8°
L	0.50	0.60	0.70	0.020	0.024	0.028



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