



74LVQ4066

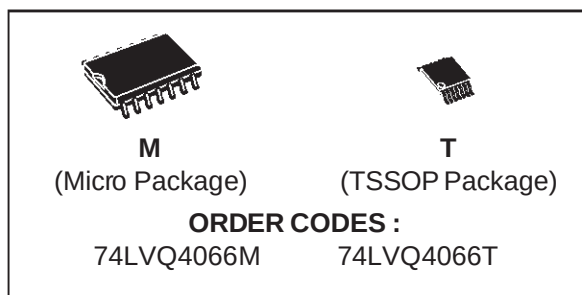
QUAD BILATERAL SWITCH

PRELIMINARY DATA

- HIGH SPEED: $t_{PD} = 0.4 \text{ ns}$ (TYP.) at $V_{CC} = 3.3\text{V}$
- COMPATIBLE WITH TTL LEVEL
- LOW POWER DISSIPATION:
 $I_{CC} = 1 \mu\text{A}$ (MAX.) at $T_A = 25^\circ\text{C}$
- LOW "ON" RESISTANCE:
 $R_{ON} = 20\Omega$ at $V_{CC} = 3.3\text{V}$, $I_{I/O} \leq 1\text{mA}$
- SINE WAVE DISTORTION:
0.04% at $V_{CC} = 3.3\text{V}$, $f = 1\text{KHz}$
- OPERATING VOLTAGE RANGE:
 $V_{CC(OPER)} = 2\text{V}$ to 3.6V
- PIN AND FUNCTION COMPATIBLE WITH
74 SERIES 4066
- IMPROVED LATCH-UP IMMUNITY

DESCRIPTION

The LVQ4066 is a low voltage CMOS QUAD BILATERAL SWITCH fabricated with sub-micron silicon gate and double-layer metal wiring $C^2\text{MOS}$ technology. It is ideal for low power and low noise 3.3V applications.

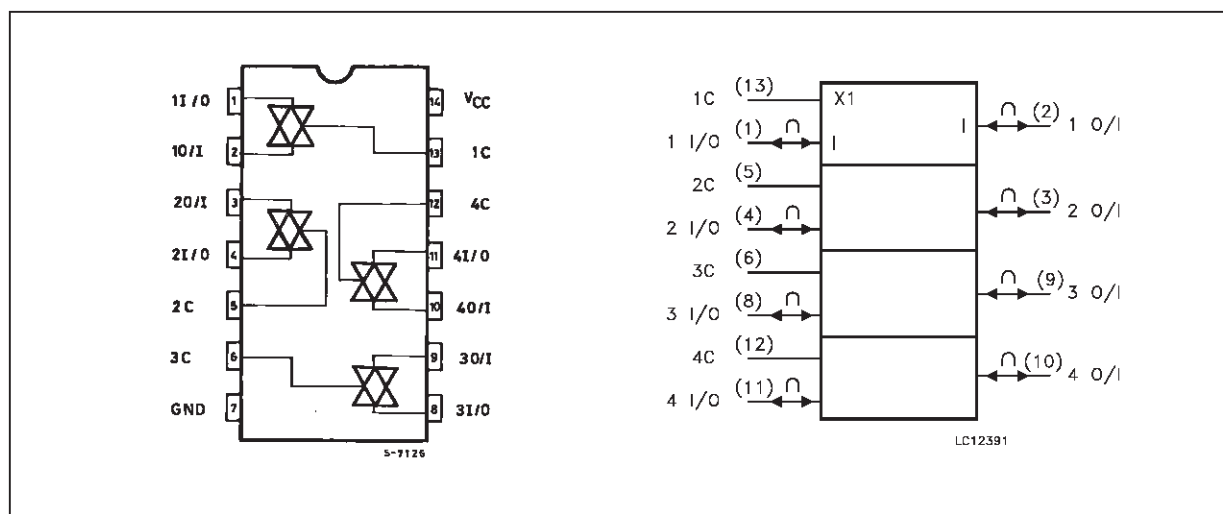


It has an ON-resistance which is greatly reduced in comparison with 74HC4066.

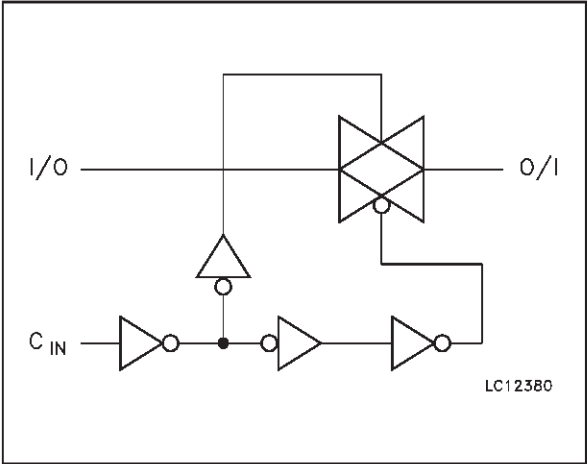
The C input is provided to control the switch; the switch is ON when the C input is held high and OFF when C is held low.

All inputs and outputs are equipped with protection circuits against static discharge, giving them 2KV ESD immunity and transient excess voltage.

PIN CONNECTION AND IEC LOGIC SYMBOLS



LOGIC DIAGRAM



PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1, 4, 8, 11	1 to 4 I/O	Independent Input/Output
2, 3, 9, 10	1 to 4 O/I	Independent Output/Input
13, 5, 6, 12	1C to 4C	Enable Input (Active HIGH)
7	GND	Ground (0V)
14	V _{CC}	Positive Supply Voltage

TRUTH TABLE

CONTROL	SWITCH FUNCTION
H	ON
L	OFF

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	-0.5 to +7	V
V _I	DC Input Voltage	-0.5 to V _{CC} + 0.5	V
V _O	DC Output Voltage	-0.5 to V _{CC} + 0.5	V
I _{IK}	DC Input Diode Current	± 20	mA
I _{OK}	DC Output Diode Current	± 20	mA
I _O	DC Output Current	± 50	mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current	± 200	mA
T _{stg}	Storage Temperature	-65 to +150	°C
T _L	Lead Temperature (10 sec)	300	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage (note 1)	2 to 3.6	V
V _I	Input Voltage	0 to V _{CC}	V
V _O	Output Voltage	0 to V _{CC}	V
T _{op}	Operating Temperature:	-40 to +85	°C
dt/dv	Input Rise and Fall Time (V _{CC} = 3V) (note 2)	0 to 10	ns/V

1) Truth Table guaranteed: 1.2V to 3.6V

2) V_{IN} from 0.8V to 2V

DC SPECIFICATIONS

Symbol	Parameter	Test Conditions		Value					Unit
		V _{CC} (V)		T _A = 25 °C			-40 to 85 °C		
				Min.	Typ.	Max.	Min.	Max.	
V _{IH}	High Level Input Voltage	3.0 to 3.6		2.0			2.0		V
V _{IL}	Low Level Input Voltage					0.8		0.8	V
R _{ON}	ON Resistance	3.0 to 3.6	V _I = V _{IH} V _{I/O} = V _{CC} to GND I _{I/O} ≤ 1mA		20	30		40	Ω
			V _I = V _{IH} V _{I/O} = V _{CC} to GND I _{I/O} ≤ 1mA		10	15		20	
ΔR _{ON}	Difference of ON Resistance Between Switches	3.0 to 3.6	V _I = V _{IH} V _{I/O} = V _{CC} to GND I _{I/O} ≤ 1mA		2				Ω
I _{OFF}	Input/Output Leakage Current (SWITCH OFF)	3.6	V _{OS} = V _{CC} to GND V _{IS} = V _{CC} to GND V _I = V _{IL}			±0.1		±1.0	μA
I _{IZ}	Switch Input Leakage Current (SWITCH ON, OUTPUT OPEN)	3.6	V _{OS} = V _{CC} to GND V _I = V _{IH}			±0.1		±1.0	μA
I _{IN}	Control Input Current	3.6	V _I = V _{CC} to GND			0.1		1.0	μA
I _{CC}	Quiescent Supply Current	3.6	V _I = V _{CC} or GND			1		10	μA

1) Maximum test duration 2ms, one output loaded at time

2) Incident wave switching is guaranteed on transmission lines with impedances as low as 50 Ω.

(*) All outputs loaded.

DYNAMIC SWITCHING CHARACTERISTICS

Symbol	Parameter	Test Conditions		Value					Unit
		V _{CC} (V)		T _A = 25 °C			-40 to 85 °C		
				Min.	Typ.	Max.	Min.	Max.	
V _{OLP}	Dynamic Low Voltage	3.3	C _L = 50 pF		0.3	0.8			V
V _{OLV}	Quiet Output (note 1, 2)			-0.8	-0.3				
V _{IHD}	Dynamic High Voltage Input (note 1, 3)	3.3				2			
V _{ILD}	Dynamic Low Voltage Input (note 1, 3)	3.3		0.8					

1) Worst case package.

2) Max number of outputs defined as (n). Data inputs are driven 0V to 3.3V, (n-1) outputs switching and one output at GND.

3) Max number of data inputs (n) switching, (n-1) switching 0V to 3.3V. Inputs under test switching: 3.3V to threshold (V_{ILD}), 0V to threshold (V_{IHD}), f=1MHz.

AC ELECTRICAL CHARACTERISTICS ($C_L = 50 \text{ pF}$, $R_L = 500\Omega$, Input $t_r = t_f = 3 \text{ ns}$)

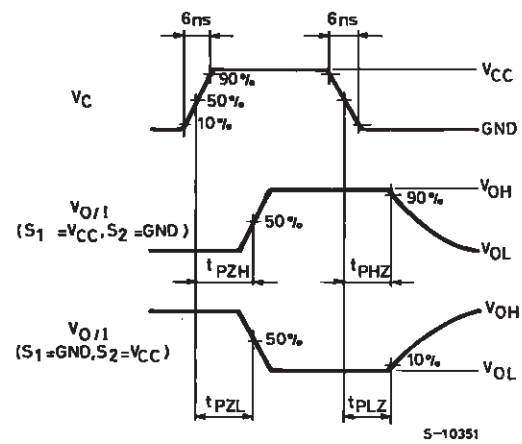
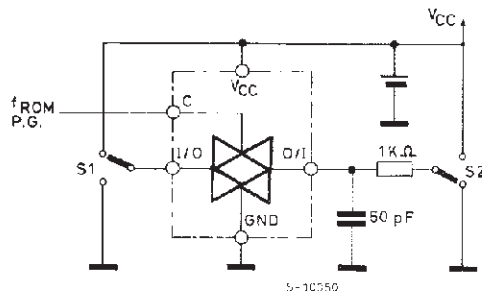
Symbol	Parameter	Test Condition		Value					Unit
		V _{CC} (V)		T _A = 25 °C			-40 to 85 °C		
				Min.	Typ.	Max.	Min.	Max.	
t _{PD}	Delay Time	3.3			0.4	0.8		1.2	ns
t _{PZL} t _{PZH}	Output Enable Time	3.3			2.5	4		5	ns
t _{PLZ} t _{PHZ}	Output Disable Time	3.3			5.0	7.5		9	ns
t _{MAX}	Control Input to Output	3.3			30				ns
C _{IN}	Input Capacitance	3.3			5				pF
C _{I/O}	Switch Terminal Capacitance	3.3			10				pF
C _{PD}	Power Dissipation Capacitance (note 1)	3.3			3				pF

1) C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation. $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

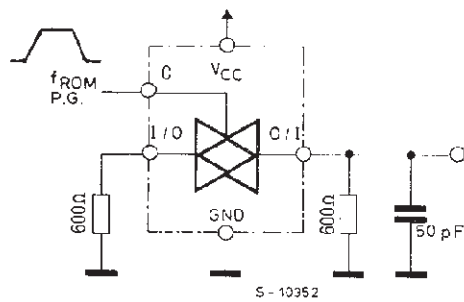
ANALOG SWITCH CHARACTERISTICS ($GND = 0 \text{ V}$, $T_A = 25^\circ\text{C}$)

Symbol	Parameter	Test Condition			Value	Unit
		V_{CC} (V)	V_{IN} (Vp-p)			
	Sine Wave Distortion (THD)	3.3	2.75	$f_{IN} = 1 \text{ KHz}$ $R_L = 10\text{K}\Omega$ $C_L = 50 \text{ pF}$	0.04	%
f_{MAX}	Frequency Responce (Switch ON)	3.3	Adjust f_{IN} voltage to Obtain 0dBm at V_{OS} . Increase f_{IN} Frequency until dB Meter reads -3dB $R_L = 50\Omega$, $C_L = 10\text{pF}$			150 MHz
	Feedthrough Attenuation (Switch OFF)	3.3	V_{IN} is centered at $V_{CC}/2$. Adjust input for 0dBm $R_L = 600\Omega$, $C_L = 50\text{pF}$, $f_{IN} = 1\text{MHz}$ sine wave			-60 dB
	Crosstalk (Control Input to Signal Output)	3.3	$R_L = 600\Omega$, $C_L = 50\text{pF}$, $f_{IN} = 1\text{MHz}$ square wave			60 mV
	Crosstalk (Between Any Switchies)	3.3	$R_L = 600\Omega$, $C_L = 50\text{pF}$, $f_{IN} = 1\text{MHz}$ sine wave			-60 dB

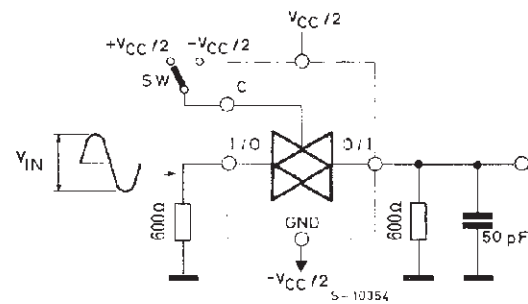
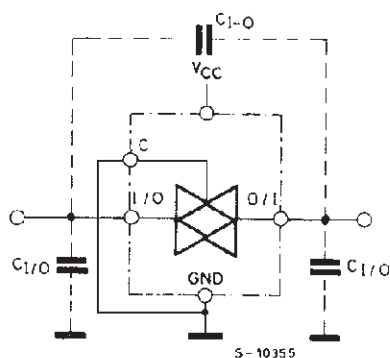
SWITCHING CHARACTERISTICS TEST CIRCUIT

 t_{PLZ} , t_{PHZ} , t_{PZL} , t_{PZH} .

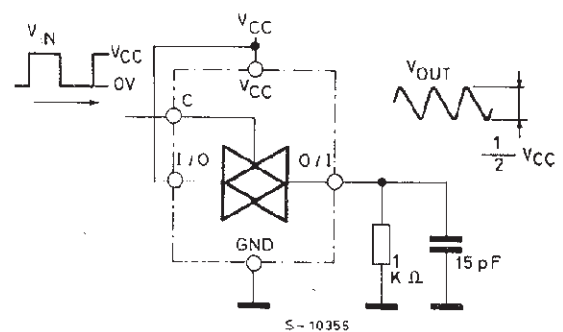
CROSSTALK (control to output)



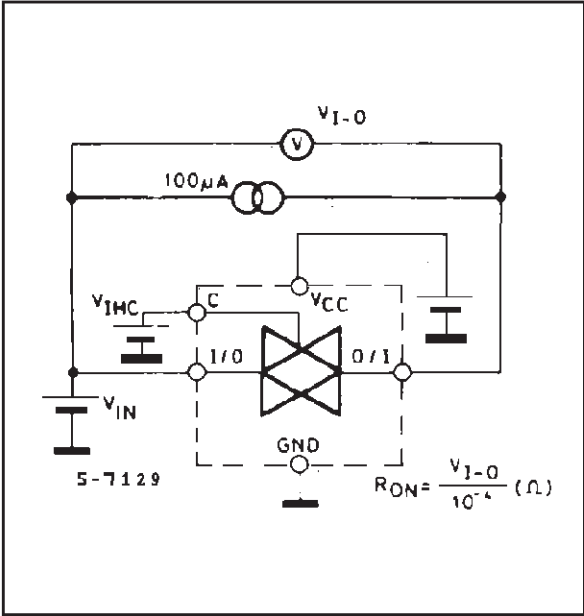
BANDWIDTH AND FEEDTHROUGH ATTENUATION

GND (V_{SS}) C_{I-O} $C_{I/O}$ GND (V_{SS})

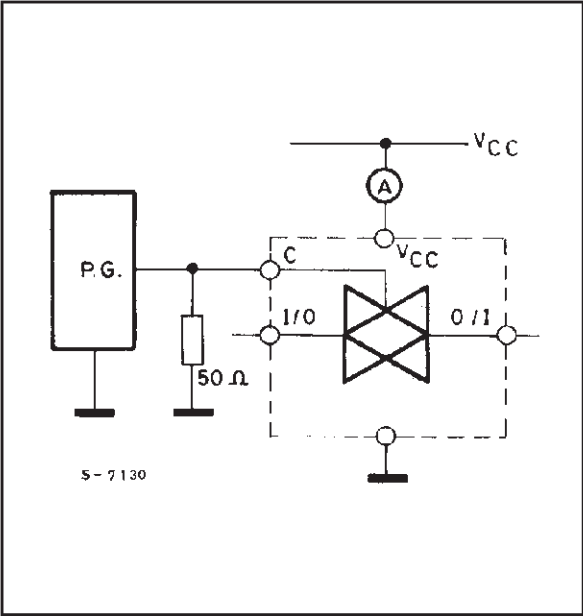
MAXIMUM CONTROL FREQUENCY



CHANNEL RESISTANCE (R_{ON})

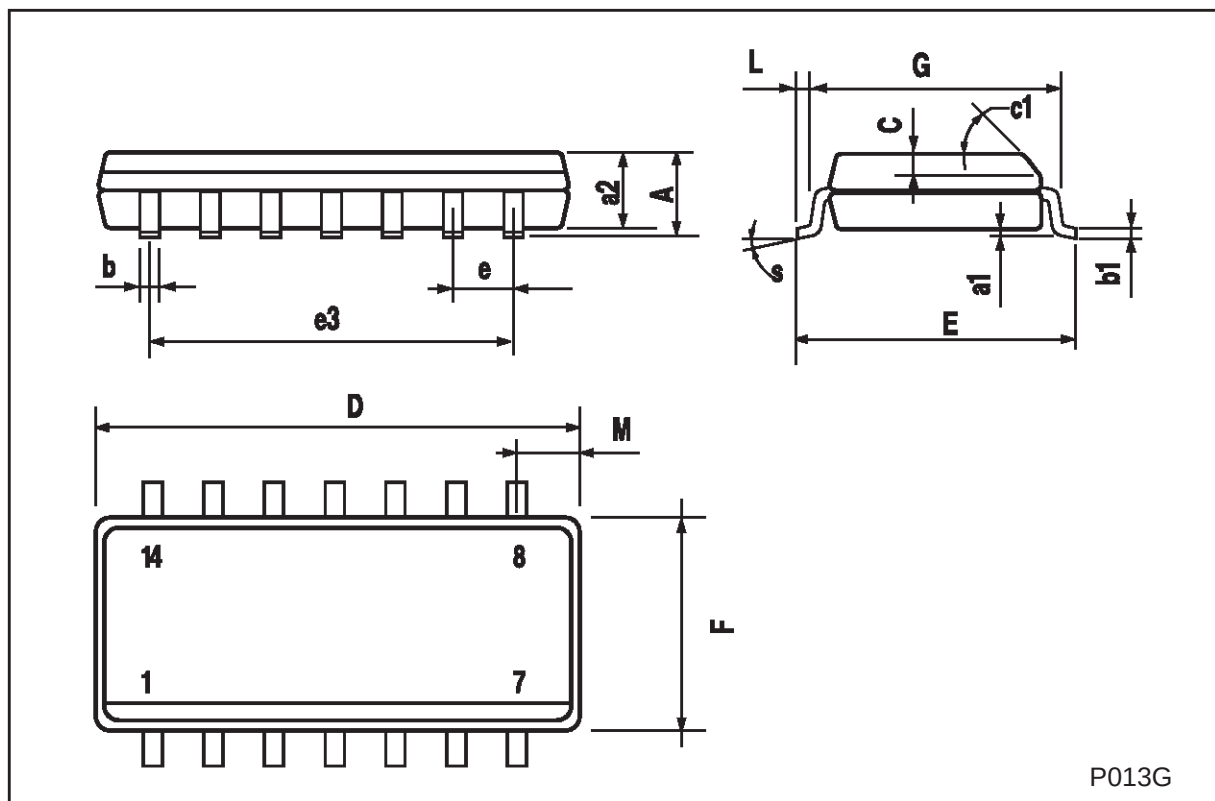


I_{CC} (Opr.)



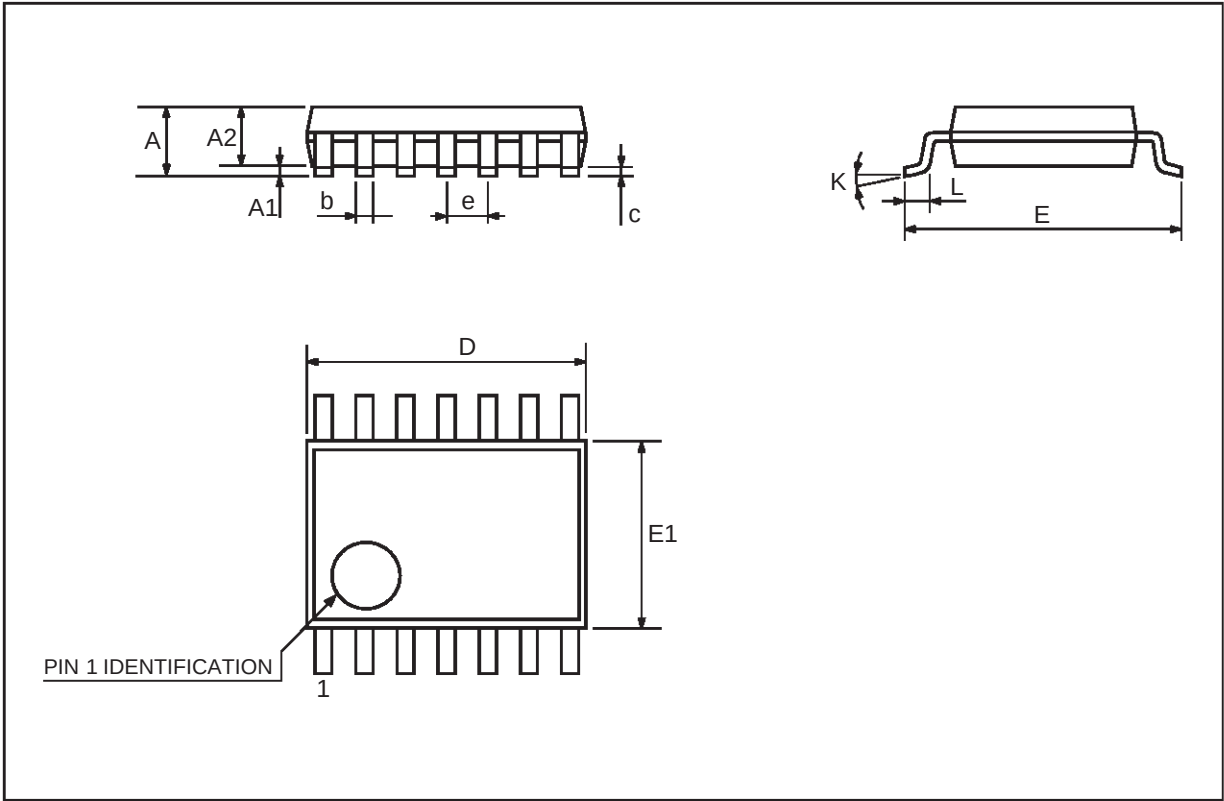
SO-14 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.75			0.068
a1	0.1		0.2	0.003		0.007
a2			1.65			0.064
b	0.35		0.46	0.013		0.018
b1	0.19		0.25	0.007		0.010
C		0.5			0.019	
c1	45 (typ.)					
D	8.55		8.75	0.336		0.344
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		7.62			0.300	
F	3.8		4.0	0.149		0.157
G	4.6		5.3	0.181		0.208
L	0.5		1.27	0.019		0.050
M			0.68			0.026
S	8 (max.)					



TSSOP14 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.1			0.433
A1	0.05	0.10	0.15	0.002	0.004	0.006
A2	0.85	0.9	0.95	0.335	0.354	0.374
b	0.19		0.30	0.0075		0.0118
c	0.09		0.20	0.0035		0.0079
D	4.9	5	5.1	0.193	0.197	0.201
E	6.25	6.4	6.5	0.246	0.252	0.256
E1	4.3	4.4	4.48	0.169	0.173	0.176
e		0.65 BSC			0.0256 BSC	
K	0°	4°	8°	0°	4°	8°
L	0.50	0.60	0.70	0.020	0.024	0.028



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