

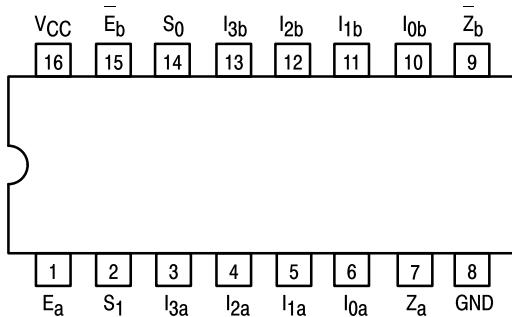


DUAL 4-INPUT MULTIPLEXER

The SN54/74LS352 is a very high-speed Dual 4-input Multiplexer with Common Select inputs and individual Enable inputs for each section. It can select two bits of data from four sources. The two buffered outputs present data in the inverted (complementary) form. The SN54/74LS352 is the functional equivalent of the SN54/74LS153 except with inverted outputs.

- Inverted Version of the SN54/74LS153
- Separate Enables for Each Multiplexer
- Input Clamp Diode Limit High Speed Termination Effects

CONNECTION DIAGRAM DIP (TOP VIEW)

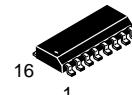
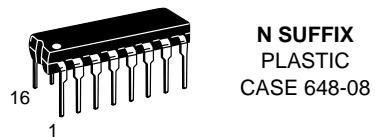
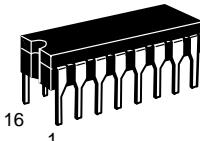


NOTE:
The Flatpak version has the same pinouts (Connection Diagram) as the Dual In-Line Package.

SN54/74LS352

DUAL 4-INPUT MULTIPLEXER

LOW POWER SCHOTTKY



J SUFFIX
CERAMIC
CASE 620-09

N SUFFIX
PLASTIC
CASE 648-08

D SUFFIX
SOIC
CASE 751B-03

PIN NAMES

PIN NAMES	LOADING (Note a)	
	HIGH	LOW
S ₀ , S ₁	Common Select Inputs	0.5 U.L.
E	Enable (Active LOW) Input	0.5 U.L.
I ₀ -I ₁	Multiplexer Inputs	0.5 U.L.
Z	Multiplexer Outputs (note b)	10 U.L. 5 (2.5) U.L.

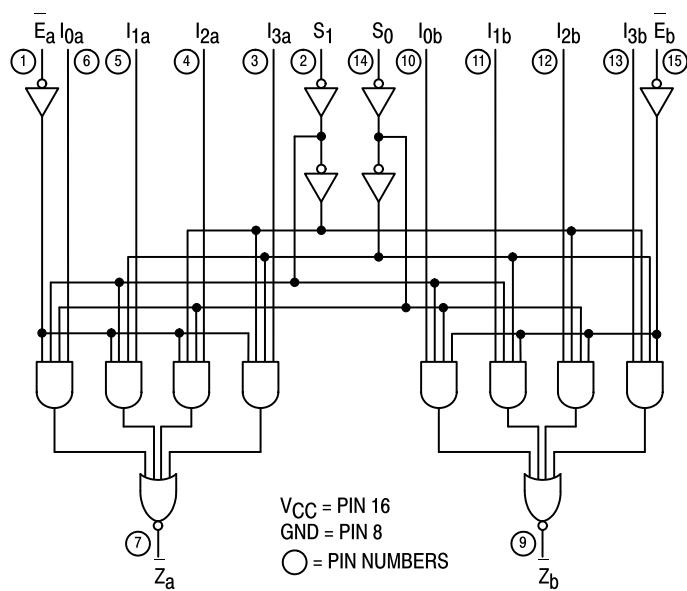
NOTES:

- a) 1 TTL Unit Load (U.L.) = 40 μ A HIGH/1.6 mA LOW.
b) The Output LOW drive factor is 2.5 U.L. for Military (54) and 5 U.L. for Commercial (74) Temperature Ranges.

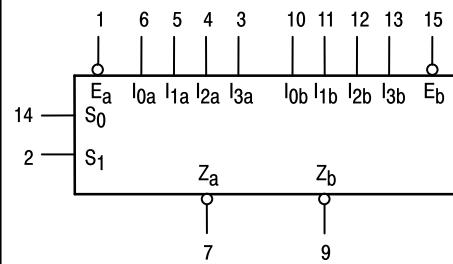
ORDERING INFORMATION

SN54LSXXXJ Ceramic
SN74LSXXXN Plastic
SN74LSXXXD SOIC

LOGIC DIAGRAM



LOGIC SYMBOL



SN54/74LS352

FUNCTIONAL DESCRIPTION

The SN54/74LS352 is a Dual 4-Input Multiplexer. It selects two bits of data from up to four sources under the control of the common Select Inputs (S_0, S_1). The two 4-input multiplexer circuits have individual active LOW Enables (E_a, E_b) which

can be used to strobe the outputs independently. When the Enables (E_a, E_b) are HIGH, the corresponding outputs (Z_a, Z_b) are forced HIGH.

The logic equations for the outputs are shown below.

$$\begin{aligned} \bar{Z}_a &= \bar{E}_a \cdot (\bar{I}_{0a} \cdot \bar{S}_1 \cdot \bar{S}_0 + \bar{I}_{1a} \cdot \bar{S}_1 \cdot S_0 + \bar{I}_{2a} \cdot S_1 \cdot \bar{S}_0 + \bar{I}_{3a} \cdot S_1 \cdot S_0) \\ \bar{Z}_b &= \bar{E}_b \cdot (\bar{I}_{0b} \cdot S_1 \cdot S_0 + \bar{I}_{1b} \cdot S_1 \cdot \bar{S}_0 + \bar{I}_{2b} \cdot \bar{S}_1 \cdot S_0 + \bar{I}_{3b} \cdot \bar{S}_1 \cdot \bar{S}_0) \end{aligned}$$

The SN54/74LS352 can be used to move data from a group of registers to a common output bus. The particular register from which the data came would be determined by the state of the Select Inputs. A less obvious application is a function gen-

erator. The SN54/74LS352 can generate two functions of three variables. This is useful for implementing highly irregular random logic.

TRUTH TABLE

SELECT INPUTS		INPUTS (a or b)				OUTPUT	
S_0	S_1	E	I_0	I_1	I_2	I_3	\bar{Z}
X	X	H	X	X	X	X	H
L	L	L	L	X	X	X	H
L	L	L	H	X	X	X	L
H	L	L	X	L	X	X	H
H	L	L	X	H	X	X	L
L	H	L	X	X	L	X	H
L	H	L	X	X	H	X	L
H	H	L	X	X	X	L	H
H	H	L	X	X	X	H	L

H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Typ	Max	Unit
V_{CC}	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T_A	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
I_{OH}	Output Current — High	54, 74			-0.4	mA
I_{OL}	Output Current — Low	54 74			4.0 8.0	mA

SN54/74LS352

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Symbol	Parameter	Limits			Unit	Test Conditions	
		Min	Typ	Max			
V_{IH}	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V_{IL}	Input LOW Voltage	54		0.7	V	Guaranteed Input LOW Voltage for All Inputs	
		74		0.8			
V_{IK}	Input Clamp Diode Voltage		-0.65	-1.5	V	$V_{CC} = \text{MIN}$, $I_{IN} = -18 \text{ mA}$	
V_{OH}	Output HIGH Voltage	54	2.5	3.5	V	$V_{CC} = \text{MIN}$, $I_{OH} = \text{MAX}$, $V_{IN} = V_{IH}$ or V_{IL} per Truth Table	
		74	2.7	3.5			
V_{OL}	Output LOW Voltage	54, 74		0.25	V	$I_{OL} = 4.0 \text{ mA}$	$V_{CC} = V_{CC} \text{ MIN}$, $V_{IN} = V_{IL}$ or V_{IH} per Truth Table
		74		0.35	V	$I_{OL} = 8.0 \text{ mA}$	
I_{IH}	Input HIGH Current			20	μA	$V_{CC} = \text{MAX}$, $V_{IN} = 2.7 \text{ V}$	
				0.1	mA	$V_{CC} = \text{MAX}$, $V_{IN} = 7.0 \text{ V}$	
I_{IL}	Input LOW Current			-0.4	mA	$V_{CC} = \text{MAX}$, $V_{IN} = 0.4 \text{ V}$	
I_{OS}	Short Circuit Current (Note 1)	-20		-100	mA	$V_{CC} = \text{MAX}$	
I_{CC}	Power Supply Current			10	mA	$V_{CC} = \text{MAX}$	

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

AC CHARACTERISTICS ($T_A = 25^\circ\text{C}$, $V_{CC} = 5.0 \text{ V}$)

Symbol	Parameter	Limits			Unit	Test Conditions
		Min	Typ	Max		
t_{PLH} t_{PHL}	Propagation Delay, Select to Output		19 25	29 38	ns	Figure 1 or 2
t_{PLH} t_{PHL}	Propagation Delay, Enable to Output		16 21	24 32	ns	Figure 2
t_{PLH} t_{PHL}	Propagation Delay, Data to Output		13 17	20 26	ns	Figure 1

AC WAVEFORMS

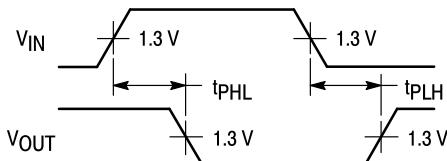


Figure 1

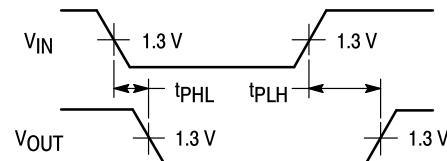
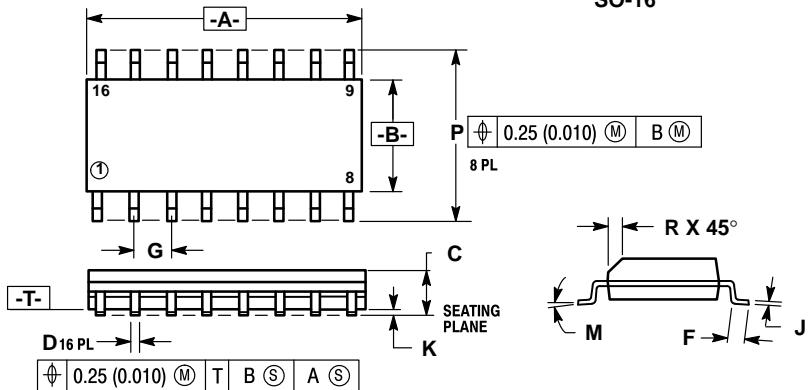


Figure 2

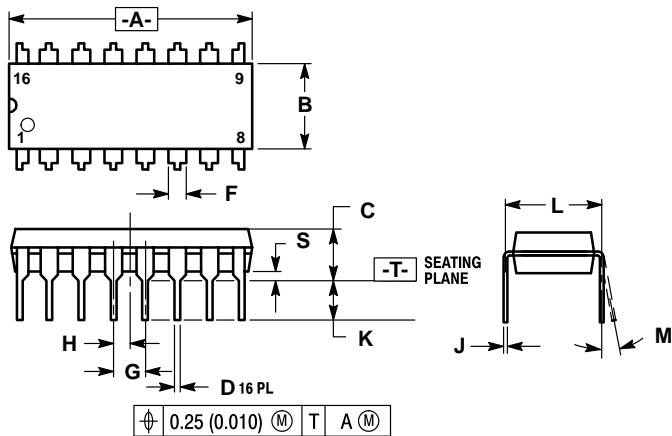
**Case 751B-03 D Suffix
16-Pin Plastic
SO-16**



NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
 5. 751B-01 IS OBSOLETE, NEW STANDARD 751B-03.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.80	10.00	0.386	0.393
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC	0.050 BSC		
J	0.19	0.25	0.008	0.009
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

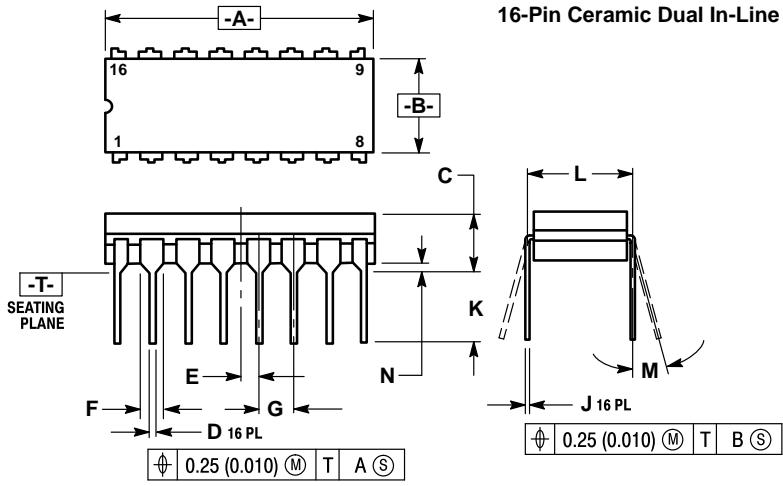
**Case 648-08 N Suffix
16-Pin Plastic**



NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 4. DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
 5. ROUNDED CORNERS OPTIONAL.
 6. 648-01 THRU -07 OBSOLETE, NEW STANDARD 648-08.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.80	19.55	0.740	0.770
B	6.35	6.85	0.250	0.270
C	3.69	4.44	0.145	0.175
D	0.39	0.53	0.015	0.021
F	1.02	1.77	0.040	0.070
G	2.54 BSC	0.100 BSC		
H	1.27 BSC	0.050 BSC		
J	0.21	0.38	0.008	0.015
K	2.80	3.30	0.110	0.130
L	7.50	7.74	0.295	0.305
M	0°	10°	0°	10°
S	0.51	1.01	0.020	0.040

**Case 620-09 J Suffix
16-Pin Ceramic Dual In-Line**



NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
 4. DIM F MAY NARROW TO 0.76 (0.030) WHERE THE LEAD ENTERS THE CERAMIC BODY.
 5. 620-01 THRU -08 OBSOLETE, NEW STANDARD 620-09.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	19.05	19.55	0.750	0.770
B	6.10	7.36	0.240	0.290
C	—	4.19	—	0.165
D	0.39	0.53	0.015	0.021
E	1.27 BSC	0.050 BSC		
F	1.40	1.77	0.055	0.070
G	2.54 BSC	0.100 BSC		
J	0.23	0.27	0.009	0.011
K	—	5.08	—	0.200
L	7.62 BSC	0.300 BSC		
M	0°	15°	0°	15°
N	0.39	0.88	0.015	0.035

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