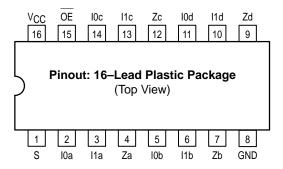
Product Preview Low-Voltage CMOS Quad 2-Input Multiplexer With 5V-Tolerant Inputs and Outputs (3-State, Non-Inverting)

The MC74LCX257 is a high performance, quad 2–input multiplexer with 3–state outputs operating from a 2.7 to 3.6V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A VI specification of 5.5V allows MC74LCX257 inputs to be safely driven from 5V devices.

Four bits of data from two sources can be selected using the Select input. The four outputs present the selected data in the true (non-inverted) form. The outputs may be switched to <u>a high impedance</u> state by placing a logic HIGH on the Output Enable (OE) input. Current drive capability is 24mA at the outputs.

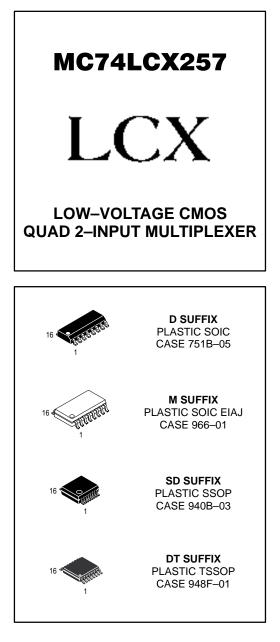
- Designed for 2.7 to 3.6V V_{CC} Operation
- 5V Tolerant Interface Capability With 5V TTL Logic
- Supports Live Insertion and Withdrawal
- IOFF Specification Guarantees High Impedance When V_{CC} = 0V
- LVTTL Compatible
- LVCMOS Compatible
- 24mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10μA) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500mA
- ESD Performance: Human Body Model >2000V; Machine Model >200V



TRUTH TABLE

Inputs				Outputs
OE	S	l0n	l1n	Zn
H L L L	XHHLL	XXXLI	X L H X X	Z L H L H

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.



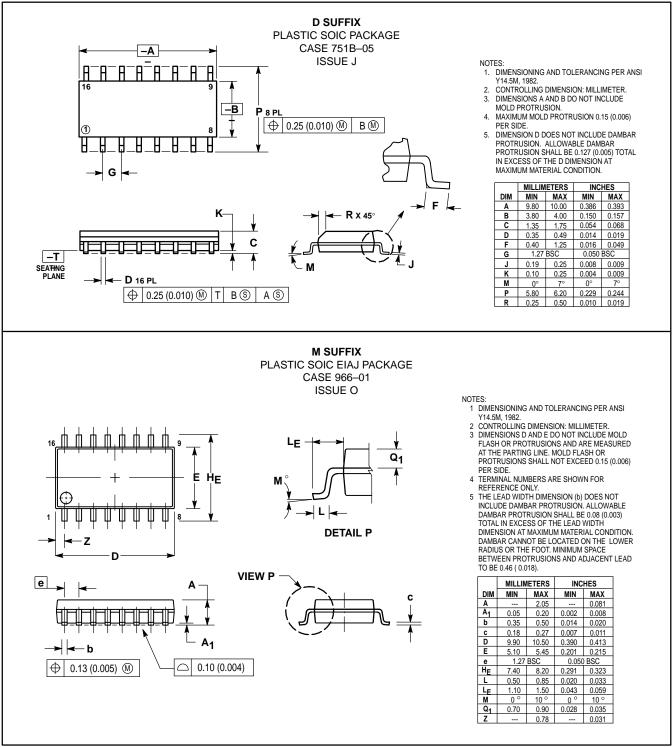
PIN NAMES

Pins	Function
I0n	Source 0 Data Inputs
<u>I1n</u>	Source 1 Data Inputs
OE	Output Enable Input
S	Select Input
Zn	Outputs

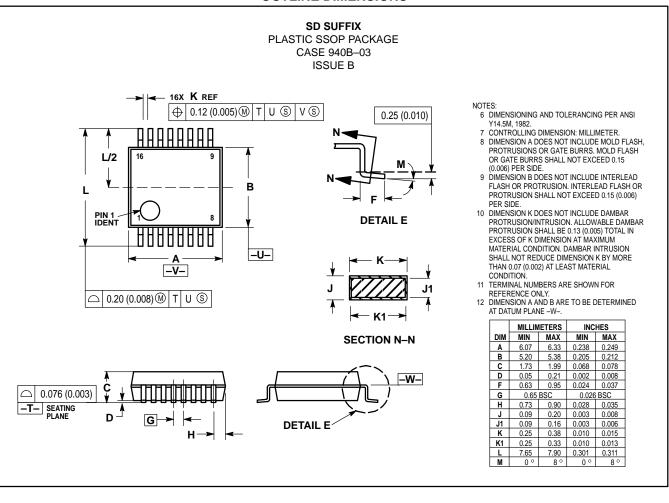


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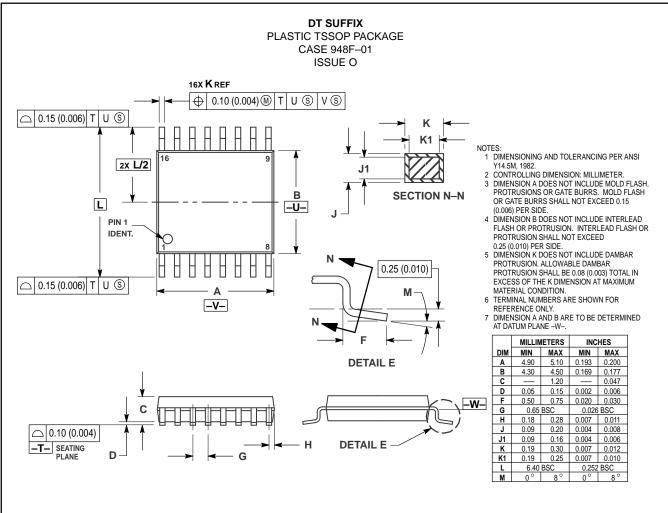
OUTLINE DIMENSIONS



OUTLINE DIMENSIONS



OUTLINE DIMENSIONS



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MC74LCX257/D