

PLEASE CHECK FOR CHANGE INFORMATION AT THE REAR OF THIS MANUAL.

# 7B10 TIME BASE

INSTRUCTION MANUAL

Tektronix, Inc.
P.O. Box 500
Beaverton, Oregon 97077
070-2316-00
Product Group 42

Serial Number:

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## **INSTRUMENT SERIAL NUMBERS**

Each instrument has a serial number on a panel insert, tag, or stamped on the chassis. The first number or letter designates the country of manufacture. The last five digits of the serial number are assigned sequentially and are unique to each instrument. Those manufactured in the United States have six unique digits. The country of manufacture is identified as follows:

B000000	Tektronix, Inc., Beaverton, Oregon, USA
100000	Tektronix Guernsey, Ltd., Channel Islands
200000	Tektronix United Kingdom, Ltd., London
300000	Sony/Tektronix, Japan
700000	Tektronix Holland, NV, Heerenveen, The Netherlands

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# **OPERATORS SAFETY SUMMARY**

The following general safety information applies to all operators and service personnel. Specific warnings and cautions will be found throughout the manual where they apply and should be followed in each instance.

WARNING statements identify conditions or practices which could result in personal injury or loss of life.

CAUTION statements identify conditions or practices which could result in damage to the equipment or other property.

WARNING

#### GROUND THE INSTRUMENT

To reduce electrical-shock hazard, the mainframe (oscilloscope) chassis must be properly grounded. Refer to the mainframe manual for grounding information.

## DO NOT REMOVE INSTRUMENT COVERS

To avoid electric-shock hazard, operating personnel must not remove the protective instrument covers. Component replacement and internal adjustments must be made by qualified service personnel only.

# DO NOT OPERATE IN EXPLOSIVE ATMOSPHERE

To avoid explosion, do not operate this instrument in an explosive atmosphere unless it has been certified for such operation.



#### PREVENT INSTRUMENT DAMAGE

Plug-in units should not be installed or removed without first turning the instrument power off, to prevent instrument damage.

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# SERVICING SAFETY SUMMARY

#### FOR QUALIFIED SERVICE PERSONNEL ONLY

Refer also to the preceeding Operators Safety Summary

The following are safety precautions which appear in the servicing information sections of this manual. This Servicing Safety Summary is in addition to the Operators Safety Summary given previously.

WARNING

#### DO NOT SERVICE ALONE

Do not attempt internal service or adjustment unless another person, capable of rendering first aid and resuscitation, is present.

# DISCONNECT INSTRUMENT POWER

To avoid electric-shock, disconnect the instrument from the power source before removing protective panels, soldering, or replacing components.

#### SILICONE GREASE HANDLING

Handle silicone grease with care. Avoid getting the silicone grease in your eyes. Wash hands thoroughly after use.



# AVOID EXCESSIVE MOISTURE

Circuit boards and components must be dry before applying power to prevent damage from electrical arcing.

### EXERCISE CARE WHEN CHECKING DIODES

When checking diodes, do not use an ohm-meter scale that has a high internal current, since high currents may damage the diodes under test.

# EXERCISE CARE WHEN SOLDERING ON MULTI-LAYER BOARDS

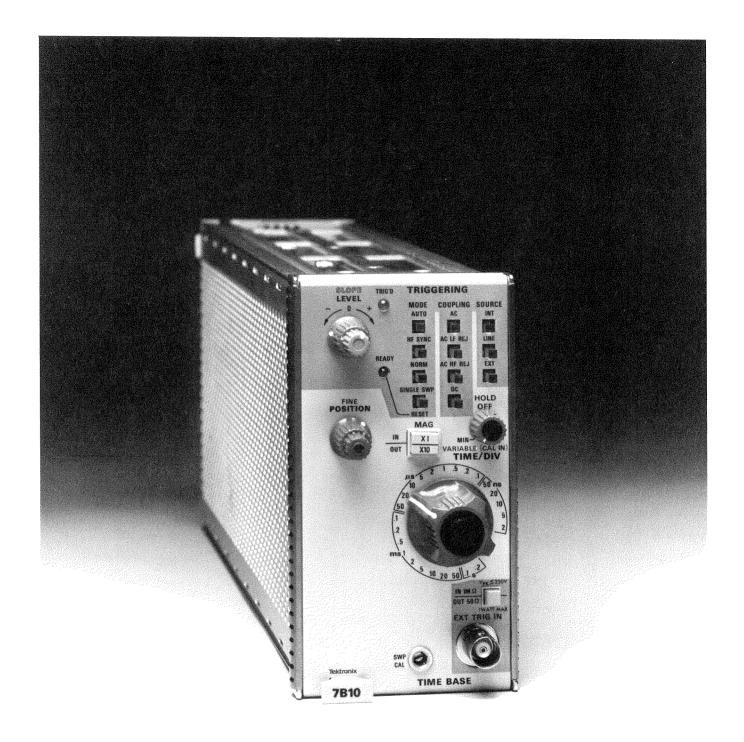
All but the readout circuit board in the instrument are multi-layer type boards with a conductive path laminated between the top and bottom board layers. All soldering on these boards should be done with extreme care to prevent breaking the connections to this center conductor. Only experienced maintenance personnel should attempt repair of these boards.

# USE PROPER CLEANING AGENTS

Avoid the use of chemical cleaning agents which might damage the plastics used in this instrument. Use a non-residue type of cleaner, preferably isopropyl alcohol, totally denatured ethyl alcohol, or Freon TF. Before using any other type of cleaner, consult your Tektronix Service Center or representative.

# DO NOT USE PIN SOCKETS FOR CONNECTION POINTS

The spring tension of the pin sockets ensures a good connection between the circuit board and the pin. This spring tension can be destroyed by using the pin sockets as a connecting point for spring-loaded probe tips, alligator clips, etc.



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# **GENERAL INFORMATION**

### INTRODUCTION

#### **OPERATORS MANUAL**

The Operators Manual contains information necessary to effectively operate the 7B10 Time Base and is divided into three sections: Section 1 provides a basic description of the 7B10 with instrument specifications and accessories, section 2 contains operating information for the instrument, Instrument Option information is located in section 3 of the manual.

To install the unit in a plug-in compartment, push it in until it fits firmly into the compartment. The front panel of the unit should be flush with the front panel of the mainframe. Even though the gain of the mainframe is standardized, the sweep calibration of the unit should be checked when installed. The procedure for checking the unit is given under Sweep Functions in the Operators Checkout procedure in section 2.

To remove the unit, pull the release latch (see Fig. 1-1) to disengage the unit from the mainframe, and pull it out of the plug-in compartment.

# INSTRUCTION MANUAL

The Instruction Manual provides both operating and servicing information for the 7B10 Time Base. The Instruction Manual is divided into nine sections. Operating information is covered in the first two sections: servicing information for use by qualified service personnel is contained in the remaining seven sections of the manual. Schematic diagrams are located at the rear of the manual and can be unfolded for reference while reading other parts of the manual. The reference designators and symbols used on the schematics are defined on the first page of the Diagrams and Circuit Board Illustrations section. All abbreviations used in this manual, with the exception of the parts list and schematic diagrams, comply with the American National Institute Y1.1-1972 publication. The parts lists are computer printouts and use computer-supplied abbreviations. Instrument Option information is located in section 6 of the Instruction Manual.

### INSTALLATION

The time-base unit is designed to operate in the horizontal plug-in compartment of the mainframe. This instrument can also be installed in a vertical plug-in compartment to provide a vertical sweep on the crt. However, when used in this manner, there are no internal triggering or retrace blanking provisions, and the unit may not meet specifications.

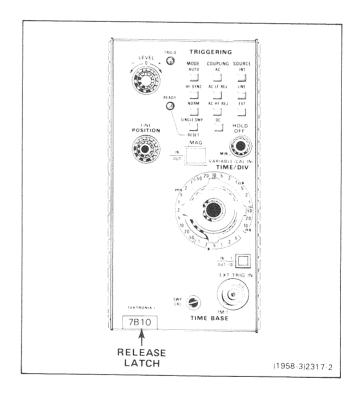


Fig. 1-1. Location of release latch.

# **SPECIFICATION**

This instrument will meet the electrical characteristics listed in Table 1-1, following complete adjustment. The following electrical characteristics are valid over the stated environmental range for instruments calibrated at an ambient temperature of  $\pm 20^{\circ}$  to  $\pm 30^{\circ}$  C, and after a twenty-minute warmup unless otherwise noted.

TABLE 1-1
Electrical Characteristics

Characteristics	Performance Requirement
SV	VEEP GENERATOR
Sweep Rates	
Calibrated Range	0.2 s/div to 2 ns/div in 25 steps. X10 Magnifier external fastest calibrated sweep rate to 0.2 ns/div.
Variable Range	Continuously variable uncalibrated sweep rate to at least 2.5 times the calibrated sweep rate setting.
Sweep Accuracy <sup>1</sup> with 7104, 7900 and 7800 Series Mainframes	Measured over center 8 displayed divisions. $^2$ SWP CAL is adjusted at 1 ms/div within the $+20^\circ$ to $+30^\circ$ C range.
+15° to +35° C	UNMAG MAG X10
0.2 s/div to 10 ns/div	2% 3%
5 ns/div and 2 ns/div	. 3%
0° to +50° C	Derate +15° to +35° C accuracy by additional 1%.
Excluded Portions of Sweep	
7104	None.
7800, 7900 Series	First 5 ns.
Sweep Length	At least 10.2 div.
MAG Registration	0.5 div or less from graticule center when changing from MAG X10 to MAG X1.
Position Range	
POSITION Controls fully cw	Start of sweep must be to right of graticule center at 1 ms/div.
POSITION Controls fully ccw	End of sweep must be left of graticule center at 1 ms/

Some mainframes limit fastest calibrated sweep rate.

<sup>&</sup>lt;sup>2</sup>200 ps/div is measured over any 5 divisions within the center 8 divisions.

### TABLE 1-1 (CONT.) Electrical Characteristics

Characteristic	Performa	ince Requiremer	nt								
	RIGGERING										
Trigger Sensitivity for Repetitive Signals	Triggering Frequency Range <sup>3</sup>	Minimum Triggering Signal Required									
Coupling		Internal	External								
AC	30 Hz to 250 MHz 250 MHz to 1 GHz	0.5 div 1.5 div	50 mV 150 mV								
AC LF REJ⁴	50 kHz to 250 MHz 250 MHz to 1 GHz	0.5 div 1.5 div	50 mV 150 mV								
AC HF REJ	30 Hz to 30 kHz	0.5 div	50 mV								
DC⁵	DC to 250 MHz 250 MHz to 1 GHz	0.5 div 1.5 div	50 mV 150 mV								
Single Sweep	Same as for Repetitive	Triggering.									
Internal Trigger Jitter	30 ps or less at 1 GHz.										
Operating in HF SYNC MODE			417								
AC, AC LF REJ, or DC	250 MHz to 1 GHz – 0.3 div 75 mV External.	250 MHz to 1 GHz – 0.3 div. Internal 75 mV External.									
External Trigger Input											
LEVEL RANGE	At least ±3.5 V (checked	on 1 kHz sine	wave).								
Maximum Safe Input											
1-Megohm Input	250 V (dc plus peak ac)										
50-Ohm Input	1 Watt average.										
Input R and C											
1-Megohm Input	1 MΩ within 5%, 20 pF	within 10%.									
50-Ohm	50 $\Omega$ within 2%.										
Trigger Holdoff Time											
Minimum Holdoff Setting <sup>6</sup>											
0.2 s/div to 50 ms/div	40 ms, or less										
20 ms/div to 2 $\mu$ s/div	2 times TIME/DIV setting, of	or less									
1 $\mu$ s/div to 2 ns/div	2.0 $\mu$ s, or less										
Maximum Holdoff Setting <sup>6</sup>											
0.2 s/div to 50 ms/div	400 ms, or greater										
20 ms/div to 2 $\mu$ s/div	20 times TIME/DIV setting,	or greater									
1 $\mu$ s/div to 0.5 $\mu$ s/div	20.0 μs, or greater										
$0.2~\mu s/div$ to $2~ns/div$	6.0 $\mu$ s, or greater										

<sup>&</sup>lt;sup>3</sup>The triggering frequency ranges given here are limited to the -3 dB frequency of the oscilloscope vertical system when operating in the Internal mode.

<sup>&</sup>lt;sup>4</sup>Will not trigger on sine waves at or below 60 Hz when amplitudes are less than 8 divisions Internal or 3 volts External.

<sup>&</sup>lt;sup>5</sup>The triggering frequency range for DC COUPLING applies to frequencies above 30 Hz when operating in the AUTO TRIGGERING MODE <sup>6</sup>Performance requirement not checked in the manual.

TABLE 1-2

# **Environmental Characteristics**

Refer to the Specification section of the associated mainframe manual.

TABLE 1-3

Physical Characteristics

Characteristic	Information
Net Weight	Approximately 2.6 pounds (1.2 kilogram).
Dimensions	See Figure 1-2, dimensional drawing.

# STANDARD ACCESSORIES

•	l ea	,			 •	 	٠	 ٠	 	٠.		•	 		 	 ٠.	 			 ٠.	 	 			 ٠.	٠.	٠.	 	٠.	Operators	6	Man	ual	,
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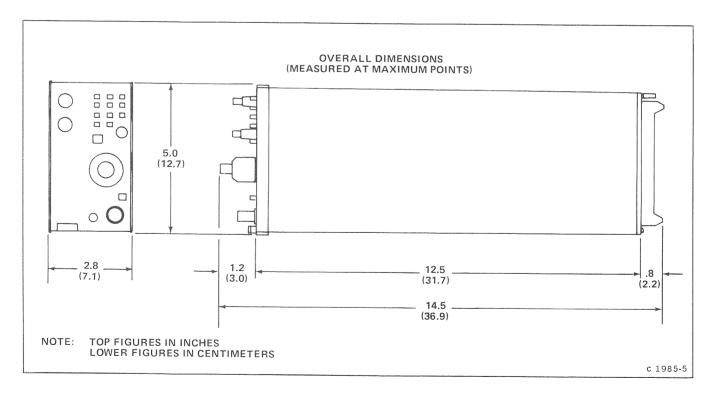


Fig. 1-2. 7B10 dimensional drawing.

# **OPERATING INSTRUCTIONS**

The 7B10 Time-Base unit operates with a Tektronix 7100, 7700, 7800, or 7900-series oscilloscope mainframe and a 7A-series amplifier unit to form a complete oscilloscope system. This section describes the operation of the front-panel controls and connectors, provides detailed operating information, an operators checkout procedure, and basic applications for this instrument.

# CONTROLS, CONNECTORS, AND INDICATORS

All controls, connectors, and indicators required for the operation of the time-base unit are located on the front panel. Figure 2-1 provides a brief description of all frontpanel controls, connectors, and indicators. More detailed information is given in the Detailed Operating Information.

## **OPERATORS CHECKOUT**

The following procedures are provided for checking basic instrument functions. Refer to the description of the controls, connectors, and indicators while performing this procedure. If performing the operators checkout procedure reveals a malfunction or possible maladjustment, first check the operation of the associated plug-in units, then refer to the instruction manual for maintenance and adjustment procedures.

#### SETUP PROCEDURE

- 1. Install the time-base unit being checked in the A horizontal compartment of the mainframe.
- 2. Install an amplifier plug-in unit in a vertical compartment.
- 3. Set the time-base unit controls as follows:

SLOPE (+)	)
MODE AUTO	
COUPLINGAC	
SOURCE INT	
POSITIONMidrange	,
TIME/DIV 1 ms	
VARIABLE (CAL IN)	
(Pushed in)	)
HOLD OFF MIN (fully	,
counterclockwise)	1
MAG X1 (pushed in)	į
EXT TRIG IN OUT 50 O	,

- 4. Turn on the mainframe and allow at least 20 minutes warmup.
- 5. Set the mainframe vertical and horizontal modes to display the plug-in units used and adjust the intensity and focus for a well-defined display. See the oscilloscope mainframe and amplifier unit instruction manuals for detailed operating instructions.

#### SWEEP FUNCTIONS

#### Normal Sweep

Perform the following procedure to obtain a normal sweep and to demonstrate the function of the related controls:

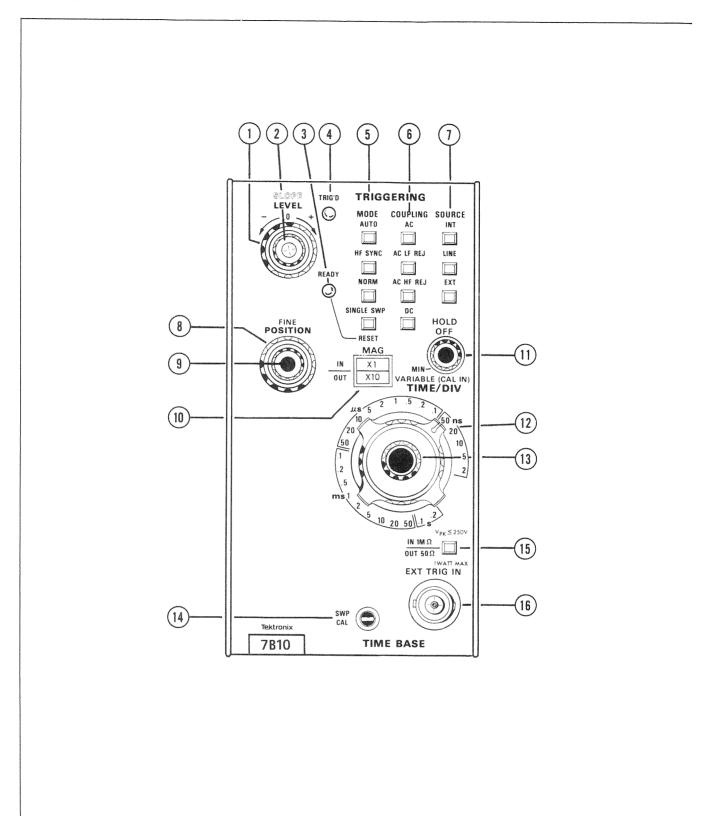
- 1. Perform the preceding Setup Procedure.
- 2. Connect a 0.4-volt, 1-kilohertz signal from the mainframe calibrator to the amplifier unit input.
- 3. Set the amplifier unit deflection factor for 4 divisions of display.
- 4. Adjust the LEVEL control for a stable display.
- 5. Turn the POSITION control and note that the trace moves horizontally.
- 6. Turn the FINE control and note that the display can be precisely positioned horizontally.
- 7. Check the display for one complete cycle per division. If necessary, adjust the front-panel SWP CAL screwdriver adjustment for one complete cycle per division over the center 8 graticule divisions. Be sure that the timing of the mainframe calibrator signal is accurate to within 0.25%  $(+20^{\circ}\ to\ +\ 30^{\circ}\ C)$ .
- 8. Press to release the VARIABLE (CAL IN) control. Turn the VARIABLE (CAL IN) control fully counterclockwise and note that the displayed sweep rate changes to at least the next slower TIME/DIV switch setting (i.e., 2 milliseconds/division). Press the VARIABLE (CAL IN) knob in to the calibrated position.

### Magnified Sweep

Perform the following procedure to obtain a X10 magnified display and to demonstrate the function of the related controls:

- 1. Obtain a one cycle per division display as described in the preceding Normal Sweep procedure.
- 2. Press to release the MAG button (X10). Note that the unmagnified display within the center division of the graticule is magnified to about 10 divisions.
- 3. Press the MAG button (X1).

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Fig. 2-1. Front-panel controls, connectors, and indicators.

#### **TRIGGERING**

- $ig( \ 1 ig)$  LEVEL Control—Selects a point on the trigger signal where triggering occurs.
- (2) SLOPE Switch—Permits sweep to be triggered on negative or positive-going portions of the trigger signal.
- $\left(\,3\,
  ight)$  READY Indicator—Illuminates when sweep circuit is armed (SINGLE SWP Mode).
- (4) TRIG'D Indicator-Illuminates when the display is triggered.
- $oxed{5}$  MODE Pushbuttons—Selects the operating mode of the triggering circuit.
- $ig( oldsymbol{\mathfrak{f}} ig)$  COUPLING Pushbuttons—Selects the method of coupling the trigger signal to triggering circuit.
- (7) SOURCE Pushbuttons—Selects source of the trigger signal.

#### **SWEEP**

- $\left( \ 8 \ 
  ight)$  POSITION Control—Provides horizontal positioning.
- $\left( \ {rac{9}{3}} 
  ight)$  FINE Control—Provides precise horizontal positioning.
- ig(10ig) MAG Pushbutton—Selects magnified X10 or unmagnified sweep.
- 11 HOLD OFF Control—Permits hold off period to be varied to improve trigger stability on repetitive complex waveforms.
- $\left(12
  ight)$  TIME/DIV Selector—Selects the sweep rate of the sweep generator.
- (13) VARIABLE Control and CAL Switch—Selects calibrated or uncalibrated sweep rates. Uncalibrated sweep rates can be continuously reduced to at least the sweep rate of the next slower position.
- $\left(14
  ight)$  SWP CAL Adjustment—Compensates for basic timing changes due to the differences in sensitivity of mainframes.

## EXTERNAL TRIGGER INPUT

- (15) EXT TRIG Button—Selects input impedance.
- (16) EXT TRIG IN Connector—Connector (BNC type) provides input for external trigger signals.

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Fig. 2-1 (cont.). Front-panel controls, connectors, and indicators.

#### TRIGGERING FUNCTIONS

Perform the following procedure to obtain a triggered sweep and to demonstrate the functions of the related controls:

- 1. Obtain a display as described in the preceding Normal Sweep procedure.
- 2. Turn the LEVEL control fully counterclockwise to obtain a free-running sweep.
- 3. Slowly turn the HOLD OFF control clockwise and note that a stable display can be obtained at several positions of the HOLD OFF control. Return the HOLD OFF control to the fully counterclockwise (MIN) position.

#### NOTE

The HOLD OFF control varies the sweep holdoff time which effectively changes the repetition-rate of the horizontal sweep signal. However, its primary function is to obtain a stable display of complex waveforms which are otherwise difficult to trigger.

- 4. Press the AC, AC HF REJ, and DC COUPLING buttons for both the + and positions of the SLOPE switch and check for a stable display (LEVEL control may be adjusted, if necessary, to obtain a stable display).
- 5. Apply the 0.4-volt, 1 kilohertz signal from the mainframe calibrator to the amplifier unit and to the EXT TRIG IN connector.
- 6. Press the EXT SOURCE button and set the amplifier unit deflection factor for a 4-division display.
- 7. Press the AC, AC HF REJ, and DC COUPLING buttons for both the + and positions of the SLOPE switch and check for a stable display (LEVEL control may be adjusted, if necessary).
- 8. Press the AC COUPLING, INT SOURCE, and NORM MODE buttons. Adjust the LEVEL control for a stable display.
- 9. Press the AUTO MODE button and adjust the LEVEL control for a free-running display.
- 10. Press the NORM MODE button and check for no display.
- 11. Adjust the LEVEL control for a stable display and press the SINGLE SWP MODE button.
- 12. Note that one trace occurs when the SINGLE SWP button is pressed again.
- 13. Disconnect the mainframe calibrator signal from the amplifier unit input and press the SINGLE SWP button. Check for no display and note that the READY indicator is lit.

14. Note that one trace occurs and that the READY indicator extinguishes when the mainframe calibrator signal is reconnected to the amplifier unit input.

# DETAILED OPERATING INFORMATION

#### TRIGGERING SWITCH LOGIC

The MODE, COUPLING, and SOURCE push buttons of the TRIGGERING switches are arranged in a sequence which places the most-often used position at the top of each vertical row of push buttons. With this arrangement, a stable display can usually be obtained by pressing the top push buttons: AUTO, AC, INT. When an adequate trigger signal is applied and the LEVEL control is correctly set. the unit is triggered as indicated by the illuminated TRIG'D light. If the TRIG'D light is not on, the LEVEL control is either at a setting outside the range of the trigger signal applied to this unit from the vertical unit, the trigger signal amplitude is inadequate, or its frequency is below the lower frequency limit of the AC COUPLING switch position. If the desired display is not obtained with these buttons pushed in, other selections must be made. Refer to the following discussions or the instruction manuals for the associated oscilloscope mainframe and vertical unit(s) for more information.

#### TRIGGERING MODE

The MODE push-button switches select the mode in which the sweep is triggered.

#### Auto

The AUTO MODE provides a triggered display with the correct setting of the LEVEL control whenever an adequate trigger signal is applied (see Trigger Level discussions). The TRIG'D light indicates when the display is triggered.

When the trigger repetition rate is outside the frequency range selected by the COUPLING switch or the trigger signal is inadequate, the sweep free runs at the rate indicated by the TIME/DIV switch (TRIG'D indicator off). An adequate trigger signal ends the free-running condition and a triggered display is presented. The sweep also free runs at the rate indicated by the TIME/DIV switch when the LEVEL control is at a setting outside the amplitude range of the trigger signal. This type of free-running display is useful when it is desired to measure only the peak-to-peak amplitude of a signal without observing the waveshape (such as bandwidth measurements).

### HF Sync

The HF SYNC mode provides a triggered display with the correct setting of the LEVEL control whenever a high frequency, (100 MHz or higher) low amplitude signal is applied. This mode is useful when the incoming signal is too small to produce stable triggering in the AUTO or NORMAL modes. The HF SYNC mode increases trigger sensitivity and provides automatic trigger amplifier centering for optimum triggering under these conditions.

(a)

#### Normal

The NORM MODE provides a triggered display with the correct settings of the LEVEL control whenever an adequate trigger signal is applied. The TRIG'D light indicates when the display is triggered.

The normal trigger mode must be used to produce triggered displays with trigger repetition rates below about 30 hertz. When the TRIG'D light is off, no trace is displayed.

#### Single Sweep

When the signal to be displayed is not repetitive or varies in amplitude, waveshape, or repetition rate, a conventional repetitive type display may produce an unstable presentation. Under these circumstances, a stable display can often be obtained by using the single-sweep feature of this unit. The single-sweep mode is also useful to photograph non-repetitive or unstable displays.

To obtain a single-sweep display of a repetitive signal, first obtain the best possible display in the NORM MODE. Then, without changing the other TRIGGERING controls. press the SINGLE SWP RESET button. A single trace is presented each time this button is pressed. Further sweeps cannot be presented until the SINGLE SWP RESET button is pressed again. If the displayed signal is a complex waveform composed of varying amplitude pulses, successive single-sweep displays may not start at the same point on the waveform. To avoid confusion due to the crt persistence, allow the display to disappear before pressing the SINGLE SWP RESET button again. At fast sweep rates, it may be difficult to view the singlesweep display. The apparent trace intensity can be increased by reducing the ambient light level or by using a viewing hood as recommended in the mainframe instruction manual.

When using the single-sweep mode to photograph waveforms, the graticule may have to be photographed separately in the normal manner to prevent over exposing the film. Be sure the camera system is well protected against stray light, or operate the system in a darkened room. For repetitive waveforms, press the SINGLE SWP RESET button only once for each waveform unless the signal is completely symmetrical. Otherwise, multiple waveforms may appear on the film. For random signals, the lens can be left open until the signal triggers the unit. Further information on photographic techniques is given in the appropriate camera instruction manual.

### TRIGGERING COUPLING

The TRIGGERING COUPLING push buttons select the method in which the trigger signal is connected to the trigger circuits. Each position permits selection or rejection of some frequency components of the signal which triggers the sweep.

### **Alternating Current**

AC COUPLING blocks the dc component of the trigger signal. Signals with low-frequency components below about 30 hertz are attenuated. In general, AC COUPLING

can be used for most applications. However, if the signal contains unwanted frequency components or if the sweep is to be triggered at a low repetition rate or do level, one of the other COUPLING switch positions will provide a better display.

# Alternating Current Low Frequency Rejection

AC LF REJ COUPLING rejects dc, and attenuates low-frequency trigger signals below about 50 kilohertz. Therefore, the sweep is triggered only by the higher-frequency components of the trigger signal. This position is particularly useful for providing stable triggering if the trigger signal contains line-frequency components. Also, the AC LF REJ position provides the best alternate-mode vertical displays at fast sweep rates when comparing two or more unrelated signals.

## Alternating Current High Frequency Rejection

AC HF REJ COUPLING passes all low-frequency signals between about 30 hertz and 30 kilohertz. Dc is rejected and signals outside the above range are attenuated. When triggering from complex waveforms, this position is useful to provide a stable display of the low-frequency components.

#### **Direct Current**

DC COUPLING can be used to provide stable triggering from low-frequency signals which would be attenuated in the other COUPLING switch positions. DC COUPLING can be used to trigger the sweep when the trigger signal reaches a dc level set by the LEVEL control. When using internal triggering, the setting of the vertical unit position control affects the triggering point.

### TRIGGERING SOURCE

The TRIGGERING SOURCE push buttons select the source of the trigger signal which is connected to the trigger circuits.

#### Internal

The INT position connects the trigger signal from the vertical plug-in unit. Further selection of the internal trigger signal may be provided by the vertical plug-in unit or by the mainframe; see the instruction manuals for these instruments for more information. For most applications, the internal source can be used. However, some applications require special triggering which cannot be obtained in the INT position. In such cases, the LINE or EXT positions of the SOURCE switches must be used.

#### Line

The LINE position connects a sample of the power-line voltage from the mainframe to the trigger circuit. Line triggering is useful when the input signal is time-related (multiple or submultiple) to the line frequency. It is also useful for providing a stable display of a line-frequency component in a complex waveform.

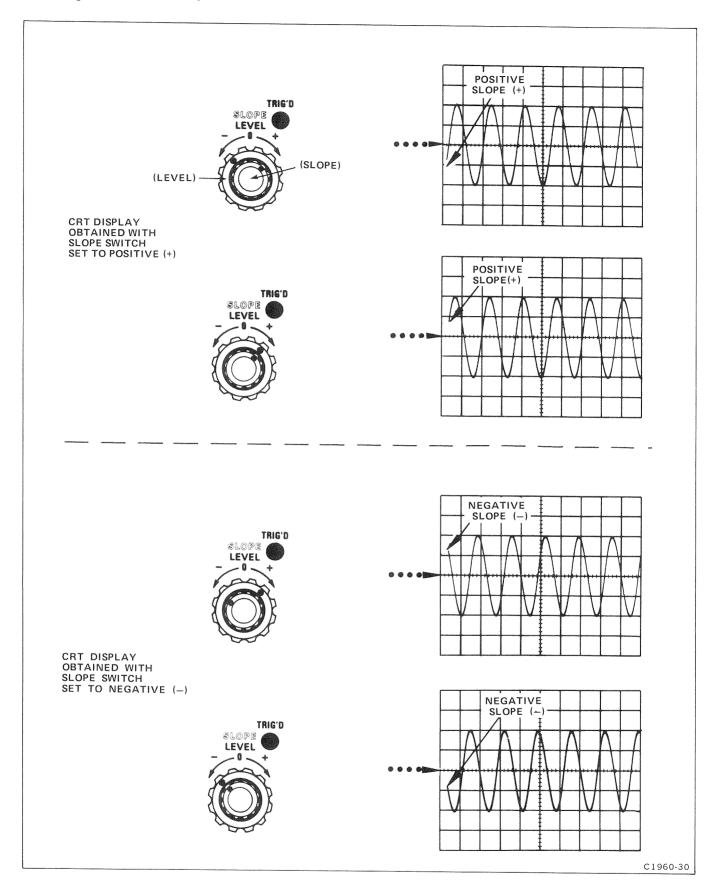


Fig. 2-2. Effect of LEVEL control and SLOPE switch on crt display.

#### External

The EXT position connects the signal from the EXT TRIG IN connector to the trigger circuit. The external signal must be time-related to the displayed waveform for a stable display. An external trigger signal can be used to provide a triggered display when the internal signal is either too low in amplitude for correct triggering or contains signal components on which triggering is not desired. It is also useful when signal tracing in amplifiers, phase-shift networks, wave-shaping circuits, etc. The signal from a single point in the circuit can be connected to the EXT TRIG IN connector through a probe or cable. The sweep is then triggered by the same signal at all times and allows amplitude, time relationship, or waveshape changes of signals at various points in the circuit to be examined without resetting the TRIGGERING controls

The IN 1 M $\Omega/O$ UT 50  $\Omega$  pushbutton provides a convenient means of selecting external-trigger input impedance. Pushing the button in sets the amplifier input impedance to 1 M $\Omega$  and the OUT position provides 50  $\Omega$  input impedance.

#### TRIGGERING SLOPE

The TRIGGERING SLOPE switch (concentric with the TRIGGERING LEVEL control) determines whether the trigger circuit responds on the positive- or negative-going portion of the trigger signal. When the SLOPE switch is in the (+) (positive-going) position, display starts on the positive-going portion of the waveform (see Figure 2-2). When several cycles of a signal appear on the display the setting of the SLOPE switch is often unimportant. However, if only a certain portion of a cycle is to be displayed, correct setting of the SLOPE switch is important to provide a display that starts on the desired slope of the input signal.

### TRIGGERING LEVEL

The TRIGGERING LEVEL control determines the voltage level on the trigger signal at which the sweep is triggered. When the LEVEL control is set in the + region, the trigger circuit responds at a more positive point on the trigger signal. When the LEVEL control is set in the region, the trigger circuit responds at a more negative point on the trigger signal. Figure 2-2 illustrates this effect with different settings of the SLOPE switch.

To set the LEVEL control, first select the TRIGGERING MODE, COUPLING, SOURCE, and SLOPE. Then set the LEVEL control fully counterclockwise and rotate it clockwise until the display starts at the desired point.

In the HF SYNC mode, the trigger LEVEL control varies the sensitivity of the Trigger Generator. The LEVEL control is set to provide a stable display.

### HORIZONTAL SWEEP RATES

The TIME/DIV switch provides calibrated sweep rates from .2 seconds/division to 2 nanosecond/division in a 1-2-5 sequence. The VARIABLE TIME/DIV control must be in the calibrated position and the MAG switch set to X1 to obtain the sweep rate indicated by the TIME/DIV switch. However, the mainframe crt readout will display the appropriate sweep rate.

The VARIABLE TIME/DIV control includes a two-position switch to determine if the sweep rate is calibrated, or uncalibrated. When the VARIABLE control is pressed in, it is inoperative and the sweep rate is calibrated. When pressed and released outward, the VARIABLE control is activated for uncalibrated sweep rates, to at least the sweep rate of the next slower position.

A calibrated sweep rate can be obtained in any position of the VARIABLE control by pressing in the VARIABLE control. This feature is particularly useful when a specific uncalibrated sweep rate has been obtained and it is desired to switch between calibrated and uncalibrated displays.

#### TIME MEASUREMENTS

When making time measurements from the graticule, the area between the second and tenth vertical lines of the graticule provides the most linear time measurements (see Fig. 2-3). Position the start of the timing area to the second vertical line and adjust the TIME/DIV switch so the end of the timing area falls between the second and tenth vertical lines.

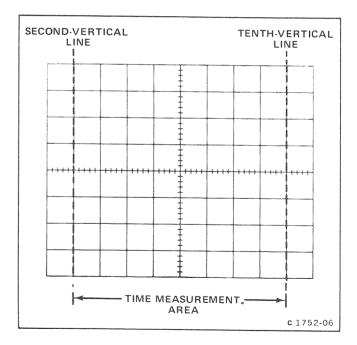


Fig. 2-3. Area of graticule used for most accurate time measurements.

#### SWEEP MAGNIFICATION

The sweep magnifier can be used to expand the display be a factor of 10. The center division of the unmagnified display is the portion visible on the crt in the magnified form (see Fig. 2-4). The equivalent length of the magnified sweep is more than 100 divisions; any 10 division portion can be viewed by adjusting the POSITION and FINE POSITION controls to bring the desired portion into the viewing area. When the MAG switch is set to X10 (OUT) the equivalent magnified sweep rate can be determined by dividing the TIME/DIV setting by 10; the equivalent magnified sweep rate is displayed on the crt readout.

#### VARIABLE HOLD OFF

The HOLD OFF control improves triggering stability on repetitive complex waveforms by effectively changing the repetition rate of the horizontal sweep signal. The HOLD OFF control should normally be set to its minimum setting. When a stable display cannot be obtained with the TRIGGERING LEVEL control, the HOLD OFF control can be carried for an improved display. If a stable display cannot be obtained at any setting of the LEVEL and HOLD OFF controls, check the TRIGGERING COUPLING and source switch settings.

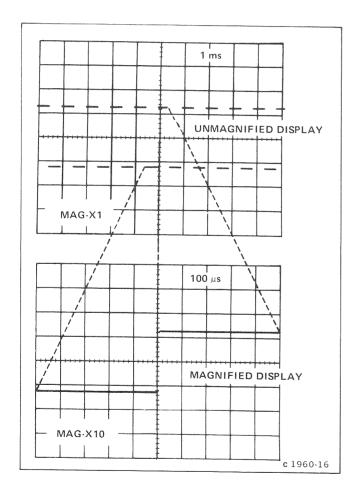


Fig. 2-4. Operation of sweep magnifier.

# MAINFRAME OPERATING MODES

The time-base unit can be operated either as an independent time base in any Tektronix 7100-, 7700-, 7800-, or 7900-series oscilloscope mainframes, or as a delayed-sweep unit in those mainframes that have two horizontal compartments. A companion delaying time base unit is required for delayed-sweep operation. Refer to the delaying time-base unit instruction manual for additional information.

### **APPLICATIONS**

The following information describes procedures and techniques for making basic time measurements with the time-base unit installed in a Tektronix 7100, 7700, 7600, or 7900-series oscilloscope. These procedures provide enough detail to enable the operator to adapt them to other related time measurements. Contact your Tektronix Field Office or representative for assistance in making measurements that are not described in this manual.

## TIME-INTERVAL MEASUREMENTS

Since time is a function of the sweep rate and the horizontal distance (in divisions) that the sweep travels across the graticule in a calibrated-sweep oscilloscope system, the time interval between any two points on a waveform can be accurately measured. The following procedures provide methods to measure some of the more common time-related definable characteristics of a waveform such as period, frequency, rise time, fall time, and pulse width. The procedure for each of these measurements is essentially the same, except for the points between which the measurements are made. The time interval between any two selected points on a displayed waveform can be measured with basically the same technique.

# PERIOD AND FREQUENCY MEASUREMENTS

Perform the following procedure to measure the period and determine the frequency of a displayed waveform:

- 1. Install the time-base unit in a mainframe horizontal compartment (either A or B horizontal in a four-compartment mainframe).
- 2. Connect the signal to be measured to the vertical unit input.
- 3. Set the mainframe horizontal- and vertical-mode switches to display the time base and vertical units. (Check that the time base VARIABLE (CAL IN) control is pushed in and the HOLD OFF control is in the MIN position.)
- 4. Set the TRIGGERING switches and LEVEL control for a stable display (see General Operating Information for selecting proper triggering).

2-8

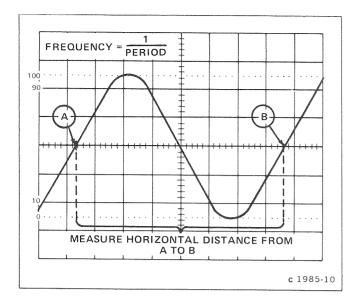


Fig. 2-5. Measuring the period and determining the frequency of a displayed waveform.

- 5. Set the vertical deflection factor and position control for about a 5-division display, vertically centered on the graticule.
- 6. Set the TIME/DIV switch and POSITION controls for a complete cycle displayed within the center 8 graticule divisions as shown in Figure 2-5.
- 7. Measure the horizontal distance in divisions over 1 complete cycle of the displayed waveform.
- 8. Multiply the horizontal distance measured in Step 7 by the TIME/DIV switch setting. (Divide the answer by 10 if sweep magnification is used.)

Example: Assume that the horizontal distance over 1 complete cycle is 7 divisions, and the TIME/DIV switch setting is .1 ms (see Fig. 2-5).

Using the formula:

$$\mbox{Period} = \frac{\mbox{Horizontal distance} \quad \mbox{TIME/DIV}}{\mbox{Magnification}}$$

Substituting values:

Period = 
$$\frac{7 \times 0.1 \text{ ms}}{1}$$
 = 0.7 millisecond

9. Determine the frequency of the displayed waveform obtained in steps 1 through 8 by taking the reciprocal of the period of 1 cycle.

Example: Assume that the period of the displayed waveform is 0.7 millisecond.

Using the formula:

Frequency = 
$$\frac{1}{\text{period}}$$

Substituting values:

Frequency = 
$$\frac{1}{0.7 \text{ ms}}$$
 = 1.43 kilohertz

## RISE-TIME AND FALL-TIME MEASUREMENTS

Perform the following procedure to measure the rise time and fall time of a displayed waveform:

- 1. Install the time-base unit in a mainframe horizontal compartment (either A or B horizontal in a four-compartment mainframe).
- 2. Connect the signal to be measured to the vertical unit input.
- 3. Set the mainframe horizontal- and vertical-mode switches to display the time base and the vertical unit. (Check that the time base VARIABLE (CAL IN) control is pushed in and the HOLD OFF control is in the MIN position.)
- 4. Set the TRIGGERING switches and LEVEL control for a stable display (see General Operating Information for selecting proper triggering).
- 5. Set the vertical deflection factor and position controls for a vertically-centered display with an exact number of divisions of amplitude.
- 6. Set the TIME/DIV switch and POSITION control to display the rising or falling portion of the waveform within the center 8 graticule divisions as shown in Figure 2-6 (see General Operating Information in this section for discussion of timing measurement accuracy).
- 7. Determine rise time or fall time by measuring the horizontal distance in divisions between the point on the rising or falling portion of the waveform that is 10% and the point that is 90% of the total display amplitude (see Fig. 2-6).

### NOTE

The left edge of the oscilloscope graticule is scribed with 0, 10, 90, and 100% lines for convenience when measuring rise time or fall time. To use this feature, adjust the vertical deflection factor and position controls to fit the display between the 0 and 100% graticule lines. Then measure the horizontal distance between the points where the waveform crosses the 10% and 90% graticule lines.

8. Multiply the horizontal distance measured in step 7 by the TIME/DIV switch setting. (Divide the answer by 10 if sweep magnification is used.)

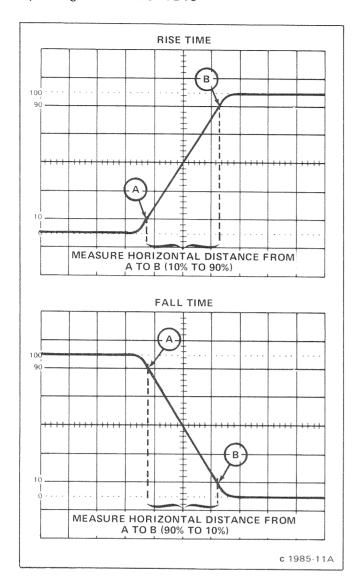


Fig. 2-6. Measuring the rise time and fall time of a displayed waveform.

Example: Assume that the horizontal distance between the 50% amplitude points is 3 divisions, and the TIME/DIV switch setting is .1  $\mu$ s (see Fig. 2-6).

Using the formula:

Rise Time = 
$$\frac{\text{Horizontal distance}}{\text{(divisions)}} \times \frac{\text{TIME/DIV}}{\text{setting}}$$

$$\frac{\text{Magnification}}{\text{Magnification}}$$

Substituting values:

Rise Time = 
$$\frac{2.5 \times 0.1 \,\mu s}{1}$$
 = 0.25 microsecond

# PULSE WIDTH MEASUREMENTS

Perform the following procedure to measure the pulse width of a displayed waveform:

- 1. Install the time-base unit in a mainframe horizontal compartment (either A or B horizontal in a four-compartment mainframe).
- 2. Connect the signal to be measured to the vertical unit input.
- 3. Set the mainframe horizontal- and vertical-mode switches to display the time base and vertical unit. (Check that the time base VARIABLE (CAL IN) control is pushed in and the HOLD OFF control is in the MIN position.)
- 4. Set the TRIGGERING switches and LEVEL control for a stable display (see General Operating information for selecting proper triggering).
- 5. Set the vertical deflection factor and position control for about a 5-division pulse vertically centered on the graticule.
- 6. Set the TIME/DIV switch and POSITION control for 1 complete pulse displayed within the center 8 graticule divisions as shown in Figure 2-7.
- 7. Measure the horizontal distance in divisions between the 50% amplitude points of the displayed pulse (see Fig. 2-7.).
- 8. Multiply the horizontal distance measured in step 7 by the TIME/DIV switch setting. (Divide the answer by 10 if sweep magnification is used.)

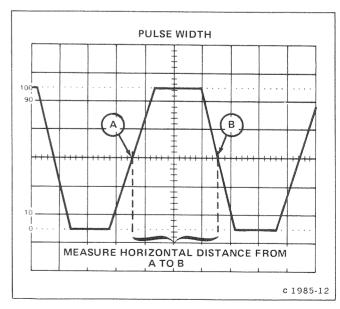


Fig. 2-7. Measuring the pulse width of a displayed waveform.

Example: Assume that the horizontal distance between the B amplitude points is 3 divisions, and the TIME/DIV switch setting is .1 ms (see Fig. 2-7).

Using the formula:

$$Pulse \ Width \ = \ \frac{ \ \ \ \ \ \ \, \frac{ \ \ \ \, Magnification }{ \ \ \, Magnification }$$

Substituting values:

Pulse Width = 
$$\frac{3 \times 0.1 \text{ ms}}{1}$$
 = 0.3 millisecond

# **DELAYED-SWEEP MEASUREMENTS**

The time-base unit may be used with a delaying time-base unit in a mainframe with two horizontal compartments to make delayedsweep measurements. See the Tektronix Products Catalog for compatible delaying time-base plug-in units. If a compatible delaying time-base unit is available, refer to the instruction manual for that unit for detailed delayed-sweep measurement procedures.

# WARNING

THE FOLLOWING SERVICING INSTRUCTIONS ARE FOR USE BY QUALIFIED PERSONNEL ONLY. TO AVOID PERSONAL INJURY, DO NOT PERFORM ANY SERVICING OTHER THAN THAT CONTAINED IN OPERATING INSTRUCTIONS UNLESS YOU ARE QUALIFIED TO DO SO. REFER TO OPERATORS SAFETY SUMMARY AND SERVICE SAFETY SUMMARY PRIOR TO PERFORMING ANY SERVICE.



# THEORY OF OPERATION

This section of the manual describes the circuitry used in the 7B10 Time-Base unit. The description begins with a discussion of the instrument, using the block diagram shown in Figure 3-1. The schematic diagrams at the rear of this manual are blocked off according to circuit function. These circuit block titles serve as indexes to the circuit diagram discussion. Refer to the appropriate diagram along with the Troubleshooting Chart in the Diagrams and Circuit Board Illustrations section throughout the following discussion.

# **BLOCK DIAGRAM DESCRIPTION**

The following discussion is provided to aid in understanding the overall concept of the time-base unit before the individual circuits are discussed in detail. A basic block diagram is shown in Figure 3-1. The numbered diamond in each block refers to the corresponding circuit diagram at the rear of this manual.

## TRIGGER GENERATOR

The Trigger Generator ensures a stable display by starting each sweep at the same point on the waveform. Circuitry is included for selection of trigger mode, coupling, and source. The output of the Trigger Generator is a fast-rise gate which enables the Sweep Generator.

#### SWEEP GENERATOR

The sweep sawtooth signal is initiated when the Trigger Generator output is applied to the Sweep Generator. The rate of change (slope) of the sawtooth signal is determined by the TIME/DIV switch setting. The sawtooth signal provides horizontal deflection for the mainframe (oscilloscope). The Sweep Generator also generates a Sweep Gate pulse which unblanks the crt in the mainframe.

## LOGIC

The Logic circuits control the sweep modes and associated functions of the time-base unit (e.g., auto sweep, single sweep, hold off, etc.). The Logic circuits also generate the control signals for the mainframe.

# DETAILED CIRCUIT DESCRIPTION

The operation of circuits to this instrument is described in detail in this discussion. Circuits commonly used in the electronics industry are not described in detail.

# LOGIC FUNDAMENTALS

Digital logic techniques are used to perform many functions within this instrument. The function and operation of the logic circuits are described using logic symbology and terminology. This portion of the manual is provided to be an aid in the understanding of these symbols and logic concepts, not a comprehensive discussion of the subject.

#### **SYMBOLS**

The symbols used to describe digital circuits in this instrument are based on ANSI standard Y32.14-1973. Table 3-1 provides a basic reference for the logic devices used within this instrument. Any deviations from the

standard symbology, or devices not defined by the standard, are described in the circuit description for the applicable device.

#### NOTE

Logic symbols used on the diagrams depict the logic function as used in this instrument, and may differ from the manufacturer's data.

### LOGIC POLARITY

All logic functions are described using the positive logic convention. Positive logic is a system of notation where the more positive of two levels (HI) is called the true or 1-state; the more negative level (LO) is called the false or 0-state. The HI-LO method of notation is used in this description. The specific voltages that constitute a HI or LO state may vary between individual devices. Wherever possible, the input and output lines are named to indicate the function performed when at the HI (true) state.

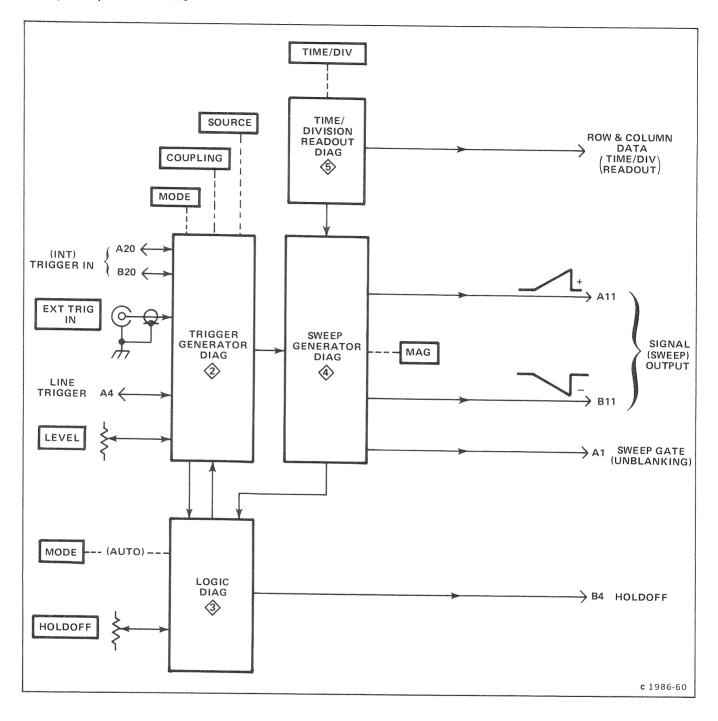


Fig. 3-1. Basic block diagram of the 7B10 Time Base unit.

## INPUT/OUTPUT TABLES

Input/Output (truth) tables are used to show the input combinations important to a particular function, along with the resultant output conditions. This table applies either to an individual device or to a complete logic stage. For examples of input/output tables for individual devices, see Table 3-1.

## NON-DIGITAL DEVICES

Not all of the integrated circuits in this instrument are digital logic devices. The function of nondigital devices is described individually, using operating waveforms or other techniques to illustrate the function.

**3-2** @

TABLE 3-1
Basic Logic Reference

A B X LO LO LO LO HI HI HI HI LO HI HI HI HI LO HI HI HI HI HI LO HI HI HI HI LO HI HI HI HI HI LO HI HI HI HI LO  OR gate  A device with two or more inputs and one output. The output of the NAND gate is LO if and only if all of the inputs are at the HI state.  OR gate  A device with two or more inputs and one output. The output of the OR gate is HI if one or more of the inputs are at the HI state.	Device	Symbol	Description	Input/Outpu	t Table
A B X LO LO LO HI LO HI LO LO HI LO HI HI HI HI  NAND gate  A device with two or more inputs and one output. The output of the NAND gate is LO if and only if all of the inputs are at the HI state.  OR gate  A device with two or more inputs and one output. The output of the NAND gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the OR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state is always opposite to the input state is always opposite to the input state.	AND gate		•	Input	/Output
The inputs are at the HI state.  A device with two or more inputs and one output. The output of the NAND gate is LO if and only if all of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NAND gate is LO if and only if all of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the OR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output of the NOR gate is LO if one or more of the input state is always opposite to the			,	АВ	X
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NAND gate  A device with two or more inputs and one output. The output of the NAND gate is LO if and only if all of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the OR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the OR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state is always opposite to the input state.		В		LO HI	LO
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NAND gate is LO if and only if all of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.	NAND gate		·	Input	Output
OR gate  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.				АВ	Х
A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.		1		LO LO	ні
A device with two or more inputs and one output. The output of the OR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the Input are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.					НІ
A device with two or more inputs and one output. The output of the OR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.				<del> </del>	
A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.				HI HI	LO
OR gate is HI if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  Inverter  A device with one input and one output. The output of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.	OR gate		1	Input	/Output
NOR gate  A device with two or more inputs and one output. The output of the inputs are at the HI state.  A device with two or more inputs and one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.				АВ	Х
NOR gate  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.			-	LO LO	LO
NOR gate  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.		1		LO HI	HI
NOR gate  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with two or more inputs and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A device with one input and one output. The output state is always opposite to the input state.  A device with one input and one output. The output state is always opposite to the input state.					HI
and one output. The output of the NOR gate is LO if one or more of the inputs are at the HI state.  A B X LO LO HI LO A CO HI LO HI LO HI LO HI LO A CO HI LO HI L				HI HI	HI
NOR gate is LO if one or more of the inputs are at the HI state.    A	NOR gate			Input	/Output
the inputs are at the HI state.  LO LO HI LO HI LO HI LO HI HI LO  Inverter  A device with one input and one output. The output state is always opposite to the input state.  A X			-	АВ	Х
Inverter  A device with one input and one output. The output state is always opposite to the input state.  LO HI L			-	LO LO	НІ
Inverter  A device with one input and one output. The output state is always opposite to the input state.  A X		В		LO HI	LO
Inverter  A device with one input and one output. The output state is always opposite to the input state.  A X					
output. The output state is always opposite to the input state.				HI HI	LO
A opposite to the input state.	Inverter		*	Input /	Output
LO HI		A		Α	X
			opposite to the hiper state.	LO	НІ
HI LO				HI	LO

TABLE 3-1 (CONT.)
Basic Logic Reference

Device	Symbol	Description	Input/Output Table
LO-state indicator	<u> </u>	A small circle at the input or output of a symbol indicates that the LO state is the significant state. Absence of the circle indicates that the HI state is the significant state. Two examples follow:	
	A — C X	AND gate with LO-state indicator at the A input.  The output of this gate is HI if and only if the A input is LO and the B input is HI.	Input Output  A B X  LO LO LO  LO HI HI  HI LO LO  HI HI LO
	A	OR gate with LO-state indicator at the A input:  The output of this gate is HI if either the A input is LO or the B input is HI.	Input Output  A B X LO LO HI LO HI HI HI LO LO HI HI HI
Dynamic Indicator	<b>→</b>	Indicates that this input (usually the trigger input of a flip-top) re- sponds to the indicated transition of the applied signal.	
Triggered (toggle) Flip-Flop	о Т <sup>FF</sup> <u> </u>	A bistable device with one input and two outputs (either or both outputs may be used). When triggered, the outputs change from one stable state to the other stable state with each trigger. The outputs are complementary (i.e., when one output is HI the other is LO). The dynamic indicator on the trigger (T) input may be of either polarity depending on the device.	Input / Output  Condition Condition after trigger trigger pulse  Q Q Q Q Q  LO HI HI LO  HI LO LO HI

TABLE 3-1 (CONT.) Basic Logic Reference

Device	Symbol	Description	Input/Output Table
Triggered Set-Clear (J-K) Flip-Flop	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	A bistable device with three or more inputs and two outputs (either or both outputs may be used). When gated, the outputs change state in response to the states at the inputs prior to the trigger. The outputs are complementary (i.e., when one output is HI the other is LO). The dynamic indicator on the gate (G) input may be of either polarity depending on the device.	Input Output  J K Q Q  LO LO No change  LO HI LO HI  HI LO HI LO  Changes state  Output conditions shown after gate pulse.
D (data) Type Flip-Flop with Direct Inputs  (Direct Inputs may be applied to all triggered flip-flops)	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	A bistable device with two inputs and two outputs (either or both outputs may be used). When clocked, the state of the Q output changes to the state at the data (D) input. The outputs are complementary (e.g., when one output is HI the other is LO). The dynamic indicator on the clock input may be of either polarity, depending upon the device.	Set (S) and reset (R) inputs override data (D) and clock (C) inputs.  Input / Output  S R Q Q  HI LO LO HI  LO LO Undefined  HI HI No Effect  LO HI HI LO
		For devices with set (S) or reset (R) inputs, the indicated state at either of these inputs overrides all other inputs to the states shown in the Input/Output Table.	Set (S) and Reset (R) inputs both high.  Input Output  Condition Condition before after clock pulse pulse  D Q Q HI HI LO LO LO HI



# FRONT-PANEL WIRING

The Front-Panel Wiring diagram shows the interconnections between front-panel functions (controls, connectors, and indicators) and circuit boards within this instrument.



### TRIGGER GENERATOR

The Trigger Generator provides a stable display by starting the Sweep Generator (diagram 4) at a selected point on the input waveform. The triggering point can be varied by the LEVEL control and may be on either the positive or negative slope of the waveform. The triggering signal source may be from either the signal being displayed (INT), a signal from an external source (EXT), or a sample of the power-line voltage (LINE).

## EXTERNAL TRIGGER AMPLIFIER

The external trigger signal is connected to the Trigger Generator through EXT TRIG IN connector J90. Pushbutton switch S90 selects either 1 M $\Omega$  or 50  $\Omega$  input impedance. The 0.2 amp fuse protects the 50  $\Omega$  load from signal overload.

Pushing in the EXT push button of SOURCE switch (\$50) allows external trigger signals to pass from the External Trigger Amplifier to U124 for amplification. Field-effect transistor Q98 conducts in the DC COUPLING mode only and Q108 is on in all coupling modes except AC LF REJ. Components U110, Q114, and Q118 compose an operational amplifier whose gain is approximately 1/4 determined by feedback resistors R93 and R74. Integrated circuit U110 provides dc stability. Diodes CR74, CR76, and CR77 protect the trigger amplifier from possible overload caused by high amplitude external signals. A portion of the dc leveling voltage from R67 is applied to U110 to provide additional level range in the EXT triggering mode. Pressing the INT button causes Q118 to saturate which interrupts signal flow to the external amplifier of U124.

When the AC HF REJ button is pressed, signals in the frequency range of 5 Hz to 30 kHz pass from U64A to U64B and then to U124's Level Input. Also, U124's Ext In amplifier is disabled and U124's low-frequency amplifier is enabled, allowing only filtered signals from U64B to be amplified.

# TRIGGER AMPLIFIER AND SOURCE SELECTOR

The time base trigger source is selected by the SOURCE switch (S50) which enables the appropriate amplifier in U124. A dc voltage from the LEVEL control is applied to pins 6 and 8 of U124 to provide internal amplifier voltage

offset. The amplifiers' outputs are summed and applied to pins 14 and 16 to drive the following Trigger Generator stage.

The internal trigger signal from the trigger source selector of the mainframe is connected to U12 via interface connector pins A20 and B20. Integrated circuit U12 provides common mode rejection for frequencies up to 100 kHz, T1 provides cmr above 100 kHz. Ac-coupled trigger signals enter U124 at pin 3 and are terminated in  $50~\Omega$  at pin 4. Integrated circuit U38 provides a path for low-frequency trigger signals which are fed to pin 5 of U64B. These signals then arrive at the level port of U124 (pin 6) where they are summed with the high-frequency signals to provide wide-band triggering in the AC and DC COUPLING modes. Pressing the front-panel AC LF REJ push button breaks the low-frequency signal path allowing only high-frequency trigger signals to appear at the output of U124. When the AC HF REJ push button is pressed, the internal signal amplifier is disabled and the low-frequency amplifier is enabled allowing only lowfrequency signals to pass.

In the HF SYNC mode, the output of U138A drives the level input of U124. A voltage appears at the output of U138A when a voltage difference is detected at its input. Thus, when an unbalanced trigger condition occurs, U138A provides automatic dc leveling of U124 so that U144 will always have a balanced signal input in the HF SYNC mode. Transistor Q142 is turned off in the HF SYNC mode providing a path through CR145 for voltage from the LEVEL control. This enables the LEVEL control to adjust the hysteresis of Trigger Generator U144 to almost zero, causing very small signals to trigger U144.

# SLOPE SELECTOR AND TRIGGER GENERATOR

Integrated circuit U144 converts the differential trigger signal from the Trigger Amplifier and Source Selector block to a differential gate waveform for use by the Gate Generator stage.

SLOPE switch S140 is connected to U144-pin 3 to determine whether the display is triggered on the positive-going or negative-going slope. When the SLOPE switch is set to +, a positive-going signal on pin 5 produces a positive-going gate on pin 15 and a negative-going gate on pin 16. When the SLOPE switch is set to a negative-going signal on pin 5 produces a positive-going gate on pin 15 and a negative-going gate on pin 16. Trigger Generator sensitivity is controlled by R147.

The delay mode control input at U144-pin 4 provides control when the unit is operating as a delayed sweep unit in the B horizontal compartment of a mainframe with 2 horizontal compartments. When the unit is operating in the independent or triggerable after delay time modes (as determined by the delaying sweep time-base unit in the A horizontal compartment), there is no effect on the Trigger Generator circuits. However, when the unit is operating in the B starts after delay time mode, U144-pin 4 is HI, causing the trigger disable signal at pin 2 to initiate a trigger gate pulse at U144-pins 15 and 16.

#### **GATE GENERATOR**

The Gate Generator stage provides an auto enable gate to the Logic circuits (diagram 3), and a Sweep Start Gate and Z Axis Gate (unblanking) to the Sweep Generator circuit (diagram 4). Refer to Figure 3-2 for a timing diagram of the Gate Generator functions.

When an adequate trigger signal is applied to U144-pins 5 and 8 and when U144 is enabled (pin 2 is LO), a HI level is produced at U144-pin 15 and a LO level is produced at U144-pin 16.

The HI level from U144-pin 15 is coupled through emitter follower Q184 and J200-2 into the Logic circuit (diagram 3) to indicate that a triggering signal has been received. The Logic circuit (diagram sets the Auto Drive at J200-3 HI, turning off Q160. Simultaneously, the LO level at U144-pin 16 gates comparator Q174-Q164. The collector of Q164 rises HI to provide a sweep start gate at J200-5 and the collector of Q174 falls LO to provide a Z-Axis gate (unblanking) at J200-4.

In the absence of a trigger signal at U144-pins 5 and 8, pin 15 is set LO and pin 16 is set HI. The LO level from U144-pin 15 is coupled through J200-2 to the Logic circuit (diagram to indicate the absence of a triggering signal. The Logic circuit provides a LO-level auto drive pulse through J200-3 to the base of Q160. This LO level gates the comparator (Q160 and Q174). The collector of Q160 rises high to provide a sweep start gate and the collector of Q174 falls LO to provide a Z-Axis Gate (unblanking) at J200-4.



The Logic circuit controls the sweep modes and associated functions of the time-base unit (e.g., sweep display, hold off, auto sweep, single sweep, etc). The Logic circuit also generates control signals for the mainframe

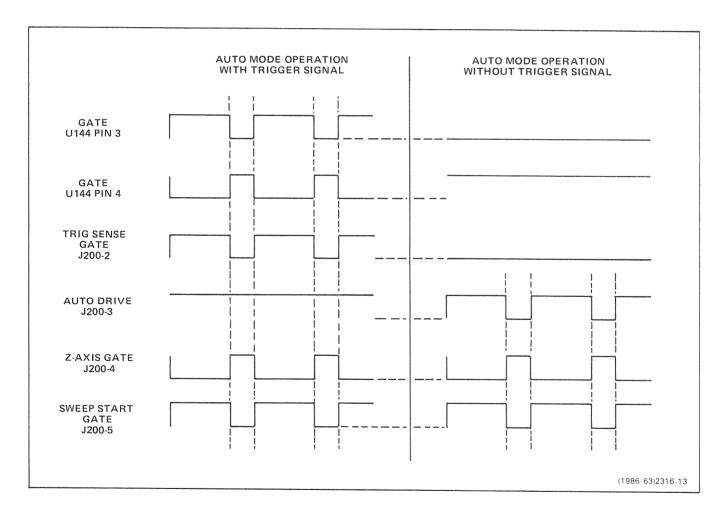


Fig. 3-2. Timing diagram for Gate Generator stages Q160, Q164, and Q174.

#### TRIGGER MODE SWITCHING

Integrated circuit U220 controls the NORM, AUTO, and SINGLE SWP MODE and also generates control signals used in the 10. HF SYNC operation is described in the Trigger Generator circuit description (diagram 2).

#### Normal Mode

The NORM MODE is provided when U220-pin 12 is LO. In the NORM MODE, only an appropriate trigger signal can initiate a sweep gate to the Ramp Generator (diagram 4). Integrated circuit U220 controls sweep lockout and hold off functions.

## Auto Mode

An internal control stage (within U220) produces a freerunning reference trace (bright base line) in the absence of a trigger signal. The HF SYNC mode defaults logic to the auto mode.

A HI level from MODE switch S230 is inverted by Q230 to U220-pin 19 LO, which selects AUTO MODE operation. In the presence of a trigger pulse from the Trigger Generator (diagram 2), a HI level at U220-pin 1 discharges an internal control stage which inhibits the auto sense signal from U220-pin 3. In the absence of a trigger pulse, the LO level at U220-pin 1 enables this internal control stage with a time delay generated by R228, C228, and other circuitry internal to U220-pin 2. After the time delay, an auto sense signal is initiated from U220-pin 3 to the Trigger Generator (diagram 2).

#### Single Sweep Mode

The SINGLE SWP MODE provides display of only one sweep. After one sweep has run, all other sweeps are inhibited until the SINGLE SWP-RESET push button is pressed. The READY light indicates when the sweep is ready to accept a trigger.

After completion of one sweep, the hold off start pulse at U220-pin 16 causes the sweep disable out at pin 17 to rise HI. A HI level at U220-pin 12 initiates single-sweep operation and holds the sweep disable out at U220-pin 17 HI after completion of the sweep. Momentary contact of the RESET push button places a LO at U220-pins 14 and 15, which removes the sweep disable out from pin 17 and allows the Ramp Generator (diagram 4) to accept a trigger. Interface connector B15 provides a remote single-sweep reset input from compatible mainframes.

### HOLD OFF TIMING

The hold off stages prevent the Ramp Generator (diagram 4) from being retriggered until the sweep timing capacitors are discharged.

At the end of each sawtooth waveform from the Ramp Generator (diagram 4), a sweep stop comparator pulse (HI) is coupled to U220-pin 16. This pulse enables the hold off timing circuits at U220-pin 8, which sets the sweep disable out at U220-pin 17 HI and the hold off signal at pin 10 LO for the duration of the hold off cycle. Hold off timing (U220-pin 8) is provided by capacitors

C212 through R215 and resistors R212 through R14. Transistors Q203 and Q204 prevent the sweep disable out pulse at U220-pin 17 from falling LO until the holdoff timing capacitors have discharged. Transistors Q210 and Q214 and front-panel HOLD OFF control R210 provide variable current to the timing components to change the hold off time period.

#### LOCKOUT BUFFER AMPLIFIER

A lockout pulse (HI) may be initiated at interface connector pin B8 by mainframe switching functions. A HI level, coupled from interface connector B8 through the Lockout Buffer Amplifier (Q201, Q202, and Q206) to the lockout input at U220-pin 18, initiates a sweep disable pulse at U220-pin 17 thereby disabling the sweep. The lockout pulse (HI) is also applied through Q358 (diagram 4) to the hold off start input at U220-pin 16 to enable the hold off cycle.

#### HOLD OFF OUTPUT AMPLIFIER

The Hold Off Output Amplifier inverts and amplifies the hold off signal from U220-pin 10 for use by the mainframe.

Transistor Q272 inverts the hold off signal from U220-pin 10 to provide a HI level when hold off is present. The inverted signal is coupled through emitter follower Q274 to interface connector B4.



The Sweep Generator produces a linear ramp waveform for the mainframe when gated by the Trigger Generator. A sweep gate (unblanking) is also generated in this circuit block.

The linear sweep ramp waveform is produced by charging a capacitor from a constant current source. The slope of the ramp determines the sweep rate of the displayed trace.

## TIMING CURRENT SOURCE

The Timing Current Source stages generate a constant current for the Ramp Generator stages. A reference voltage source is established by the +50 volt supply and R306, R305, R304, and R300 (front-panel SWP CAL adjustment).

The reference voltage is applied to operational amplifier U314 which provides unity voltage gain and low output impedance. The output of U314 is connected through Q322 and Q324 to the timing resistors (R392 through R399 and R328). Timing current is the result of the voltage drop across the timing resistors and flows through the collector of Q324 to the Ramp Generator stages.

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#### RAMP GENERATOR

The Ramp Generator stages produce a linear positivegoing ramp for the Output Preamplifier and Sweep Gate Generator stages.

Upon the arrival of a HI-level sweep start gate, Q354 turns on and Q356 turns off. The source current from Q324 charges the timing capacitors (C364, C365, C366) in a positive ramp. Field effect transistors Q372A, Q372B, and transistor Q376 form a unity-gain ramp voltage follower for the sweep ramp. The output of Q376 is connected to the Output Preamplifier, Sweep Gate Generator, and Auxiliary Sweep Preamplifier stages.

When the sweep start gate is LO, Q354 turns off and Q356 turns on causing the timing capacitors (C364, C365, and C366) to discharge. Transistors Q336 and Q342 maintain a constant level from which the ramp begins. The output of Q376 is compared (by way of Q336A) with the reference level at the base of Q336B. If the output of Q376 is less than the reference, Q342 will charge the timing capacitors through CR345 until the output and reference voltages are equal. If the output of Q376 is greater than the reference, Q342 conducts more and CR345 conducts less causing the timing capacitors to discharge through Q356 and R358. When the output and reference voltages are equal, the current through CR345 and Q354 equal the current through Q356.

#### **OUTPUT PREAMPLIFIER**

The Output Preamplifier stages connect the differential sweep signal to the mainframe and provide an offset voltage for trace positioning. Provisions are made in these stages for sweep magnification, and a negative-going sawtooth signal is supplied to the mainframe for sawtooth output and special plug-in unit functions.

The sweep ramp voltage from Q376 is coupled to the Output Preamplifier stage at the base of Q454. Transistors Q454 and Q464 form a single-ended to pushpull converter with Q458 and Q468 as current follower stages for the push-pull signal. Transistor Q460 is employed as a nonlinear capacitance to compensate for the nonlinear collector to base capacitance of Q458. Output drivers Q476 and Q496 provide final amplification and connect the sweep signal to the mainframe.

The MAG switch, S460, increases the Output Preamplifier gain ten times by connecting R461 and R460 in parallel with R454 and R464. In the 2 ns and 5 ns TIME/DIV switch positions field effect transistor Q484 is biased into a low resistance state setting the gain of the Output Preamplifier at two times its normal value.

Operational amplifier U386 combines the dc voltages of the FINE and POSITION controls to produce a position voltage level at its output. This voltage level on the base of Q464 provides a ramp waveform offset voltage to horizontally position the displayed trace.

#### AUXILIARY SWEEP PREAMPLIFIER

The Auxiliary Sweep Preamplifier stage provides a negative-going sweep ramp to the mainframe (via interface connector pins-A3 and B3) for sawtooth output and special plug-in unit functions. Transistors Q434 and Q438 form a unity-gain inverting amplifier for the sawtooth signal from the ramp voltage follower Q376. Diode CR434 provides emitter-base compensation.

### SWEEP GATE GENERATOR

The Sweep Gate Generator produces an unblanking gate for the Z-axis system of the mainframe. When the sweep is displayed, the crt is unblanked (gate level LO). The sweep is blanked (gate level HI) between sweeps.

The sweep ramp is applied to the base of Q402. A comparison voltage is set at the base of Q406. When the ramp voltage exceeds the comparison voltage, Q402 turns off and Q406 couples a HI level through commonbase transistor Q410. The output of Q410 is coupled to Q415, Q420, Q425 and to the hold off start U220 (diagram 3) to initiate hold off.

The Z-axis gate from the Trigger Generator circuit (diagram 2) is LO at the start of the sweep. This LO level turns off Q420. The resultant HI-level sweep gate pulse at the collector of Q420 is coupled through emitter follower Q425 to the mainframe for sweep unblanking. At the end of the sweep, the HI level from the collector of Q410 turns Q415 off and Q420 on. The resultant LO is coupled through emitter follower Q425 to the mainframe for sweep blanking.



# TIME/DIVISION AND READOUT SWITCHING

The Readout circuits provide information to the mainframe readout system. Readout circuitry is shown on the Time/Division and Readout Switching schematic (diagram 5) at the rear of this manual.

### BASIC READOUT SYSTEM

The readout system in 7000-series mainframes provides an alphanumeric display of information encoded by the plug-in units. This display is presented on the crt, and is written by the crt beam on a time-shared basis with the analog waveform display.

The readout system produces a pulse train consisting of ten negative-going pulses called time-slots. Each pulse represents a possible character in a readout word, and is assigned a time-slot number corresponding to its position in the word (refer to Table 3-2). Each time-slot pulse is directed to one of ten output lines, labeled TS 1 through

## Theory of Operation-7B10

TS 10 (time slots one through ten), which are connected to the vertical and horizontal plug-in compartments. Two output lines, row and column, are connected from each channel (two channels per plug-in compartment) back to the readout system.

Data is encoded on these output lines either by connecting resistors between them and the time-slot input lines or by generating equivalent currents. The resultant output is a sequence of analog current levels on the row and column output lines. The row and column current levels are decoded by the readout system to address a character matrix during each time slot, thus selecting a character to be displayed or a special instruction to be followed.



The Interface Connectors provide interconnection for control signals and power supply voltages between the mainframe and the time-base unit.

The Power Supply derives supply voltages from the mainframe supplies for power requirements unique to this instrument. Additional voltage regulation is also provided.

TABLE 3-2
Readout Character Selection

Characters	Time-Slot	Description	Encoded By			
Decimal	TS-1	Determines decimal magnitude (number of zeros displayed or prefix change information).	R751, R752, R756			
Uncalibrated (>)	TS-3	Indicates calibrated or uncali- brated sweep rates and delay times.	R761, R764			
1, 2, 5	TS-4	Scaling (TIME/DIV)	R771 R772, R773			
m, μ, n, p	TS-8	Defines the prefix which modifies the units of measurement.	R781, R782 R783, R784			
s(seconds)	TS-9	Defines the unit of measurement.	R793, R794			

**3-10** @

## **MAINTENANCE**

This section of the manual contains information for performing preventive maintenance, troubleshooting, and corrective maintenance for this instrument.

## PREVENTIVE MAINTENANCE

Preventive maintenance consists of cleaning, visual inspection, lubrication, etc. Preventive maintenance performed on a regular basis may prevent instrument breakdown and will improve the reliability of the instrument. The severity of the environment to which this instrument is subjected determines the frequency of maintenance. A convenient time to perform preventive maintenance is preceding adjustment of the instrument.

#### **CLEANING**

This instrument should be cleaned as often as operating conditions require. Accumulation of dirt on components acts as an insulating blanket and prevents efficient heat dissipation which can cause overheating and component breakdown.



Avoid the use of chemical cleaning agents which might damage the plastics used in this instrument. Use a non-residue type of cleaner, preferably isopropyl alchohol, totally denatured ethyl alchohol, or Freon TF. Before using any other type of cleaner, consult your Tektronix Service Center or representative.

## **EXTERIOR**

Loose dust accumulated on the front panel can be removed with a soft cloth or small brush. Dirt that remains can be removed with a soft cloth dampened with a mild detergent and water solution. Abrasive cleaners should not be used.

WARNING

To avoid electric shock, disconnect the instrument from the power source before removing protective panels.

### INTERIOR

Dust in the interior of the instrument should be removed occasionally due to its electrical conductivity under high-humidity conditions. The best way to clean the interior is to blow off the accumulated dust with dry, low-pressure air. Remove any dirt which remains with a soft brush or a cloth dampened with a mild detergent and water solution. A cotton tipped applicator is useful for cleaning in narrow spaces.

#### SWITCH CONTACTS

Switch contacts and pads are designed to operate dry for the life of the switch. However, as the switches are not sealed, dust attracted to the contact area may cause switch contacts to become electrically noisy. Cleaning may be accomplished by flushing the contact area with isopropyl alcohol or kelite (1 part kelite to 20 parts water). Do not use chemical cleaning agents that leave a film or that might damage plastic parts. Do not use cotton swabs or similar applicators to apply cleaning agents, as they tend to snag and leave strands of cotton on switch contacts. Should it become necessary to remove a switch for replacement or cleaning, refer to Component Removal and Replacement in this section.

### VISUAL INSPECTION

This instrument should be inspected occasionally for such defects as broken connections, improperly seated semiconductors, damaged circuit boards, and heat-damaged parts.

The corrective procedure for most visible defects is obvious; however, particular care must be taken if heat-damaged components are found. Overheating usually indicates other trouble in the instrument; therefore, it is important that the cause of overheating be corrected to prevent recurrence of the damage.

#### LUBRICATION

Generally, there are no components in this instrument that require a regular lubrication program during the life of the instrument

## CAM SWITCH LUBRICATION

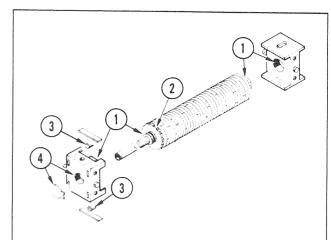
In most cases, factory lubrication should be adequate for the life of the instrument. However, if the switch has been disassembled for replacement of switch sub-parts, a lubrication kit containing the necessary lubricating materials and instructions is available through any Tektronix Field Office. Order Tektronix Part 003-0342-01. General Electric Versilube silicone grease should be applied sparingly so that the lubricant does not get on the contacts. Refer to Figure 4-1 for lubrication instructions.

## SEMICONDUCTOR CHECKS

Periodic checks of the semiconductors in this instrument are not recommended. The best check of semiconductor performance is actual operation in the instrument. More details on checking semiconductor operation are given under Troubleshooting.

### ADJUSTMENT AFTER REPAIR

After any electrical component has been replaced, the adjustment of that particular circuit should be checked, as well as the adjustment of other closely related circuits. The Performance Check procedure in this manual provides a quick and convenient means of checking instrument operation. In some cases, minor troubles may be revealed or corrected by adjustment.



- 1 Apply lubricant to the drum journals and mating surface in the mounting bearings.
- 2 Apply lubricant to the wear surface of the index wheel.
- 3 Apply lubricant to the index roller and roller guide in the front bearing. A thin film should be applied to the inner face of the detent springs if more than one spring is replaced.
- 4) Ensure that some lubricant is present at the interface between the bearing and retainer clip.

C1067

Fig. 4-1. Lubrication procedure for a typical cam switch.

## **TROUBLESHOOTING**

The following information is provided to help troubleshoot this instrument. Information contained in other sections of this manual should be used along with the following information to aid in locating the defective component. An understanding of the circuit operation is very helpful in locating troubles, particularly where integrated circuits are used.

#### TROUBLESHOOTING AIDS

## **DIAGRAMS**

Circuit diagrams are given on foldout pages in section 8. The component number and electrical value of each component in this instrument is shown on the diagrams. Components that are mounted on circuit boards are outlined on the diagrams with a heavy black line.

## **VOLTAGES AND WAVEFORMS**

Typical operating voltages and waveforms are shown next to the diagram where they were measured. Each waveform is numbered to locate on the diagram the point

where the waveform was taken. Voltages and waveform conditions given on the diagram page list the test equipment used and the front-panel control status necessary to obtain the given waveform.

#### TROUBLESHOOTING CHART

The Troubleshooting Chart in section 8 is useful for locating a fault in the absence or presence of specific symptoms. Refer to the chart, circuit description, and circuit diagrams when troubleshooting the instrument.

**4-2** 

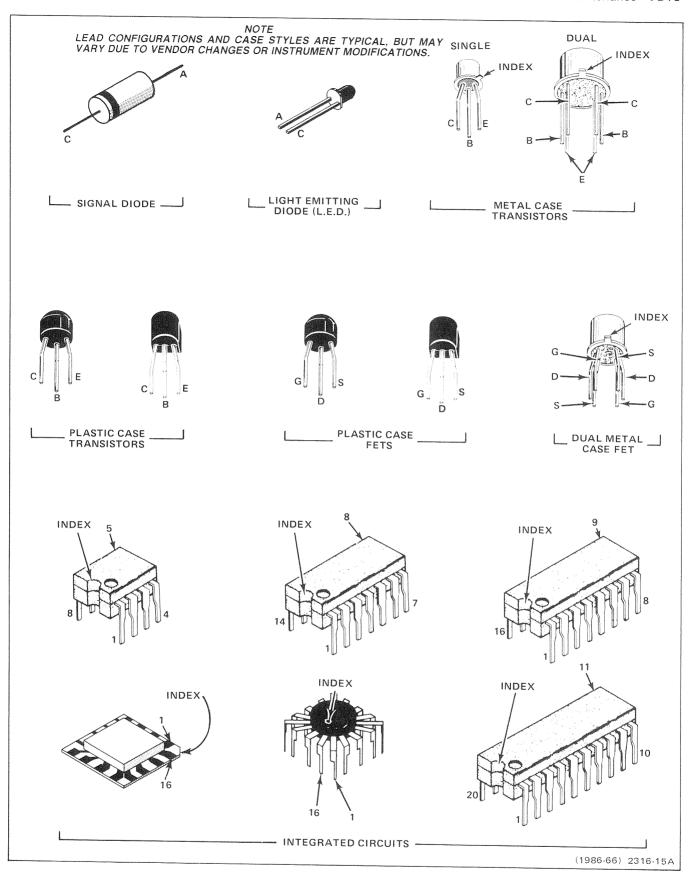


Fig. 4-2. Semiconductor lead configuration.

## CIRCUIT-BOARD ILLUSTRATIONS

Circuit-board illustrations are shown on the foldout page preceding the associated diagram. Each board-mounted electrical component is identified by its circuit number, as are interconnecting wires and connectors.

Figure 8-2, in the front of the diagrams section, shows the location and assembly number of each circuit board in this instrument.

### SWITCH IDENTIFICATION

Switch cam numbers shown on diagrams indicate the position of each cam in the complete switch assembly. The switch cams are numbered from front to rear.

#### DIODE COLOR CODE

The cathode end of each glass-encased diode is indicated by a stripe, a series of stripes, or a dot. The cathode and anode ends of metal-encased diodes are identified by the diode symbol marked on the case. For most silicon or germanium diodes with a series of stripes, the color code identifies the four significant digits of the JEDEC or vendor number using the resistor color-code system (e.g., a diode color-coded yellow-brown-green-red indicates a 1N-4152 diode).

### WIRING COLOR CODE

Insulated wire and cable used in this instrument is color-coded to facilitate circuit tracing.

#### SEMICONDUCTOR BASING

Figure 4-2 illustrates the basing configurations for all semiconductors used in this instrument. Some plastic-case transistors have lead configurations that do not agree with those shown here. If a replacement transistor is made by a different manufacturer than the original, check the manufacturer's basing diagram. All transistor sockets in this instrument are wired for the standard basing used for metal-case transistors.

## INTER-BOARD PIN CONNECTOR IDENTIFICATION

The inter-board pin connector sockets are installed on circuit boards in groups of 5 sockets (as in Fig. 4-3). Socket number 1 is indexed on the circuit board with either a triangular mark or the number 1. Each group of

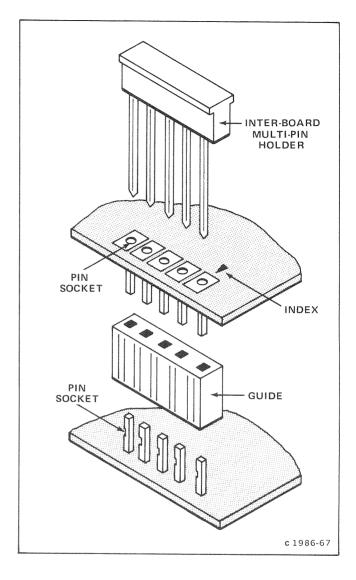


Fig. 4-3. Inter-board multi-pin connector assembly.

sockets is identified by its J (jack) number etched on the circuit board. The J numbers correspond with the J (jack) and P (plug) circuit numbers on the schematic diagrams.

#### MULTI-PIN CONNECTOR IDENTIFICATION

Multi-pin connectors mate with groups of pins soldered to circuit boards. Pin number 1 is indexed with a triangular mark on the circuit board and molded on the holder of the multi-pin connector, as shown in Figure 4-4. Each group of pins is identified by its corresponding J number etched on the circuit board. J numbers on the circuit boards correspond with J and P component numbers on the schematic diagrams.

4-4

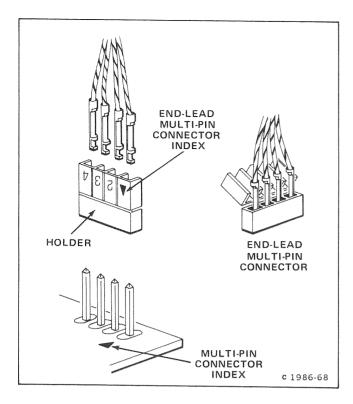


Fig. 4-4. End-lead multi-pin connector assembly.

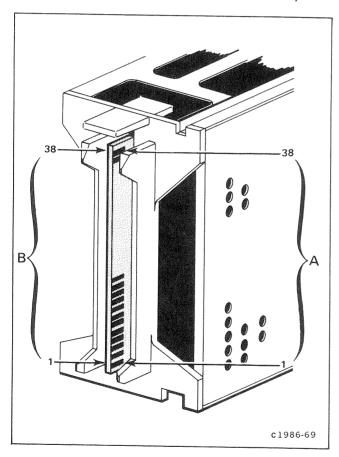


Fig. 4-5. Location of pin numbers on Interface connector.

## INTERFACE CONNECTOR PIN LOCATIONS

The Interface circuit board couples the plug-in unit to the associated mainframe (oscilloscope). Figure 4-5 identifies the pins on the interface connector as shown on Interface Connectors and Power Supply diagram 6 in the Diagrams section.

## ADJUSTMENT AND PERFORMANCE CHECK

The Adjustment and Performance Check procedure in section 5 of this manual provides a quick and convenient means of checking instrument operation. In some cases, minor troubles may be revealed or corrected by adjustment.

## TROUBLESHOOTING EQUIPMENT

The following equipment, in addition to that listed in the Calibration section, is useful for troubleshooting.

#### Transistor Tester

Description: Dynamic-type tester.

Purpose: Test semiconductors.

Recommended Tektronix types: 576 Curve Tracer, 577/177 Curve Tracer system, 7CT1N Curve Tracer unit and a 7000-series oscilloscope system or a 5CT1N Curve Tracer unit and a series oscilloscope.

#### Multimeter

**Description:** Voltmeter, 10 megohm input impedance and a range from 0 to at least 50 volts dc; accuracy, within 0.1%. Ohmmeter to 20 megohms. Test probes should be insulated to prevent accidental shorting.

Purpose: Check voltage and resistance.

### Test Oscilloscope

**Description:** Frequency response, dc to 100 megahertz minimum; deflection factor, 5 millivolts to 5 volts/division. A 10X, megohm voltage probe should be used to reduce circuit loading.

Purpose: Check operating waveforms.

## TROUBLESHOOTING TECHNIQUES

The following troubleshooting procedure is arranged to check the simple trouble possibilities before proceeding with extensive troubleshooting. The first few checks ensure proper connection,

## 1. CHECK CONTROL SETTINGS

Incorrect control settings can indicate a trouble that does not exist. If there is any question about the correct function or operation of any control, see the Operating Instructions, Section 2.

#### 2. CHECK ASSOCIATED EQUIPMENT

Before troubleshooting, check that the equipment used with this instrument is operating correctly. Check that the signal is properly connected and that the interconnecting cables are not defective. Also, check the power source. If the trouble persists, the time-base unit is probably at fault.

#### 3. VISUAL CHECK

Visually check the portion of the instrument in which the trouble is located. Many troubles can be located by visible indications such as unsoldered connections, broken wires, damaged circuit boards, damaged components.

## 4. CHECK INSTRUMENT ADJUSTMENT

Check the adjustment of this instrument, or the affected circuit if the trouble appears in one circuit. The apparent trouble may be the result of misadjustment. Complete adjustment instructions are provided in Adjustment and Performance Check, section 5.

#### 5. ISOLATE TROUBLE TO A CIRCUIT

To isolate trouble to a circuit, note the trouble symptom. The sympton often identifies the circuit in which the trouble is located. When trouble symptoms appear in more than one circuit, check the affected circuits by taking voltage and waveform readings. Incorrect operation of all circuits often indicates trouble in the power supply. Check first for correct voltages of the individual supplies. However, a defective component elsewhere in the instrument can appear as a power-supply trouble and may also affect the operation of other circuits.

The Troubleshooting Chart at the rear of the manual serves as a guide for locating a defective circuit. Start at the top of the chart and perform the checks given on the left side of the page until a step is found that does not produce the indicated results. Further checks, or the circuit in which the trouble is probably located, are listed to the right of the step. The shaded blocks on the Troubleshooting Chart indicate circuit(s) that may cause instrument malfunction. The circuit(s) listed in shaded blocks are discussed in detail in the Theory of Operation section of this manual. This chart does not include checks for all possible defects; use steps 6 and 7 in such cases.

After the defective circuit has been located, proceed with steps 6 and 7 to locate the defective component(s).

#### 6. CHECK VOLTAGES AND WAVEFORMS

Often the defective component can be located by checking for the correct voltages and waveforms in the circuit. Refer to the diagrams section at the rear of the manual for typical voltages and waveforms.

#### NOTE

Voltages and waveforms on the diagrams are not absolute and may vary slightly between instruments. To obtain operating conditions similar to those used to take these readings, see the voltage and waveforms page adjacent to each schematic diagram. Note the recommended test equipment, front-panel control settings, voltage and waveform conditions, and test equipment cable connection instructions.

### 7. CHECK INDIVIDUAL COMPONENTS

The following procedures describe methods for checking individual components. Two-lead components that are soldered in place are best checked by first disconnecting one end. This isolates the measurement from the effects of surrounding circuitry.

WARNING

To avoid electric shock, always disconnect the instrument from the power source before replacing components.

## **Transistors**

The best check of transistor operation is actual performance under operating conditions. A transistor can be most effectively checked by substituting a new component or one that has been checked previously. However, be sure that circuit conditions are not such that a replacement transistor might also be damaged. If substitute transistors are not available, use a dynamic tester. Static-type testers are not recommended, since they do not check operation under simulated operating conditions.

#### Integrated Circuits

Integrated circuits (IC's) can be checked with a voltmeter, test oscilloscope, or by direct substitution. A good understanding of circuit operation is desirable when troubleshooting circuits using IC's. Use care when checking voltages and waveforms around the IC's so that adjacent leads are not shorted together. A convenient means of clipping a test probe to the in-line IC's is with an IC test clip. This device also serves as an extraction tool. The lead configuration for the semiconductors used in this instrument are shown on a pullout page in the front of the diagrams section.



When checking diodes, do not use an ohmmeter scale that has a high internal current, since high currents may damage the diodes under test.

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#### Diodes

A diode can be checked for an open or shorted condition by measuring the resistance between terminals with an ohmmeter, using a scale having a low internal source current, such as the R X 1K scale. The resistance should be very high in one direction and very low when the meter leads are reversed.

The cathode end of each glass-encased diode is indicated by a stripe, a series of stripes, or a dot. The cathode and anode ends of metal-encased diodes are identified by the diode symbol marked on the case. For most silicon or germanium diodes with a series of stripes, the color code identifies the four significant digits of the JEDEC or vendor number using the resistor color-code system (e.g., a diode color-coded yellow-brown-green-red indicates a 1N-4152 diode).

### Resistors

Check resistors with an ohmmeter. See the Replaceable Electrical Parts list for the tolerance of the resistors used in this instrument. Resistors normally do not need to be replaced unless the measured value varies widely from that specified.

#### Inductors

Check for open inductors by checking continuity with an ohmmeter. Shorted or partially shorted inductors can usually be found by checking the waveform response when high-frequency signals are passed through the circuit. Partial shorting often reduces high-frequency response.

#### Capacitors

A leaky or shorted capacitor can usually be detected by checking resistance with an ohmmeter on the highest scale. Do not exceed the voltage rating of the capacitor. The resistance reading should be high after initial charge of the capacitor. An open capacitor can best be detected with a capacitance meter or by checking that the capacitor does not pass ac signals.

#### 8. REPAIR AND ADJUSTMENT

If any defective parts are located, follow the replacement procedures in Corrective Maintenance. Be sure to check the performance of any circuit that has been repaired or had any electrical components replaced.

## CORRECTIVE MAINTENANCE

Corrective maintenance consists of component replacement and instrument repair. Special techniques required to replace components in this instrument are given here.

### **OBTAINING REPLACEMENT PARTS**

All electrical and mechanical part replacements can be obtained through your Tektronix Field Office or representative. However, many of the standard electronic components can be obtained locally in less time than is required to order them from Tektronix, Inc. Before purchasing or ordering replacement parts, check the parts list for value, tolerance, rating, and description.

#### NOTE

When selecting replacement parts, remember that the physical size and shape of a component may affect the performance of the instrument, particularly at high frequencies. All parts should be direct replacements unless a different component will not adversely affect instrument performance.

Some parts are manufactured or selected by Tektronix, Inc. to satisfy particular requirements, or are manufactured to specifications for Tektronix, Inc. Most of the mechanical parts are used in this instrument have been manufactured by Tektronix, Inc. To determine the manufacturer of parts, refer to parts list, Cross Index Mfr. Code Number to Manufacturer.

When ordering replacement parts from Tektronix, Inc., include the following information:

- 1. Instrument type.
- 2. Instrument serial number.
- 3. A description of the part (if electrical, include circuit number).
- 4. Tektronix part number.

### SOLDERING TECHNIQUES

WARNING

To avoid electrical shock, disconnect the instrument from the power source before soldering.

#### Maintenance-7B10

The reliability and accuracy of this instrument can be maintained only if proper soldering techniques are used when repairing or replacing parts. General soldering techniques, which apply to maintenance of any precision electronic equipment, should be used when working on this instrument. Use only 60/40 rosin-core, electronicgrade solder. The choice of soldering iron is determined by the repair to be made. When soldering on circuit boards, use a 15- to 40-watt pencil-type soldering iron with a 1/8-inch wide, wedge-shaped tip. Keep the tip properly tinned for best heat transfer to the solder joint. A higher wattage soldering iron may separate the wiring from the base material. Avoid excessive heat; apply only enough heat to remove the component or to make a good solder joint. Also, apply only enough solder to make a firm solder joint; do not apply too much solder.

CAUTION

All circuit boards, except the readout circuit board, in this instrument are multilayer type boards with a conductive path(s) laminated between the top and bottom board layer. All soldering on these boards should be done with extreme care to prevent breaking the connections to the center conductor(s); only experienced maintenance personnel should attempt repair of these boards.

For metal terminals (e.g., switch terminals, potentiometers, etc.), a higher wattage-rating soldering iron may be required. Match the soldering iron to the work being done. For example, if the component is connected to the chassis or other large heat-radiating surface, it will require a 75-watt or larger soldering iron.

The following techniques should be used to replace a component on a circuit board:

- 1. Grip the component lead with long-nose pliers. Touch the soldering iron to the lead at the solder connection. Do not lay the iron directly on the board, as it may damage the board.
- 2. When the solder begins to melt, gently pull the lead out. If unable to pull out the lead without using force, try removing the other end of the component as it may be more easily removed.

#### NOTE

Some component leads are difficult to remove due to a bend placed on each lead during the manufacturing process. The bent leads hold components in place during a process that solders many components at one time.

If a component lead is extremely difficult to remove, it may be helpful to straighten the leads on the back side of the board with a small screwdriver or pliers while heating the soldered connection.

Use only enough heat to remove the component lead without removing the solder from the board. If it is desired to remove solder from a circuit-board hole for easier installation of a new component, a solder-removing wick or solder extractor should be used.

- 3. Bend the leads of the new component to fit the holes in the board. If the component is replaced while the board is mounted in the instrument, cut the leads so they will just protrude through the board. Insert the leads into the holes so the component is firmly seated against the board (or as positioned originally). If it does not seat properly, heat the solder and gently press the component into place.
- 4. Touch the iron to the connection and apply a small amount of solder to make a firm solder joint. To protect heat-sensitive components, hold the lead between the component body and the solder joint with a pair of longnose pliers or other heat sink.
- 5. Clip any excess lead protruding through the board (if not clipped in step 3).
- 6. Clean the area around the solder connection with a flux-removing solvent. Be careful not to remove information printed on the board.

# COMPONENT REMOVAL AND REPLACEMENT

WARNING

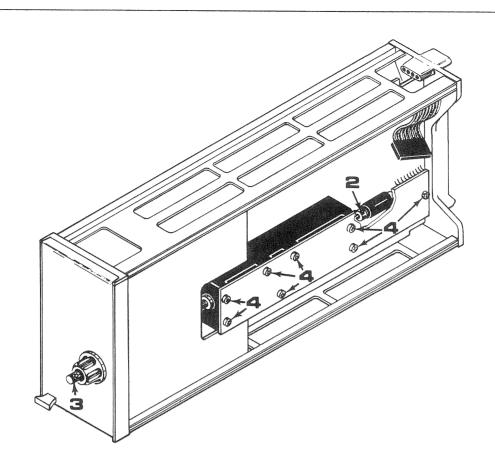
To avoid electrical shock, disconnect the instrument from the power source before replacing components.

The exploded-view drawing associated with the Replaceable Mechanical Parts list may be helpful in the removal or disassembly of individual components or subassemblies. Component locations and circuit board locations are shown in the Diagrams section.

#### CIRCUIT BOARDS

If a circuit board is damaged beyond repair, replace the entire board assembly. Part numbers for completely wired boards are given in the Replaceable Electrical Parts list.

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#### REMOVE READOUT BOARD AS FOLLOWS:

- 1. Note index of multi-pin connector attached to board and disconnect cable,
- 2. Loosen set screw on variable switch assembly.
- 3. Remove VARIABLE (CAL IN) TIME/DIV knob and attached shaft out the front of the instrument.
- 4. Remove the 8 securing screws from board.
- 5. Lift board away from cam switch assembly.

TO REPLACE READOUT BOARD, REVERSE THE ORDER OF REMOVAL.

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Fig. 4-6. Readout board removal procedure.

#### A3-Readout Circuit Board

To remove the circuit board, follow the procedure given in Figure 4-6.

### A2-Trigger Circuit Board

To remove the circuit board:

- 1. Remove 3 inter-board multi-pin connectors (see Fig. 4-3 for identification).
- 2. Remove 2 screws from circuit board.

- 3. Lift rear of circuit board away from frame and slide board to the rear until push-button switches are clear of the front panel.
- 4. Note wire color on single-conductor shielded cables (see Fig. 4-7 for identification) and connector to which each is attached.
- 5. Disconnect cables from back of circuit board.

To replace the circuit board, reverse the order of removal.

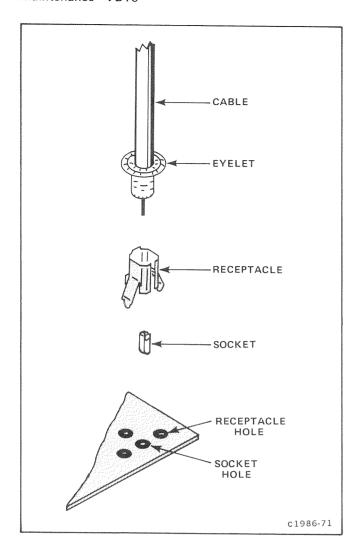


Fig. 4-7. Coaxial end-lead connector assembly.

#### A1-Interface Circuit Board

To remove circuit board:

- 1. Remove Trigger and Readout circuit boards using procedures given previously.
- 2. Set TIME/DIV knob to 2 ms position and VARIABLE TIME/DIV knob to expose the set screw.
- 3. With hex-key wrench, loosen set screws in both knobs. Remove knobs from shafts.
- 4. Note color of multi-pin connectors (see Fig. 4-4 for identification) and P numbers to which each connect. Disconnect all multi-pin connectors from board.
- 5. Remove 4 screws that secure gray plastic rear panel to instrument frame.
- 6. Remove 6 screws that secure perimeter of board to instrument frame.

7. Remove Interface circuit board through rear of instrument.

To install the Interface circuit board:

- 1. Guide TIME/DIV switch shaft through hole in front panel.
- 2. Install 6 screws that secure perimeter of board to instrument frame.
- 3. Install gray plastic rear panel with 4 securing screws.
- 4. Replace TIME/DIV knob on shaft. Align knob index with 2 ms position; then, tighten 2 set screws on knob.
- 5. Replace VARIABLE knob and tighten set screw.
- 6. Replace all cables as noted during removal procedure.
- 7. Replace Trigger circuit board.

#### **SWITCHES**

Two types of switches are used in this instrument. Contact alignment and spacing are critical to the operation of the push-button and cam switches. Therefore, defective switches should either be replaced as a unit or repaired only by personnel experienced with these types of switches. Your local Tektronix Field Office or representative can provide additional repair information. The following special maintenance information is provided for switch replacement.

#### Cam Switches

Cam switches consist of a rotating cam that mates with contacts on the adjacent circuit board. These contacts are activated by lobes on the cam as the switch is rotated. A cam switch can be disassembled for inspection, cleaning, repair, or replacement; however, it is recommended that the switch be removed and replaced as a unit. Refer to Figure 4-8 for special instructions on cam switch removal.



Cam switch repair should be undertaken only by experienced maintenance personnel. Switch alignment and contact spacing must be carefully maintained for proper operation. A cam switch repair kit is available (Tektronix part 040-0541-00) which contains special alignment tools for use in repairing or replacing the switch contacts. For information or assistance on maintenance of cam switches, contact your local Tektronix Field Office or representative.

(a)

4-10

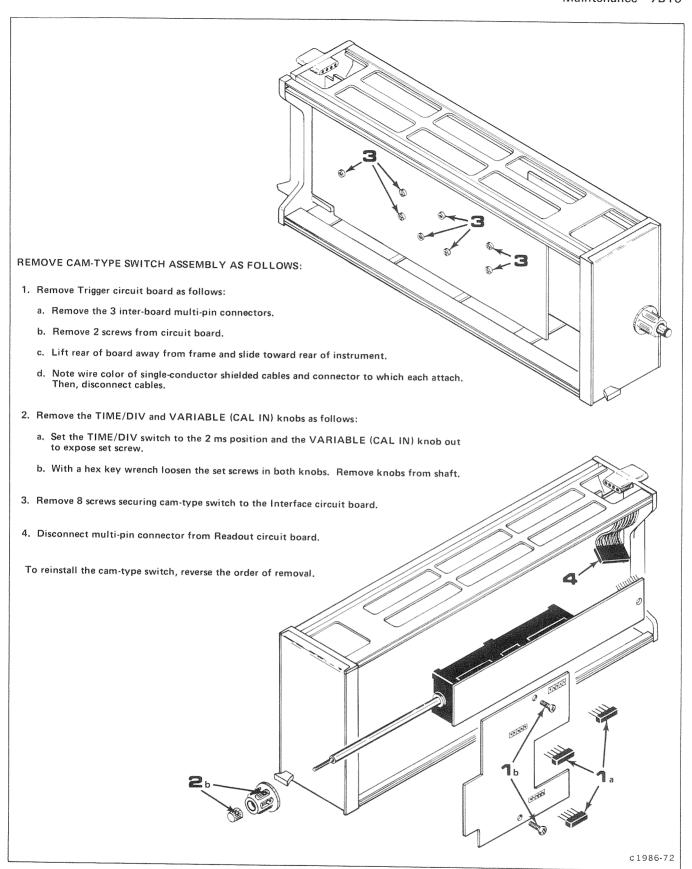
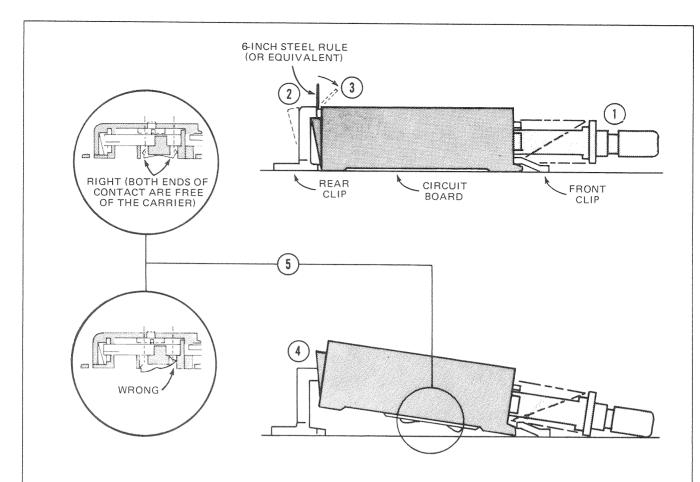


Fig. 4-8. Cam switch removal procedure.



- $\overbrace{1}$  Make sure that all switch shafts are in the OUT position to clear the rear clip.
- Place the long edge of a six-inch rule or similar thin straight edge between the top edge of the rear clip and the switch body.
- 3 Carefully pry the rear clip back just far enough to push the steel rule down between the clip and switch body.



When the switch is removed, the contacts may drop free and be damaged or lost. Body salts or acids can contaminate the switch contacts. Wear cotton gloves to prevent touching the contacts in the switch or on the board with bare hands.

- 4 Pull the rear of the switch up, remove the steel rule, and pull the switch out of the front clip.
- To replace the switch, first check that the slide contacts are properly installed in the carrier. Then, place the front of the switch into the front clip and push the rear of the switch down until the rear clip catches and holds the switch in place.

c1967-3

Fig. 4-9. Removal procedure for typical push-button switch.

#### Push-Button Switches

Removal and replacement instructions for push-button switches are shown in Figure 4-9.

#### **SEMICONDUCTORS**

WARNING

To avoid electric shock, disconnect the instrument from the power source before replacing components.

Semiconductors should not be replaced unless actually defective. If semiconductors are removed during routine maintenance, return them to their original sockets. Unnecessary replacement of semiconductors may affect the adjustment of this instrument. When semiconductors are replaced, check the operation of that part of the instrument which may be affected

WARNING

Handle silicone grease with care. Avoid getting silicone grease in eyes. Wash hands thoroughly after use.

Replacement devices should be of the original type or a direct replacement. Figure 4-2 shows the lead configurations of the semiconductor devices used in this instrument. Some plastic-case transistors have lead configurations that do not agree with those shown here. When replacing, check the manufacturer's basing diagram for correct basing. All transistor sockets in this instrument are wired for the standard basing used for metal-case transistors. Semiconductors that have heat radiators use silicone grease to increase heat transfer. Replace the silicone grease when replacing these semiconductors. See HYPCON CONNECTORS for hybrid integrated circuit replacement instructions.

An extraction tool should be used to remove the in-line integrated circuits to prevent damage to the pins. This tool is available from Tektronix, Inc. Order Tektronix part 003-0619-00. If an extraction tool is not available when removing one of these integrated circuits, pull slowly and evenly on both ends of the device. Try to avoid having one end of the integrated circuit disengage from the socket before the other, as the pins may be damaged.

### HYPCON CONNECTORS

The Hypcon connector is a precision-made connector designed to provide low loss electrical and thermally efficient connection between the printed circuit board and hybrid integrated circuit. An exploded view of the Hypcon connector is shown in Figure 4-10. Care must be taken when replacing the hybrid IC's not to touch the elastomer gold-plated contacts with the fingers or to use a cleaner which will degrade the conductivity of the contacts. The Hypcon connector and hybrid IC should be removed if it becomes necessary to use a cleaning

solvent near the connector when replacing adjacent (within 1/2") circuit board components. IMPORTANT: Remove all traces of solder flux or foreign material contamination from the circuit board contact area before replacing the connector. Contamination usually takes place during the soldering and cleaning processes. Even when the soldering is done carefully, flux, oil, or other contaminants can be carried into these devices during the cleaning operation. When the solvent evaporates, non-conductive contaminants may remain on or near the contact interfaces.

The cleaning process, either hand cleaning with a solvent or machine cleaning in an automatic detergent wash, is not recommended for boards fitted with Hypcon connectors.

If a component adjacent to a Hypcon connector must be replaced, the following steps are recommended:

- 1. Remove the hybrid IC and Hypcon connector (see Disassembly and Removal instructions) before any soldering or cleaning and store in a dirt-free covered container. When several hybrids and Hypcon connectors are to be removed, keep parts together and replace as sets; do not interchange parts.
- 2. Hand soldering:
- a. Use small diameter solder (0.030"-0.040").
- b. Use low wattage soldering irons (15 to 40 watts).
- c. Use care with flux amount and placement.
- 3. Remove solder flux and contact contamination with isopropyl alcohol.
- 4. Flush the hybrid and Hypcon connector mounting area with isopropyl alcohol. Do not scrub with a Q-tip, as cotton fibers will adhere to edges and surfaces of contact areas and cause open or intermittent connections. If the etched circuit board surfaces require more cleaning, scrub with a soft rubber eraser and blow or vacuum clean while dusting surface with a small soft clean brush.
- 5. If the hybrid IC and elastomer contact holder are contaminated, clean the contact holder and hybrid by flushing or spraying with alcohol and oven dry at 100° C. Do not scrub with a cotton-tipped applicator or similar device. If the contact holder is excessively contaminated, replace it with a new one.

Two inch-pounds of torque should be applied to the mounting screws to secure the Hypcon to the circuit board.

Exercise care when mounting the frame-elastomer connector holder-hybrid IC assembly to the circuit board to prevent misalignment between the connector and board. Grasp the assembly at the hybrid (hat) with tweezers to facilitate correct alignment of the plastic frame projections with the circuit board.

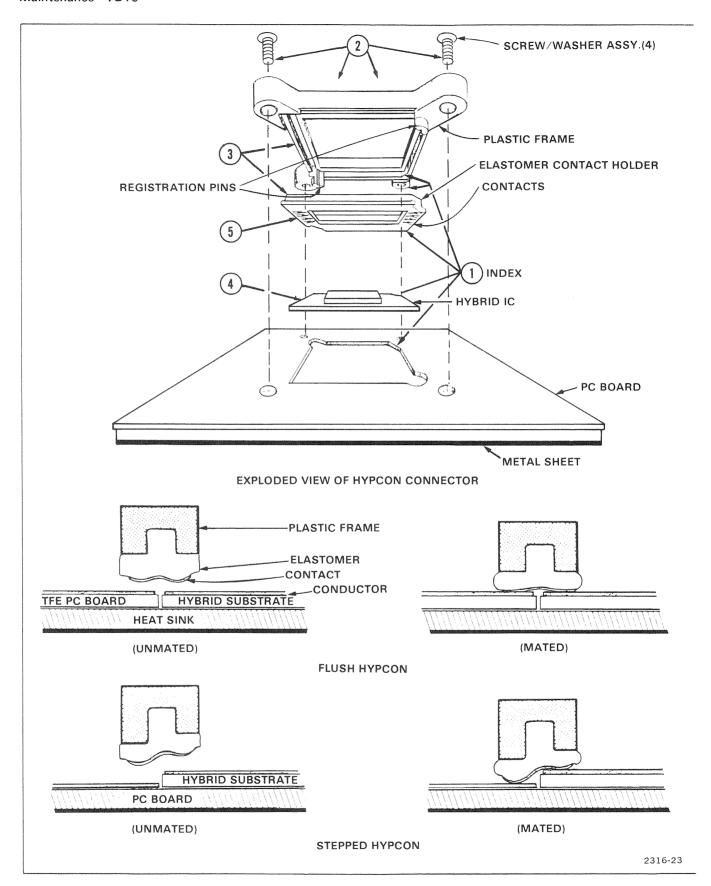


Fig. 4-10. Hypcon connector removal and replacement.

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#### **DISASSEMBLY AND REMOVAL**

- 1) Note index on circuit board (arrow) and Hypcon plastic frame (pointed mounting ear).
- (2) Unscrew and remove the 4 screw/washer assemblies.
- (3) Lift Hypcon connector from board.
- 4 Note index location of hybrid and remove from board with tweezers.
- 5 Note index location of elastomer contact holder and remove by grasping a corner of the contact holder with tweezers and lifting up. Do not touch the gold-plated contacts with your fingers.

#### REASSEMBLY AND REPLACEMENT

Grasp corner of elastomer contact holder with tweezers and place in plastic frame slot being careful to match the flat contact holder with the flat frame corner. Place a clean plastic envelope over finger and press with finger to seat contact holder into the frame. The contact holder must be evenly seated on all four sides.

Flush Hypcon: Match hybrid flat corner with board receptacle flat corner and place hybrid in receptacle. Match pointed mounting ear of Hypcon connector with flat corner of receptacle and guide registration pins into the board holes.

Stepped Hypcon: Using tweezers, match the hybrid corner index with the elastomer contact holder index and insert between the registration pins. Turn the assembly over, grasp the hybrid "hat" with the tweezers, and guide the registration pins into the board holes. Match the plastic frame pointed mounting ear with the circuit board arrow.

Insert mounting hardware and apply 2 inch-pounds of torque to secure the connector assembly.

2316-24

Fig. 4-10 (cont.). Hypcon connector removal and replacement.

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Because of the close tolerances involved, special care must be taken to assure correct index alignment of each Hypcon part during reassembly. Failure to do so can result in damage to the parts when they are joined together. See Figure 4-10 for index locations.

If your instrument contains both the flush and stepped type of Hypcon connectors be careful not to mix the elastomer contact holders during reassembly. The flush Hypcon connectors have green elastomer contact holders and the plastic frame is marked "FLUSH." The stepped Hypcons have neutral-colored elastomer contact holders with a slight ridge or step on the contact surface; the large frames are marked "STEPPED." The registration pins on the stepped plastic frame are slightly longer than those on the flush frame. The elastomer contact holder in the small stepped connectors is indexed differently than the large connectors. Look for a small gold arrow in one corner of the holder instead of a flat corner. Match this corner arrow with the pointed corner of the plastic frame. Give close attention to this indexing, as it is easy to insert the elastomer contact holder incorrectly.

Differences also exist between the large flush and large stepped Hypcon circuit board receptacles. Figure 4-10 shows the cross-sectional differences which must be observed when working with an instrument that contains both types of Hypcon connectors.



Damage to the elastomer contact holder can result if the connectors are not mated properly with the board receptacles.

When replacing the flush-type hybrid, insert the hybrid in the board opening and then position the Hypcon connector in the board registration holes for perfect alignment. With the large and small-size stepped connectors, assemble the connector and hybrid before installing on the circuit board. Use tweezers to hold the assembly by the hybrid (hat) and guide the frame registration pins into the circuit board openings. Avoid touching the hybrid and elastomer contact holder with your fingers; finger oils can degrade conductivity.

A procedure for removal and replacement is included in Figure 4-10.

Beginning and ending hybrid substrate contact numbers are printed on the substrate at the index corner. See Figure 4-2, Semiconductor lead configurations.

#### INTERCONNECTING PINS

Three methods of interconnection are used to connect the circuit boards with other boards and components. When the interconnection is made with a coaxial cable, a special end-lead connector plugs into a socket on the board (Fig. 4-7). When the interconnection is made with a wire lead, an end-lead connector is used which mates with the interconnecting pin soldered into the board (Fig. 4-4). When the interconnection is made between adjacent boards, an inter-board multi-pin connector is uded (Fig. 4-3). The following information provides the removal and replacement procedure for the various types of interconnection methods.

#### Coaxial End-Lead Connectors

Replacement of the coaxial-type end-lead connectors requires special tools and techniques; only experienced maintenance personnel should attempt to remove and replace these connectors. It is recommended that the cable be replaced as a unit. For cable part numbers, see the Replaceable Mechanical Parts list. An alternative solution is to refer the replacement of the defective connector to your local Tektronix Field Office or representative.

#### **End-Lead Pin Connectors**

The pin connectors used to connect the wires to the interconnecting pins are clamped to the ends of the associated leads. To remove and replace damaged endlead pin connectors, remove the old pin connector from the end of the lead and clamp the replacement connector to the lead.

Some of the pin connectors are grouped and mounted together in a plastic holder; the overall result is that these connectors are removed and installed as a multipin connector (see Fig. 4-4). To provide correct orientation of this multi-pin connector when it is replaced, an arrow is marked on the circuit board and a matching arrow is molded into the plastic holder of the multi-pin connector. Be sure these arrows are aligned as the multi-pin connector is replaced. If the individual end-lead pin connectors are removed from the plastic holder, note the color of the individual wires for replacement.

## Inter-Board Multi-Pin Connector

The inter-board multi-pin connector pin-holder is not repairable and should be replaced as a unit (see Fig. 4-3). Refer to the Replaceable Mechanical Parts list for part number. Inter-board multi-pin connector pin-sockets are soldered to circuit boards (see Fig. 4-3). To replace a socket, first remove the guide. Then, remove the old socket using soldering techniques previously described. Solder the new socket in place, making sure it will align properly with the inter-board connector pins.

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#### CIRCUIT-BOARD PINS



All circuit boards in this instrument, except the Readout circuit board, are multilayer type boards with a conductive path(s) laminated between the top and bottom board layers. All soldering on these boards should be done with extreme care to prevent breaking the connection to the center conductor(s); only experienced maintenance personnel should attempt repair of these boards.

A circuit-board pin replacement kit including the necessary tools, instructions, and replacement pins is available from Tektronix, Inc. Order Tektronix part 040-0542-00. Replacement of circuit-board pins on multilayer boards is not recommended; refer such repairs to your local Tektronix Field Office or representative.

To replace a damaged pin which is mounted on a singlelayer circuit board, first disconnect any pin connectors. Then (using Soldering Techniques given earlier in this section), unsolder the damaged pin and pull it from the board with a pair of pliers, leaving the ferrule (see Fig. 4-11) in the hole, if possible. If the ferrule remains in the circuit board, remove the spare ferrule from the replacement pin and press the new pin into the hole in the circuit board. If the ferrule is removed with the damaged pin, clean out the hole using a solder-removing wick and a scribe. Then, press the replacement pin with attached spare ferrule into the hole. Position the replacement pin in the same manner as the damaged pin. Solder the pin to the circuit board on each side of the board, if the old pin was bent at an angle to mate with a connector, carefully bend the new pin to the same angle. Replace the pin connector.

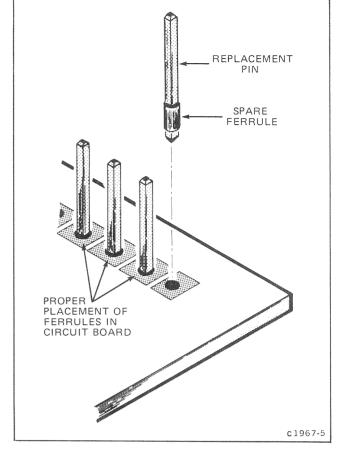


Fig. 4-11. Exploded view of circuit-board pin and ferrule.

### FRONT-PANEL LIGHTS

This instrument uses LED's (light-emitting-diodes) and incandescent lamps for front-panel lights.

LED's are used to illuminate the TRIG'D and SINGLE SWP READY lights. To replace LED's, remove the cap from the sleeve as in Figure 4-12. Note lead wire color coding and LED lead configuration. Unsolder wire leads and remove LED from the cap. Solder the replacement LED and lead wires to the socket cap as noted previously. Install the cap in the sleeve.

Incandescent lamps are used to illuminate the transparent push-button switches. To replace incandescent lamps, unsolder the lead wires from the rear of the cap (see Fig. 4-12), pull the cap and bulb out of the sleeve. Solder the replacement lamp and lead wires to the cap. Install the assembly in the sub-panel sleeve.

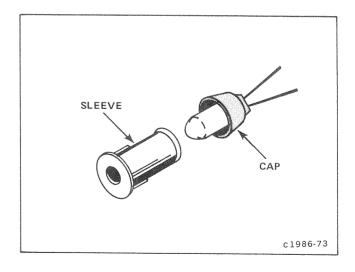


Fig. 4-12. Front-panel light socket assembly.

(a)

## ADJUSTMENT AFTER REPAIR

After any electrical component has been replaced, the adjustment of that particular circuit should be checked, as well as other closely related circuits. See section 5 for a complete adjustment procedure.

### INSTRUMENT REPACKAGING

If the Tektronix instrument is to be shipped to a Tektronix Service Center for service or repair, attach a tag showing: owner (with address) and the name of an individual at your firm that can be contacted. Include complete instrument serial number and a description of the service required.

Save and re-use the package in which your instrument was shipped. If the original packaging is unfit for use or not available, repackage the instrument as follows:

Surround the instrument with polyethylene sheeting to protect the finish of the instrument. Obtain a carton of corrugated cardboard of the correct carton strength and having inside dimensions of no less than six inches more than the instrument dimensions. Cushion the instrument by tightly packing three inches of dunnage or urethane foam between carton and instrument, on all sides. Seal carton with shipping tape or industrial stapler.

The carton test strength for your instrument is 200 pounds.

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## **CALIBRATION**

This section provides information necessary to: (1) Verify that this instrument meets the electrical specifications in Section 1, General Information, (2) verify that all controls function properly, and (3) perform all internal adjustments. The Part I—Performance Check procedure checks the electrical specifications listed in section 1 without making any internal adjustments. The Part II—Adjustment and Performance Check procedure provides a complete sequential check of instrument performance concurrent with a complete sequential adjustment of internal controls. A separate Operators Checkout Procedure, in the Installation section of this manual, can be used to check only the functions of the front-panel controls and connectors.

## PRELIMINARY INFORMATION

#### USING THESE PROCEDURES

Both the Part I-Performance Check and Part II-Adjustment and Performance Check procedures are divided into functional block subsections (e.g., A. TRIGGERING SYSTEM and B. HORIZONTAL SYSTEM). The order in which the subsections and steps (A1, A2, B1, B2, etc.) appear in each procedure is the recommended sequence for accomplishing a performance check or calibration of the instrument. Subsections within either procedure can be performed independently, as can each step within any subsection. Refer to Partial Procedures for specific instructions on performing either a partial Performance Check or a partial Adjustment and Performance Check.

All functional block subsections begin with a list of required test equipment, followed by instructions for **Before You Begin** and the list of Preliminary Control Settings for that subsection (e.g., TRIGGERING SYSTEM Preliminary Control Settings, etc.). Each step contains separate Setup Conditions which, if applicable, include the instrument control settings, an illustrated test setup, and test equipment control settings. The instrument and test equipment control settings listed in the Setup Conditions for each step may include additional settings, changes from the previous step, or changes to the Preliminary Control Settings making it possible to perform partial procedures. The Setup Conditions illustrate the setup and the best equipment required to accomplish the step instructions.

### Partial Procedures

Part I-Performance Check. To perform a partial Performance Check procedure, first determine which electrical specifications are to be checked. Table 5-1. Performance Check Summary, lists the applicable electrical specifications and provides references to the step(s) in which the performance requirements are checked. The Performance Check Index, at the start of Part I-Performance Check, provides a convenient means for locating the desired subsections and steps. For example: If the external trigger amplifier had been repaired and a performance check was considered necessary, use the Performance Check Summary table to locate the specifications affected by the repair, and the step title of Part I-Performance Check in which those performance requirements are checked. Then use the Performance Check Index to locate the TRIGGER SYSTEM subsection and the step and page number of the applicable step(s)

Any step of a subsection can be performed separately by following the instructions given below.

- 1. Locate the desired subsection and applicable steps (e.g., B1, B2, B4, etc.) with the Performance Check Summary table and the Performance Check Index.
- 2. Perform the Performance Check Power-Up Sequence at the start of Part I-Performance Check. Also follow the instructions under **Before You Begin** and Preliminary Control Settings at the beginning of the subsection.
- 3. Perform the Setup Conditions instructions for the desired step. Disregard any control settings which are the same as those under Preliminary Control Settings.
- 4. Proceed with the lettered instructions (e.g., a, b, c, etc.).

#### NOTE

If the steps performed are consecutive, it is not necessary to repeat the Preliminary Control Settings after the first step. However, when a step is skipped, the Preliminary Control Settings must be performed again.

Part II—Adjustment and Performance Check. Although each step in the Part II-Adjustment and Performance Check procedure can be performed independently, we recommend that the entire subsection be performed if any adjustments are made. Table 5-1, Performance Check Summary, lists the applicable electrical specifications and provides references to the step(s) in which the performance requirements are checked and appropriate adjustments are made. The Adjustment and Performance Check Index, at the start of Part II-Adjustment and Performance Check, provides a convenient means for locating the desired subsections and steps. For example: If the A1 Interface board had been replaced, use the Performance Check Summary table to locate the specifications affected by the repair, and the step title(s) of Part II-Adjustment and Performance Check in which those performance requirements are checked or adjusted. Then use the Adjustment and Performance Check Index to locate the HORIZONTAL SYSTEM subsection and the step and page number of the appropriate step(s).

**(a)** 

A heading system is used to readily identify the steps (A1, A2, B1, B2, etc.) that contain performance check and/or adjustment instructions. For example, if CHECK is the first word in the title of a step, a performance requirement listed in the Specifications is checked. If ADJUST appears as the first word in the title, the step concerns one or more internal adjustments. And if CHECK/ADJUST appears in the title, the step involves one or more performance requirement checks and adjustments.

The alphabetical instructions under each step (a, b, c, etc.) may contain CHECK, EXAMINE, or ADJUST as the first word of the instruction. These terms are defined as follows:

- 1. CHECK—indicates that the instruction accomplishes a performance requirement check. Each performance requirement is derived from the instrument specification and is listed in Table 5-1 Performance Check Summary.
- 2. **EXAMINE**—usually precedes an ADJUST instruction and describes how to determine whether the adjustment is necessary. Measurement limits following the word EXAMINE are not to be interpreted as performance limits

derived from the instrument specifications. They are provided as indicators of a properly functioning instrument and to aid in the adjustment process.

3. ADJUST—describes which adjustment to make the desired result. We recommend that adjustments not be made if a previous CHECK or EXAMINE instruction indicates that no adjustment is necessary.

#### ADJUSTMENT INTERVAL

To maintain instrument accuracy, check the performance of the time base every 1000 hours of operation, or every 6 months if used infrequently. Before complete adjustment, thoroughly clean and inspect this instrument as outlined in Section 4, Maintenance.

#### TEKTRONIX FIELD SERVICE

Tektronix Field Service Centers and the Factory Service Center provide instrument repair and adjustment services. Contact your Tektronix Field Office or representative for further information.

TABLE 5-1
Performance Check Summary

Performance Check Summary				
Characteristic	Performance Requirement		Performance Check Procedure Title	Adjustment and Performance Check Procedure Title
		HORIZONTA	L SYSTEM	J-
Sweep Rates				
Calibrated Range	0.2 s/div to 2 ns/div in 25 steps. X10 Magnifier extends fastest calibrated sweep rate to		B5. Check Sweep Timing.	B5. Adjust Sweep Timing.
0.2 ns/div.			B6. Check Magnified Sweep Timing.	B7. Check Magnified Sweep Timing.
Variable Range	Continuously variable uncalibrated sweep rate to at least 2.5 times the calibrated sweep rate setting.		B4. Check Variable Time/Division and Variable Hold Off.	B4. Check Variable Time/Division and Variable Hold Off.
Sweep Accuracy <sup>1</sup> with 7104, 7900 and 7800 Series Mainframes	Measured over center 8 displayed divisions. $^2$ SWP CAL is adjusted at 1 ms/div within the $+20^\circ$ to $+30^\circ$ C range.			
+15° to +35° C	UNMAG MAG X 10		B5. Check Sweep	B5. Adjust Sweep
0.2 s/div to 10 ns/div	2%	3%	Timing.	Timing.
5 ns/div and 2 ns/div	3%	4%²		

Some mainframes limit fastest calibrated sweep rate.

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<sup>&</sup>lt;sup>2</sup>200 ps/div is measured over any 5 divisions within the center 8 divisions.

# TABLE 5-1 (CONT.) Performance Check Summary

Characteristic	Performance Requirement	Performance Check Procedure Title	Adjustment and Performance Check Procedure Title
Sweep Accuracy (cont.)			
0° to +50° C	Derate +15° to +35° C accuracy by additional 1%.	Customer verification no	rmally not required.
Sweep Length	At least 10.2 div.	B2. Check Sweep Length and Position- ing Range.	B2. Check Sweep Length and Position- ing Range.
MAG Registration	0.5 div or less from graticule center when changing from MAG X10 to MAG X1.	B3. Check Magnifier Gain and Registration.	B3. Check/Adjust Magnifier Gain and Registration.
Position Range			
POSITION Controls fully CW	Start of sweep must be to right of graticule center at 1 ms/div.	B2. Check Sweep Length and Position- ing Range.	B2. Check Sweep Length and Position- ing Range.
POSITION Controls fully CCW	End of sweep must be left of graticule center at 1 ms/div.		-

## TRIGGERING SYSTEM

Trigger Sensitivity for Repetitive Signals	Triggering Frequency Range <sup>3</sup>	Minimum 7 Signal Req		A4. Check External Triggering Sensitivity.	A2. Adjust External Trigger Compensation (C117).
Coupling		Internal	External	Triggering Sensitivity.	A3. Adjust Sensitivitiy
AC	30 Hz to 250 MHz	0.5 div	50 mV		(R147).
	250 MHz to 1 GHz	1.5 div	150 mV		
AC LF REJ⁴	50 kHz to 250 MHz	0.5 div	50 mV		
	250 MHz to 1 GHz	1.5 div	150 mV		
AC HF REJ	30 Hz to 30 kHz	0.5 div	50 mV		
DC⁵	Dc to 250 MHz	0.5 div	50 mV		
	250 MHz to 1 GHz	1.5 div	150 mV		

<sup>&</sup>lt;sup>3</sup>The triggering frequency ranges given here are limited to the -3 dB frequency of the oscilloscope vertical system when operating in the Internal mode. <sup>4</sup>Will not trigger on sine waves at or below 60 Hz when amplitudes are less than 8 divisions Internal or 3 volts External.

<sup>&</sup>lt;sup>5</sup>The Triggering Frequency for DC COUPLING applies to frequencies above 30 Hz when operating in the AUTO TRIGGERING MODE.

# TABLE 5-1 (CONT.) Performance Check Summary

Characteristic	Performance Requirement	Performance Check Procedure Title	Adjustment and Performance Check Procedure Title
Trigger Sensitivity (cont.)			
Single Sweep	Same as for Repetitive and Pulsed Triggering.	Customer verification norr Satisfactory operation is s tests in the procedures.	mally not required. ubstantiated by other
Internal Trigger Jitter	30 ps or less at 1 GHz.	A6. Check Internal Trigger Jitter.	A8. Check Internal Trigger Jitter.
Operating in HF SYNC MODE			
AC, AC LF REJ, or DC	250 MHz to 1 GHz – 0.3 div Internal 75 mV External.	A5. Check Internal Triggering Sensitivity.	A3. Adjust Sensitivity (R147).
		A4. Check External Triggering Sensitivity.	A2. Adjust External Trigger Compensation (C117).
External Trigger Input			
LEVEL RANGE	At least ±3.5 V (checked on 1 kHz sine wave).	A3. Check External Level Range.	A5. Check External Level Range.
Maximum Safe Input	Specification applicable under fau therefore this is not a procedural		
1-Megohm Input	250 V (dc plus peak ac).		
50-Ohm Input	1 Watt average.		
Input R and C		Customer verification norr resistance and capacitance appropriate testing bridge	e can be determined wit
1-Megohm Input	1 M $\Omega$ within 5%, 20 pF within 10%.		
50-Ohm	50 $\Omega$ within 2%.	A1. Check External Trigger Input Resistance.	A1. Check External Trigger Input Resistance.
Trigger Holdoff Time			
Minimum Holdoff Setting		Customer verification norr Satisfactory operation is su in the procedures.	mally not required. ubstantiated by other test
0.2 s/div to 50 ms/div	40 ms		
20 ms/div to 2 $\mu$ s/div	2 times TIME/DIV setting.		
1 $\mu$ s/div to 2 ns/div	2.0 μs		

## TABLE 5-1 (CONT.) Performance Check Summary

Characteristic	Performance Requirement	Performance Check Procedure Title	Adjustment and Performance Check Procedure Title
Trigger Holdoff Time (cont.)			
Maximum Holdoff Setting		Customer verification normally not required. Satisfactory operation is substantiated by other t in the procedures.	
0.2 s/div to 50 ms/div	400 ms		
20 ms/div to 2 $\mu$ s/div	20 times TIME/DIV setting		
1 $\mu$ s/div to 0.5 $\mu$ s/div	20.0 μs		
0.2 $\mu$ s/div to 2 ns/div	6.0 μs		

## TEST EQUIPMENT REQUIRED

The test equipment listed in Table 5-2 is required for a complete Adjustment and Performance Check of the instrument. If only a Performance Check is to be performed, the items required for Adjustment are not required, and are indicated by footnote 1. The remaining test equipment is common to both procedures.

The specifications for test equipment, given in Table 5-2, are the minimum required to meet the Performance Requirements. Detailed operating instructions for test equipment are omitted in these procedures. Refer to the test equipment instruction manual if more information is needed.

### SPECIAL FIXTURES

Special fixtures are used only where they facilitate instrument adjustment. These fixtures are available from Tektronix, Inc. Order by part number from Tektronix Field Offices or representatives.

#### TEST EQUIPMENT ALTERNATIVES

All of the listed test equipment is required to completely calibrate this instrument. However, complete checking or calibration may not always be necessary or desirable. You may be satisfied with checking only selected characteristics, thereby reducing the amount of test equipment actually required.

The calibration procedures are based on the first item of equipment given as an example. When other equipment is substituted, control settings or setups may need to be altered. If the exact item of equipment given as an example in Table 5-2 is not available, first check the specifications column carefully to see if any other equipment might suffice. Then check the Purpose column to see what this item is used for. If used for a check or adjustment that is of little or no importance for your measurement requirements, the item and corresponding step(s) can be deleted.

TABLE 5-2
Test Equipment

Description	Minimum Specifications	Purpose	Examples of Applicable Test Equipment
Oscilloscope     Mainframe	Tektronix 7000-Series. Bandwidth, 1 GHz with 2 horizontal plug- in compartments.	Provides a display for unit under test.	a. TEKTRONIX 7104 Oscilloscope.
2. High Frequency Amplifier Plug-in Unit	Tektronix 7A-Series. Bandwidth, 1 GHz; deflection factor, 10 mV to 0.5 V.	Provides vertical input to oscilloscope main- frame.	a. TEKTRONIX 7A29 Amplifier plug-in unit.

## TABLE 5-2 (CONT.) Test Equipment

Description	Minimum Specifications	Purpose	Examples of Applicable Test Equipment
3. High Impedance Amplifier Plug-in Unit	Tektronix 7A-Series. Bandwidth, 80 MHz; deflection factor, 5 mV to 10 V/div. Input Impedance, 1 MΩ.	Provides high impedance vertical input to oscilloscope mainframe.	a. TEKTRONIX 7A15A Amplifier Plug-in unit.
4. High-Frequency Sine-Wave Generator <sup>1</sup>	Frequency, 250 MHz to 1 GHz; output amplitude, variable from 50 mV to 0.5 V into 50 Ω.	High-frequency trig- gering checks.	a. TEKTRONIX SG 504 Leveled Sine Wave Generator with power module. b. Wavetek 2001 Sweep/
			Signal Generator.
5. Time-Mark Generator	Marker outputs, 1 ns to 5 ns; accuracy, within 0.1%.	Sweep timing checks and adjustments.	a. TEKTRONIX TG 501 Time Mark Generator with power module.
6. Low-Frequency Function Generator	Frequency, 5 Hz to 500 kHz; output amplitude, variable from 50 mV to 3 V into 50 Ω.	Low-frequency trig- gering checks and adjustments.	a. TEKTRONIX FG 503 Function Generator with power module.
			b. General Radio 1310-B Oscillator.
7. Digital Ohmmeter (with test leads) <sup>1</sup>	Range, 200 Ω, accuracy, 0.5% of reading, ±1 count.	Input resistance check.	a. TEKTRONIX DM 502 Digital Multi-meter with power module.
8. Rigid Plug-in Extender	Tektronix 7000-Series extender.	Provides access to internal adjustments and test points.	a. Tektronix 067-0589-00 Calibration Fixture.
9. Coaxial Cables (2 required)	Impedance, 50 Ω; type, RG 58/U; length, 42 and 18 inches; con- nectors, BNC.	Provides signal inter- connections.	a. Tektronix Part 012-0057-01, 012-0076-00.
10. T Connector <sup>1</sup>	Connectors, BNC.	External triggering checks.	a. Tektronix Part 103-0030-00.
11. 10X Attenuator <sup>1</sup>	Dc resistance, 50 $\Omega$ , ±1 $\Omega$ ; connectors, BNC.	Trigger checks and adjust- ments.	a. Tektronix Part 011-0059-00.
12. Screwdriver	3-inch shaft, 3/32-inch bit.	Adjustments.	a. Xcelite R3323.
13. 10X Probe <sup>2</sup>	Attenuation, 10X; for use with 1 $M\Omega$ inputs.	External trigger compensation adjustment.	a. Tektronix Part 010-6105-03.
14. Low-capacitance Screwdriver <sup>2</sup>	2-inch shaft, 3/32-inch bit.	Used for adjusting variable capacitor.	a. Tektronix Part 003-0675-00.

<sup>&</sup>lt;sup>1</sup>Used for performance check only; NOT used for adjustment. <sup>2</sup>Used for adjustment only; NOT used for performance check.

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## PART I—PERFORMANCE CHECK

The following procedure (Part I—Performance Check) verifies electrical specifications without removing instrument covers or making internal adjustments. All tolerances given are as specified in the Specification tables (section 1) in this manual.

Part II—Adjustment and Performance Check provides the information necessary to: (1) verify that the instrument meets the electrical specifications, (2) verify that all controls function properly, and (3) perform all internal adjustments.

A separate Operators Checkout Procedure is provided in the Operators Manual for familiarization with the instrument and also to verify that all controls and connectors function properly.

See Preliminary Information, at the beginning of this section, for information on performing a partial Performance Check procedure.

# PERFORMANCE CHECK PROCEDURE INDEX

# PERFORMANCE CHECK POWER-UP SEQUENCE

	PAGE
A. TRIGGERING SYSTEM  1. Check External Trigger Input Resistand 2. Check Triggering Modes 3. Check External Level Range 4. Check External Triggering Sensitivity 5. Check Internal Triggering Sensitivity 6. Check Internal Trigger Jitter 7. Check Line Triggering	ce 5-8 5-9 5-9 5-10 5-11
B. HORIZONTAL SYSTEM  1. Set Basic Sweep Calibration  2. Check Sweep Length and Positioning Range  3. Check Magnifier Gain and Registration  4.Check Variable Time/Division and Variable Hold Off  5. Check Sweep Timing  6. Check Magnified Sweep Timing	5-12 5-13 5-13 5-14

# NOTE

The performance of this instrument can be checked at any ambient temperature from 0° to 50° C unless otherwise indicated.

- 1. Install a high frequency amplifier plug-in unit in the left vertical compartment of the oscilloscope mainframe.
- 2. Install the 7B10 in the B horizontal compartment of the mainframe.
- 3. Set the mainframe vertical mode switch to display the left vertical unit and the horizontal mode switch to display the B horizontal unit. Set the mainframe intensity controls fully counterclockwise and set the trigger source switches to vertical mode.
- 4. Turn on the mainframe and allow at least 20 minutes warmup before beginning the procedure.

## A. TRIGGERING SYSTEM

Equipment Required: (Numbers correspond to test equipment listed in Table 5-2)

- 1. Oscilloscope mainframe
- 2. High-frequency amplifier plug-in unit
- 4. High-frequency sine-wave generator
- 6. Low-frequency function generator
- 7. Digital ohmmeter

- 9. Coaxial cables (2 required)
- 10. BNC T connector
- 11. 10X attenuator

## BEFORE YOU BEGIN:

- (1) Perform the Performance Check Power-Up Sequence.
- (2) Refer to Section 6, Instrument Options and the Change Information at the rear of this manual for any modifications which may affect this procedure.
- (3) See **TEST POINT AND ADJUSTMENT LOCATIONS** foldout page in Section 8, Diagrams and Circuit Board Illustrations.

## TRIGGERING SYSTEM PRELIMINARY CONTROL SETTINGS:

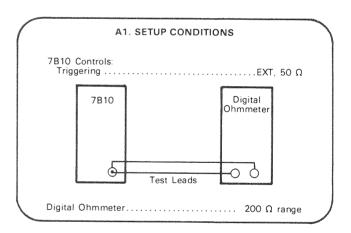
7B10 Time Base
TRIGGERING AUTO, AC, EXT, OUT 50Ω
LEVEL Midrange
TIME/DIV
VARIABLE CAL ÎN
MAG X1
POSITION Midrange
HOLD OFFMIN

## Oscilloscope Mainframe

Vertical (plug-in) Midrange
Intensity Visible display
Focus Well defined display

# A1. CHECK EXTERNAL TRIGGER INPUT RESISTANCE

#### SETUP CONDITIONS



a. CHECK—That the input resistance is  $50\Omega$  within 2% (1 $\Omega$ ).

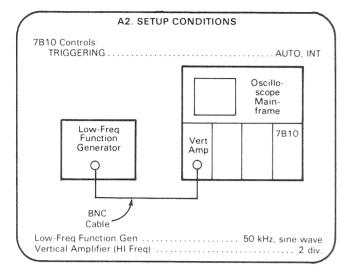
**5-8** @

### A2. CHECK TRIGGERING MODES

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



- a. Set the TRIGGERING LEVEL control for a stable display (TRIG'D light on).
- b. CHECK—For a free-running display with the TRIG'D light off when the TRIGGERING LEVEL control is set fully clockwise and fully counterclockwise.
- c. Set the TRIGGERING MODE to NORM.
- d. Set the TRIGGERING LEVEL control for a stable display (TRIG'D light on).
- e. CHECK—For no display (TRIG'D light off) when the TRIGGERING LEVEL control is set fully clockwise and fully counterclockwise.
- f. Set the TRIGGERING LEVEL control for a stable display (TRIG'D light on).
- g. Set the TRIGGERING MODE to SINGLE SWP and the SOURCE to EXT.
- h. CHECK—Press the SINGLE SWP RESET push button and check that the READY light is on.
- i. CHECK—For one sweep and that the READY light is out after completion of that sweep when the INT SOURCE push button is pressed (oscilloscope intensity may need to be increased to view the single-sweep display).

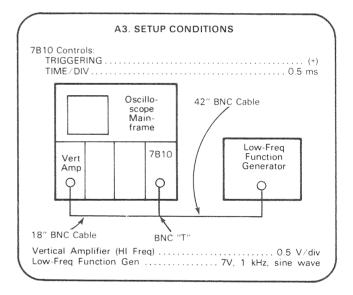
- j. Remove the low-frequency function generator cable from the vertical amplifier unit and connect the high-frequency sine-wave generator to the amplifier input with a 10X attenuator.
- k. Set the time base TRIGGERING MODE to HF SYNC and the TIME/DIV to 2 ns.
- I. Set the high-frequency sine-wave generator and amplifier plug-in unit deflection factor for approximately a 1-division display of 250 MHz signal.
- m. CHECK—For a stable display (TRIG'D light on) at all positions of the TRIGGERING LEVEL control.
- n. Set the TRIGGERING MODE to AUTO, SOURCE to EXT, and TIME/DIV to 10  $\mu s_{\rm c}$

#### A3. CHECK EXTERNAL LEVEL RANGE

## SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



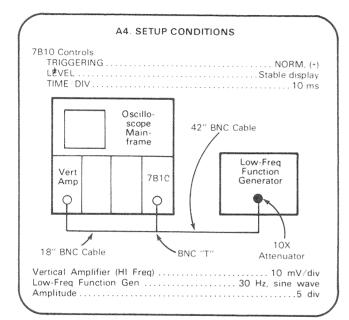
- a. CHECK—That all levels of the positive slope may be selected for the sweep starting point as the TRIGGERING LEVEL control is rotated throughout its range (indicates an external level range of at least plus and minus 1.5 volts). Check that the display is not triggered at either end of the LEVEL control rotation.
- b. CHECK—Change the TRIGGERING SLOPE to (-) and repeat part a for the negative slope of the waveform.

## A4. CHECK EXTERNAL TRIGGERING SENSITIVITY

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



- a. **CHECK**—Set the TRIGGERING MODE to AUTO and check for a stable display (TRIG'D light on) with the COUPLING push button set to:
- 1. AC
- 2. AC HF REJ
- 3. DC

(Set TRIGGERING LEVEL control as necessary).

- b.  $\mbox{CHECK}\mbox{--}\mbox{Change}$  the TRIGGERING SLOPE to (-) and repeat part a.
- c. Disconnect the low-frequency function generator from the 10X attenuator and connect the high-frequency sinewave generator to the 10X attenuator.
- d. Set the SLOPE to (+) and the TIME/DIV switch to 2 ns.
- e. Set the high-frequency sine-wave generator for a 5-division display (50 millivolts) at 250 megahertz.

- f. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set the TRIGGERING LEVEL control as necessary.)

- g. CHECK—Set the SLOPE switch to (-) and repeat part f.
- h. Set the amplifier plug-in unit deflection factor to 50 millivolts/division and set the high frequency sine-wave generator for a 3-division display (150 millivolts) at 1 GHz.
- i. **CHECK**—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary)

- j. CHECK—Set the SLOPE switch to (+) and repeat part i.
- k. Set the TRIGGERING MODE to HF SYNC and the amplifier plug-in unit deflection factor to 10 millivolts/division.
- I. Set the high-frequency sine-wave generator for a 7.5 division display (75 millivolts) at 1 GHz.
- m. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set the TRIGGERING LEVEL control as necessary.)

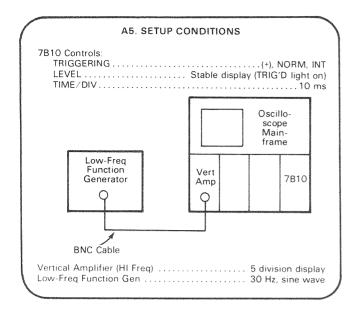
n. CHECK-Set the SLOPE switch to (-) and repeat part m.

## A5. CHECK INTERNAL TRIGGERING SENSITIVITY

#### SETUP CONDITIONS

### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



- a. CHECK—Set the TRIGGERING MODE to AUTO and check for a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC HF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary.)

- b. Change the TRIGGERING SLOPE to (-) and repeat part a.
- c. Disconnect the low-frequency function generator and connect the high-frequency sine-wave generator to the amplifier plug-in unit input. d. Set the TRIGGERING SLOPE to (+) and the TIME/DIV switch to 2 ns.
- d. Set the TRIGGERING SLOPE to (+) and the TIME/DIV switch to 2 ns.
- e. Set the high-frequency sine-wave generator and the amplifier plug-in unit deflection factor for a 0.5-division display at 250 megahertz (use 10X attenuator).
- f. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary.)

- g.  $\mbox{CHECK}\mbox{--}\mbox{Set}$  the TRIGGERING SLOPE to (-) and repeat part f.
- h. Set the high-frequency sine-wave generator and the amplifier plug-in unit deflection factor for a 1.5 division display at 1 GHz.

- i. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
- k. Set the TRIGGERING MODE switch to HF SYNC and the high-frequency sine-wave generator and amplifier plug-in unit deflection factor for a 0.3-division display at 1 GHz.
- I. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set the TRIGGERING LEVEL control as necessary.)

- m. CHECK—Set the SLOPE to (-) and repeat part I.
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

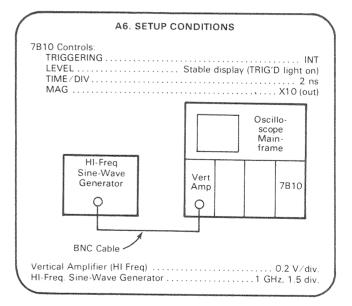
(Set TRIGGERING LEVEL control as necessary.)

j. CHECK—Set the TRIGGERING SLOPE to (+) and repeat part i.

# A6. CHECK INTERNAL TRIGGER JITTER SETUP CONDITIONS

#### NOTE

Partial procedure: first perform the Triggering System Preliminary Control Settings, then proceed with the following instructions.



a. **CHECK**—For a stable display with no more than 0.15 division (30 picosecond) of jitter.

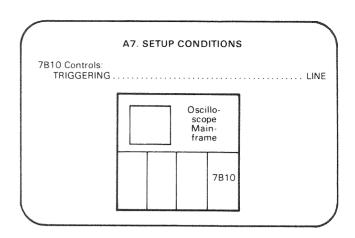
## A7. CHECK LINE TRIGGERING

SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.

- a. **CHECK**—Set the TRIGGERING LEVEL to approximately midrange and check that the TRIG'D light is on.
- b. CHECK—That the display is not triggered (TRIG'D light off) at either end of the TRIGGERING LEVEL control rotation.



## **B. HORIZONTAL SYSTEM**

Equipment Required: (Numbers correspond to test equipment listed in Table 5-2)

1. Oscilloscope Mainframe

- 8. Coaxial Cables (1 required)
- 2. High Frequency Amplifier Plug-in Unit
- 11. Screwdriver

5. Time-Mark Generator

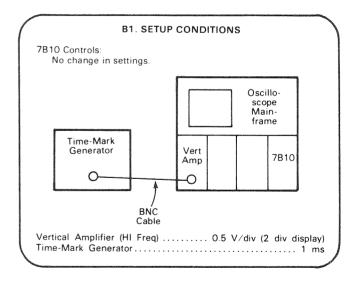
## BEFORE YOU BEGIN:

- (1) Perform the Performance Check Power-Up Sequence (not necessary if continuing Performance Check.)
- (2) Refer to Section 6, Instrument Options and the Change Information at the rear of the manual for any modifications which may affect this procedure.
- (3) See **TEST POINT AND ADJUSTMENT LOCATIONS** foldout page in Section 8, Diagrams and Circuit Board Illustrations.

## HORIZONTAL SYSTEM PRELIMINARY CONTROL SETTINGS:

7B10 Time Base
TRIGGERINGAUTO, AC, INT
LEVELMidrange
TIME/DIV 1 ms
MAG X1
VARIABLE CAL IN
HOLD OFFMIN
Oscilloscope Mainframe Vertical (plug-in)
Intensity

# B1. SET BASIC SWEEP CALIBRATION SETUP CONDITIONS



- a. **EXAMINE**—For 1 marker/division over center 8 divisions (position as necessary).
- b. Set the front-panel SWP CAL adjustment for exactly 1 marker/ division over the center 8 divisions (position as necessary).

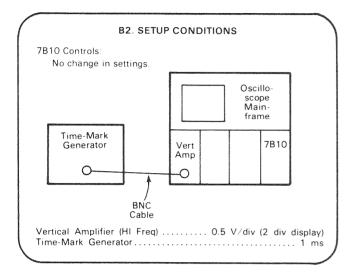
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## B2. CHECK SWEEP LENGTH AND POSITIONING RANGE

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



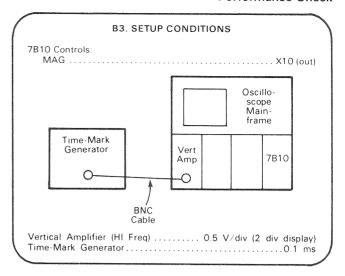
- a. Horizontally position the display to place the second time marker to the first graticule line.
- b. **CHECK**—That the end of the sweep is beyond 9.2 graticule divisions (indicates sweep length of at least 10.2 divisions.)
- c. Set the POSITION and FINE controls fully clockwise.
- d. **CHECK**—The start of sweep must be to the right of graticule center.
- e. Set the POSITION and FINE controls fully counterclockwise.
- f. CHECK—The end of sweep must be to the left of graticule center.

## B3. CHECK MAGNIFIER GAIN AND REGISTRATION

## SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



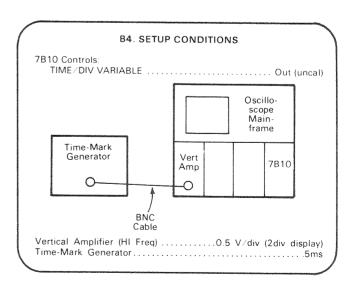
- a. CHECK—For 1 marker/division over the center 8 divisions of display (position as necessary).
- b. Set the time-mark generator for 5-millisecond markers. Align the center time marker with graticule center.
- c. **CHECK**—Set the MAG switch to X1 and check that the center time marker is at graticule center within 0.5 division.

## B4. CHECK VARIABLE TIME/DIVISION AND VARIABLE HOLD OFF

### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



## Calibration Part I—7B10 Performance Check

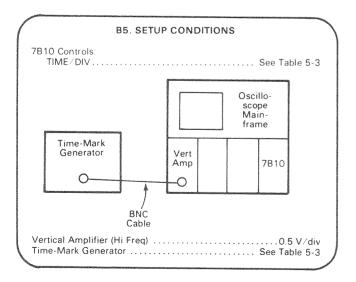
- a. Set the VARIABLE control fully clockwise and note 3 time markers in 10 graticule divisions.
- b. **CHECK**—Set the VARIABLE control fully counterclockwise and check for 2 divisions or less between 5-millisecond markers.
- c. Press the VARIABLE control in for calibrated sweep rates.
- d. Set the TRIGGERING LEVEL control for a free-running display (TRIG'D light off).
- e. Set the HOLD OFF control fully counterclockwise.
- f. CHECK—Rotate the HOLD OFF control slowly clockwise throughout its range and check that the display (3 time markers in 10 divisions) will stabilize at least 3 times throughout the range of the HOLD OFF control (disregard any slow drift).
- g. Set the HOLD OFF control counterclockwise to MIN and set the TRIGGERING LEVEL control for a stable display.

#### **B5. CHECK SWEEP TIMING**

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



#### NOTE

The tolerances given in Table 5-3 are for an ambient temperature range of +15° to +35° C. If outside this range, see the Specification section for applicable tolerances.

a. CHECK—Using the TIME/DIV setting and time-mark generator settings from Table 5-3, check sweep accuracy for 1 time mark/ division over the center 8 divisions within the tolerance given in Table 5-3. Set the POSITION controls and TRIGGERING LEVEL control as necessary for a stable display aligned with the vertical graticule lines

#### NOTE

If the time-mark generator used does not have 1-2-5 sequence markers, apply 1 unit markers in place of 2 unit markers and check for 2 markers/division, over the center eight divisions of display, to the tolerances given in Table 5-3.

TABLE 5-3
Sweep Timing

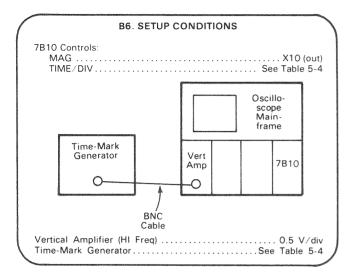
		Tolerance
Time/Div	Time Markers	(+15° to +35° C)
2 ns	2 ns	Within 0.24 div
5 ns	5 ns	Within 0.24 div
10 ns	10 ns	Within 0.16 div
20 ns	20 ns	Within 0.16 div
50 ns	50 ns	Within 0.16 div
.1 <i>μ</i> s	.1 μs	Within 0.16 div
.2 μs	.2 μs	Within 0.16 div
.5 <i>μ</i> s	.5 μs	Within 0.16 div
1 μs	1 μs	Within 0.16 div
2 μs	2 μs	Within 0.16 div
5 μs	5 <i>μ</i> s	Within 0.16 div
10 <i>μ</i> s	10 <i>μ</i> s	Within 0.16 div
20 <i>μ</i> s	20 <i>μ</i> s	Within 0.16 div
50 <i>μ</i> s	50 μs	Within 0.16 div
.1 ms	.1 ms	Within 0.16 div
.2 ms	.2 ms	Within 0.16 div
.5 ms	.5 ms	Within 0.16 div
1 ms	1 ms	Within 0.16 div
2 ms	2 ms	Within 0.16 div
5 ms	5 ms	Within 0.16 div
10 ms	10 ms	Within 0.16 div
20 ms	20 ms	Within 0.16 div
50 ms	50 ms	Within 0.16 div
.1 ຮ	.1 s	Within 0.16 div
.1 s	.1 s	Within 0.16 div
.2 s	.2 s	Within 0.16 div

#### **B6. CHECK MAGNIFIED SWEEP TIMING**

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



#### NOTE

The tolerances in Table 5-4 are for an ambient temperature range of +15°to +35° C. If outside this range, see the Specification section for applicable tolerances.

a. CHECK—Using the TIME/DIV settings and time-mark generator settings in Table 5-4, check magnified sweep accuracy for 1 time mark/division, over the center 8 divisions, within the tolerance given in Table 5-4. Set the POSITION controls and TRIGGERING LEVEL control as necessary for a stable display aligned with the vertical graticule lines.

#### NOTE

The 7B10 may be operated as a delayed sweep unit (B horizontal compartment) with a companion delaying sweep unit (A horizontal compartment). To check 7B10 delayed sweep operation, refer to a delaying sweep checkout procedure in the Operating Instructions or calibration sections of the instruction manual for the delaying sweep unit.

TABLE 5-4
Magnified Sweep Timing

		Tolerance
Time/Div	Time Markers	(+15° to +35° C)
2 ns	1 ns <sup>1</sup>	Within 0.2 div
5 ns	1 ns	Within 0.32 div
10 ns	1 ns	Within 0.24 div
20 ns	2 ns	Within 0.24 div
50 ns	5 ns	Within 0.24 div
.1 μs	10 ns	Within 0.24 div
.2 μs	20 ns	Within 0.24 div
.5 <i>μ</i> s	50 ns	Within 0.24 div
1 μs	.1 μs	Within 0.24 div
2 μs	.2 μs	Within 0.24 div
5 μs	.5 μs	Within 0.24 div
10 μs	1 μs	Within 0.24 div
20 μs	2 μs	Within 0.24 div
50 μs	5 <i>μ</i> s	Within 0.24 div
.1 ms	10 <i>μ</i> s	Within 0.24 div
.2 ms	20 <i>μ</i> s	Within 0.24 div
.5 <b>ms</b>	50 <i>μ</i> s	Within 0.24 div
1 ms	.1 ms	Within 0.24 div
2 ms	.2 ms	Within 0.24 div
5 ms	.5 ms	Within 0.24 div
10 ms	1 ms	Within 0.24 div
20 ms	2 ms	Within 0.24 div
50 ms	5 ms	Within 0.24 div
.1 s	10 ms	Within 0.24 div
.2 s	20 ms	Within 0.24 div

<sup>&</sup>lt;sup>1</sup>Check for 1 cycle in 5 divisions at 200 ps/div over center 8 divisions.

This completes the Performance Check procedure.

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## PART II—ADJUSTMENT AND PERFORMANCE CHECK

The following procedure (Part II—Adjustment and Performance Check) provides the information necessary to: (1) verify that the instrument meets the electrical specifications, (2) verify that all controls function properly, and (3) perform all internal adjustments.

Part I—Performance Check verifies electrical specifications without removing instrument covers or making internal adjustments. All tolerances given are as specified in the Specification tables (section 1) in this manual.

A separate Operators Checkout Procedure is provided in the Operators Manual for familiarization with the instrument and also to verify that all controls and connectors function properly.

See Preliminary Information, at the beginning of this section, for further information.

# ADJUSTMENT AND PERFORMANCE CHECK PROCEDURE INDEX

### PAGE A. TRIGGERING SYSTEM ..... 5-17 1. Check External Trigger Input Resistance ..... 5-17 2. Adjust External Trigger Compensation (C117) ..... 5-18 3. Adjust Sensitivity (R147)................ 5-18 4. Check Triggering Modes ..... 5-18 5. Check External Level Range . . . . . . . . . . 5-19 6. Check External Triggering Sensitivity ...... 5-19 7. Check Internal Triggering Sensitivity ...... 5-20 8. Check Internal Trigger Jitter ..... 5-21 9. Check Line Triggering ...... 5-21 B. HORIZONTAL SYSTEM ..... 5-22 1. Set Basic Sweep Calibration ...... 5-22 2. Check Sweep Length and Positioning Range ..... 5-23 3. Check/Adjust Magnifier Gain and Registration (R460, R470) . . . . . . . . . . . . 5-23 4. Check Variable Time Division and Variable Hold Off ......5-23 5. Adjust Sweep Timing (C361, R305, R310, R480)......5-24 6. Check Sweep Timing ..... 5-24 7. Check Magnified Sweep Timing ...... 5-25

## ADJUSTMENT AND PERFORMANCE CHECK POWER-UP SEQUENCE

#### NOTE

The performance of this instrument can be checked to any ambient temperature from 0° to +50° C unless otherwise stated. Adjustments must be performed at an ambient temperature between +20° and +30° C for specified accuracies.

- 1. Install a high impedance amplifier unit in the left vertical compartment of the oscilloscope mainframe.
- 2. Install the 7B10 in the B horizontal compartment of the mainframe.
- 3. Set the mainframe vertical mode switch to display the left vertical unit and the horizontal mode switch to display the B horizontal unit. Set the mainframe intensity controls fully counterclockwise and set the trigger source switches to vertical mode.
- 4. Turn on the mainframe and allow at least 20 minutes warmup before beginning the procedure.

## A. TRIGGERING SYSTEM

Equipment Required: (Numbers correspond to test equipment listed in Table 5-2)

- 1. Oscilloscope mainframe
- 2. High-frequency amplifier plug-in unit
- 3. High-impedance amplifier plug-in unit
- 4. High-frequency sine-wave generator
- 6. Low-frequency function generator
- 7. Rigid plug-in extender

- 8. Coaxial cables (2 required)
- 9. BNC T connector
- 11. Screwdriver
- 12. 10X Probe
- 13. Low-capacitance Screwdriver

## BEFORE YOU BEGIN:

- (1) Perform the Adjustment and Performance Check Power-Up Sequence.
- (2) Refer to Section 6, Instrument Options and the Change Information at the rear of the manual for any modifications which may effect this procedure.
- (3) See **TEST POINT AND ADJUSTMENT LOCATIONS** foldout page in Section 8, Diagrams and Circuit Board Illustrations.

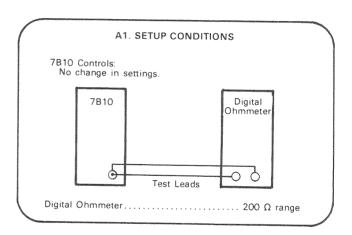
## TRIGGERING SYSTEM PRELIMINARY CONTROL SETTINGS:

7B10 Time Base

TRIGGERING	AUTO, AC, EXT
TIME/DIV MAG	OUT 50 Ω
Vertical (plug-in)	rameVisible displayMidrangeWell defined display

## A1. CHECK EXTERNAL TRIGGER INPUT RESISTANCE

#### SETUP CONDITIONS



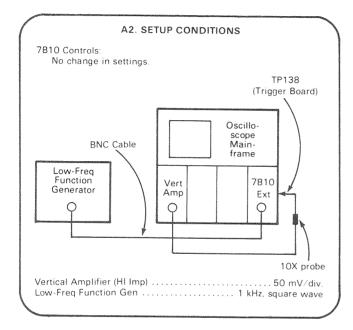
a. CHECK—That the input resistance is  $50\Omega$  within 2% (1 $\Omega$ ).

## A2. ADJUST EXTERNAL TRIGGER COMPENSATION (C117)

### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



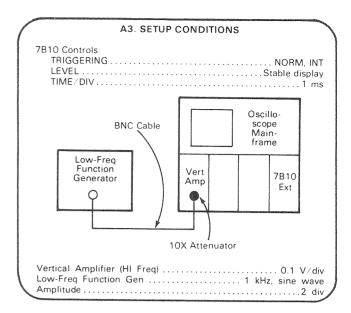
- a. Set the low-frequency function generator and the amplifier plug-in unit for a 4 to 6 division display at 1 kHz (position as necessary).
- b. **EXAMINE**—Display pulse for minimum spike and optimum flat top.
- c. **ADJUST**—C117 (External Trigger Comp) on the Trigger circuit board for minimum spike and optimum flat top on the displayed pulse.

## A3. ADJUST TRIGGER SENSITIVITY (R147)

### SETUP CONDITIONS

### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



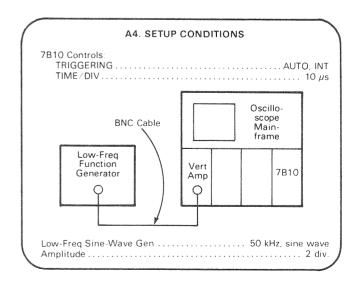
- a. Set the amplifier plug-in unit deflection factor for 1V (0.2 division).
- b. **EXAMINE**—For a stable 0.2 division display.
- c. ADJUST—R147 (Sensitivity) on the Trigger circuit board for a stable 0.2 division display.

#### A4. CHECK TRIGGERING MODES

## SETUP CONDITIONS

### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



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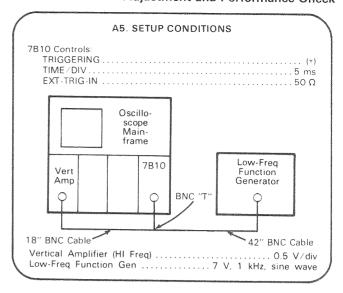
- a. Set the TRIGGERING LEVEL control for a stable display (TRIG'D light on).
- b. CHECK—For a free-running display with the TRIG'D light off when the TRIGGERING LEVEL control is set fully clockwise and fully counterclockwise.
- c. Set the TRIGGERING MODE to NORM
- d. Set the TRIGGERING LEVEL control for a stable display (TRIG'D light on).
- e. CHECK—For no display (TRIG'D light off) when the TRIGGERING LEVEL control is set fully clockwise and fully counterclockwise.
- f. Set the TRIGGERING LEVEL control for a stable display (TRIG'D light on).
- g. Set the TRIGGERING MODE to SINGLE SWP and the SOURCE to EXT.
- h. CHECK—Press the SINGLE SWP RESET push button and check that the READY light is on.
- i. CHECK—For one sweep and that the READY light is out after completion of that sweep when the INT SOURCE push button is pressed (oscilloscope intensity may need to be increased to view the single-sweep display).
- j. Remove the low-frequency function generator cable from the amplifier plug-in unit and connect the high-frequency sine-wave generator to the amplifier input with a 10X attenuator.
- k. Set the TRIGGERING MODE to HF SYNC and the TIME/DIV to 2 ns.
- I. Set the high-frequency sine-wave generator and amplifier plug-in unit deflection factor for approximately a 1-division display of 250 MHz signal.
- m. **CHECK**—For a stable display (TRIG'D light on) at all positions of the TRIGGERING LEVEL control.
- n. Set the TRIGGERING MODE to AUTO, SOURCE to EXT, and TIME/DIV to 10  $\mu s.$

#### A5. CHECK EXTERNAL LEVEL RANGE

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



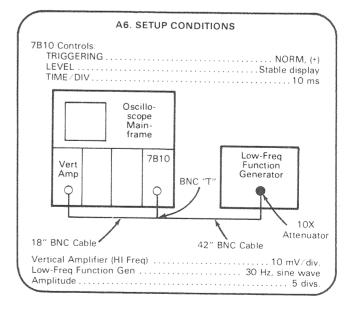
- a. CHECK—That all levels of the positive slope may be selected for the sweep starting point as the TRIGGERING LEVEL control is rotated throughout its range (indicates an external level range of at least plus and minus 1.5 volts). Check that the display is not triggered at either end of the TRIGGERING LEVEL control rotation.
- b. CHECK—Change the TRIGGERING SLOPE to (-) and repeat part a for the negative slope of the waveform.

# A6. CHECK EXTERNAL TRIGGERING SENSITIVITY

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



### Calibration Part II—7B10 Adjustment and Performance Check

- a. CHECK—Set the TRIGGERING MODE to AUTO and check for a stable display (TRIG'D light on) with the COUPLING push button set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary).

- b. CHECK—Change the TRIGGERING SLOPE to (-) and repeat part a.
- c. Disconnect the low-frequency function generator from the 10X attenuator and connect the high-frequency sinewave generator to the 10X attenuator.
- d. Set the SLOPE to (+) and the TIME/DIV switch to 2 ns.
- e. Set the high-frequency sine-wave generator for a 5-division display (50 millivolts) at 250 megahertz.
- f. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set the TRIGGERING LEVEL control as necessary.)

- g. CHECK—Set the SLOPE switch to (-) and repeat part f.
- h. Set the amplifier plug-in unit deflection factor to 50 millivolts/division and set the high-frequency sine-wave generator for a 3-division display (150 millivolts) at 1 GHz.
- i. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary.)

- i. CHECK—Set the SLOPE switch to (+) and repeat part i.
- k. Set the TRIGGERING MODE to HF SYNC and the amplifier plug-in unit deflection factor to 10 millivolts/division.
- I. Set the high-frequency sine-wave generator for a 7.5 division display (75 millivolts) at 1 GHz.
- m. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary)

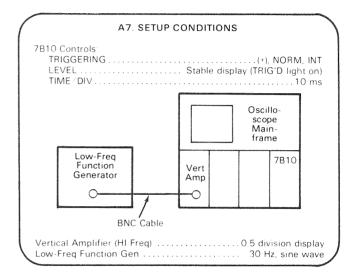
n. CHECK—Set the SLOPE switch to (-) and repeat part

### A7. CHECK INTERNAL TRIGGERING SENSITIVITY

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



- a. **CHECK**—Set the TRIGGERING MODE to AUTO and check for a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary.)

- b. Change the TRIGGERING SLOPE to (-) and repeat part a.
- c. Disconnect the low-frequency function generator and connect the high-frequency sine-wave generator to the amplifier plug-in unit input.
- d. Set the SLOPE to (+) and the TIME/DIV switch to 2 ns.
- e. Set the high-frequency sine-wave generator and the amplifier plug-in unit deflection factor for a 0.5-division display at 250 megahertz (use 10X attenuator).

- f. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary).

- g. CHECK—Set the SLOPE to (-) and repeat part f.
- h. Set the high-frequency sine-wave generator and the amplifier plug-in unit deflection factor for a 1.5 division display at 1 GHz.
- i. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary.)

- j. CHECK—Set the SLOPE to (+) and repeat part i.
- k. Set the TRIGGERING MODE switch to HF SYNC and the high-frequency sine-wave generator and amplifier plug-in unit deflection factor for a 0.3 division display at 1 GHz.
- I. CHECK—For a stable display (TRIG'D light on) with the COUPLING switch set to:
  - 1. AC
  - 2. AC LF REJ
  - 3. DC

(Set TRIGGERING LEVEL control as necessary.)

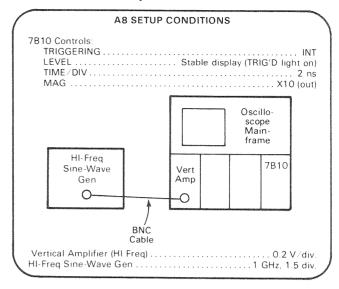
m. CHECK—Set the SLOPE to (+) and repeat part i.

#### A8. CHECK INTERNAL TRIGGER JITTER

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



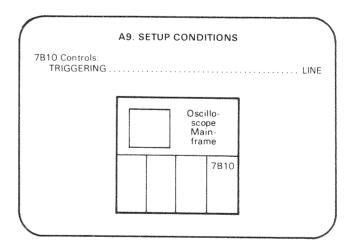
a. **CHECK**—For a stable display with no more than 0.15 division (30 picosecond) of jitter.

#### A9. CHECK LINE TRIGGERING

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Triggering System Preliminary Control Settings, then proceed with the following instructions.



- a. **CHECK**—Set the TRIGGERING LEVEL to approximately midrange and check that the TRIG'D light is on.
- b. **CHECK**—That the display is not triggered (TRIG'D light off) at either end of the TRIGGERING LEVEL control rotation.

### **B. HORIZONTAL SYSTEM**

Equipment Required: (Numbers correspond to test equipment listed in Table 5-2)

1. Oscilloscope mainframe

- 8. Coaxial cables (2 required)
- 2. High-Frequency amplifier plug-in unit
- 11. Screwdriver

- 5. Time-mark generator
- 7. Rigid plug-in extender

#### BEFORE YOU BEGIN:

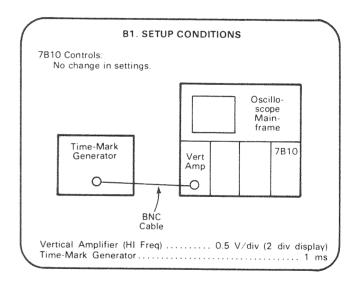
- (1) Perform the Power-Up Sequence preceding the Adjustment and Performance Check Procedure. (Not necessary if continuing Adjustment and Performance Check.)
- (2) Refer to Section 6, Instrument Options and the Change Information at the rear of the manual for any modifications which may affect this procedure.
- (3) See **TEST POINT AND ADJUSTMENT LOCATIONS** foldout page in Section 8, Diagrams and Circuit Board Illustrations.

# HORIZONTAL SYSTEM PRELIMINARY CONTROL SETTINGS:

10 Time Base TRIGGERINGAUTO, AC, INT LEVELStable display (TRIG'D light on) TIME/DIV1 ms
TIME/DIV       1 ms         MAG       X1         VARIABLE       CAL IN
POSITION
cilloscope Mainframe Vertical position (plug-in) Midrange Intensity Visible display Focus

#### **B1. SET BASIC SWEEP CALIBRATION**

#### SETUP CONDITIONS



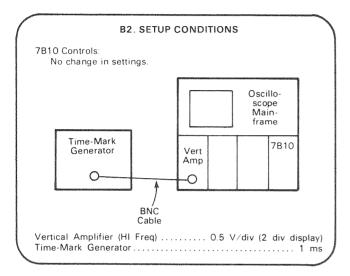
- a. **EXAMINE**—For 1 marker/division over center 8 divisions (position as necessary).
- b. Set the front-panel SWP CAL adjustment for exactly 1 marker/division over the center 8 divisions (position as necessary).

# B2. CHECK SWEEP LENGTH AND POSITIONING RANGE

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



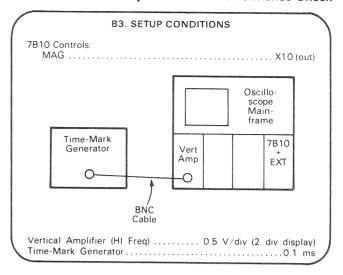
- a. Horizontally position the display to place the second time marker to the first graticule line.
- b. **CHECK**—That the end of the sweep is beyond 9.2 graticule divisions (indicates sweep length of at least 10.2 divisions).
- c. Set the POSITION and FINE controls fully clockwise.
- d. **CHECK**—The start of sweep must be to the right of graticule center.
- e. Set the POSITION and FINE controls fully counterclockwise.
- f. CHECK—The end of sweep must be to the left of graticule center.

# B3. CHECK/ADJUST MAGNIFIER GAIN AND REGISTRATION (R460, R470)

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



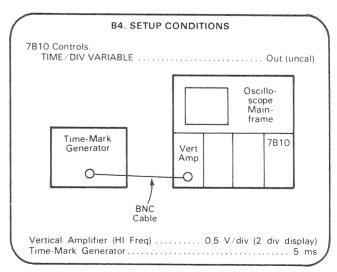
- a. CHECK—For 1 marker/division over the center 8 divisions of display (position as necessary).
- b. **ADJUST**—R460 (Mag Gain) for 1 marker/division over the center 8 divisions of display.
- c. Set the time-mark generator for 5-millisecond markers. Align the center time marker with graticule center.
- d.  $\mbox{CHECK}\mbox{--}\mbox{Set}$  the MAG switch to X1 and check that the center time marker is at graticule center within 0.5 division.
- e. ADJUST—R470 (Mag Reg) to align the center time marker with graticule center.

### B4. CHECK VARIABLE TIME/DIVISION AND VARIABLE HOLD OFF

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



### Calibration Part II—7B10 Adjustment and Performance Check

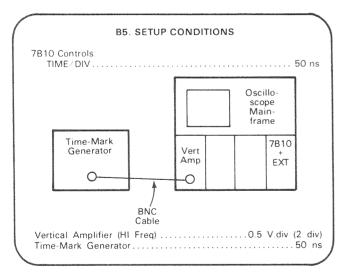
- a. Set the VARIABLE control fully clockwise and note 3 time markers in 10 graticule divisions.
- b. CHECK—Set the VARIABLE control fully counterclockwise and check for 2 divisions or less between 5 millisecond markers.
- c. Press the VARIABLE control in for calibrated sweep rates.
- d. Set the TRIGGERING LEVEL control for a free-running display (TRIG'D light off).
- e. Set the HOLD OFF control fully counterclockwise.
- f. CHECK—Rotate the HOLD OFF control slowly clockwise throughout its range and check that the display (3 time markers in 10 divisions) will stabilize at least 3 times throughout the range of the HOLD OFF control (disregard any slow drift).
- g. Set the HOLD OFF control counterclockwise to MIN and set the TRIGGERING LEVEL control for a stable display.

# B5. ADJUST SWEEP TIMING (C361, R305, R310, R480)

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



- a. **EXAMINE**—For 1 marker/division over the center 8 divisions of display (position as necessary).
- b. **ADJUST**—C361 (50 ns Timing) for 1 marker/division over the center 8 divisions of display (position as necessary).

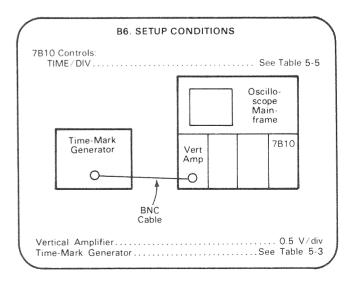
- c. Set the TIME/DIV switch to 5 nanoseconds and set the time-mark generator for 5 nanosecond markers.
- d. **EXAMINE**—For 1 marker/division over the center 8 divisions of display (position as necessary).
- e. ADJUST—R480 (5 ns Timing) for 1 marker/division over the center 8 divisions of display (position as necessary).
- f. Set the TIME/DIV switch to 10  $\mu s$  and set the timemark generator for 10  $\mu s$  markers.
- g. **EXAMINE**—For 1 marker/division over the center 8 divisions of display (position as necessary).
- h. ADJUST—R310 (10  $\mu$ s Timing) for 1 marker/division over the center 8 divisions of display (position as necessary).
- i. Set the TIME/DIV switch to 10 ms, the TRIGGERING MODE to NORM, and set the time-mark generator for 10 millisecond markers.
- j. **EXAMINE**—For 1 marker/division over the center 8 divisions of display (position as necessary).
- k. **ADJUST**—R305 (10 ms Timing) for 1 marker/division over the center 8 divisions of display (position as necessary).

#### **B6. CHECK SWEEP TIMING**

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



#### NOTE

The tolerances given in Table 5-5 are for an ambient temperature range of +15° to +35° C. If outside this range, see the Specification section for applicable tolerances.

a. CHECK—Using the TIME/DIV setting and time-mark generator settings from Table 5-5, check sweep accuracy for 1 time mark/ division over the center 8 divisions within the tolerance given in Table 5-5. Set the POSITION controls and TRIGGERING LEVEL control as necessary for a stable display aligned with the vertical graticule lines.

#### NOTE

If the time-mark generator used does not have 1-2-5 sequence markers, apply 1 unit markers in place of 2 unit markers and check for 2 markers/division, over the center eight divisions of display, according to the tolerances given in Table 5-5.

#### TABLE 5-5 Sweep Timing

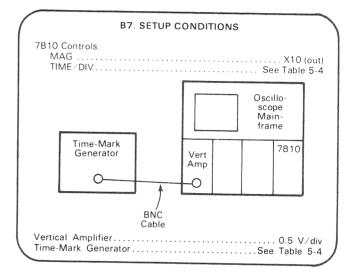
Time/Div	Time Markers	Tolerance (+15° to +35° C)
2 ns	2 ns	Within 0.24 div
5 ns	5 ns	Within 0.24 div
10 ns	10 ns	Within 0.16 div
20 ns	20 ns	Within 0.16 div
50 ns	50 ns	Within 0.16 div
.1 μs	.1 μs	Within 0.16 div
.2 μs	.2 μs	Within 0.16 div
.5 <i>μ</i> s	.5 μs	Within 0.16 div
1 μs	1 μs	Within 0.16 div
2 μs	2 μs	Within 0.16 div
5 μs	5 μs	Within 0.16 div
10 <i>μ</i> s	10 μs	Within 0.16 div
20 <i>μ</i> s	20 <i>μ</i> s	Within 0.16 div
50 <i>μ</i> s	50 <i>μ</i> s	Within 0.16 div
.1 ms	.1 ms	Within 0.16 div
.2 ms	.2 ms	Within 0.16 div
.5 ms	.5 ms	Within 0.16 div
1 ms	1 ms	Within 0.16 div
2 ms	2 ms	Within 0.16 div
5 ms	5 ms	Within 0.16 div
10 ms	10 ms	Within 0.16 div
20 ms	20 ms	Within 0.16 div
50 ms	50 ms	Within 0.16 div
.1 s	.1 s	Within 0.16 div
.1 s	.1 s	Within 0.16 div
	.2 s	

#### B7. CHECK MAGNIFIED SWEEP TIMING

#### SETUP CONDITIONS

#### NOTE

If the preceding step was not performed, first refer to the Horizontal System Preliminary Control Settings, then proceed with the following instructions.



#### NOTE

The tolerances in Table 5-6 are for an ambient temperature range of +15° to +35° C. If outside this range, see the Specification section for applicable tolerances.

a. CHECK—Using the TIME/DIV settings and time-mark generator settings in Table 5-6, check the magnified sweep accuracy for 1 time mark/division, over the center 8 divisions, within the tolerance given in Table 5-6. Set the POSITION controls and TRIGGERING LEVEL control as necessary for a stable display aligned with the vertical graticule lines.

#### NOTE

The 7B10 may be operated as a delayed sweep unit (B horizontal compartment). To check 7B10 delayed sweep operation, refer to a delaying sweep checkout procedure in the operating instructions or calibration sections of the instruction manual for the delaying sweep unit.

#### Calibration Part II—7B10 Adjustment and Performance Check

TABLE 5-6 Magnified Sweep Timing

Time/Div	Time Markers	Tolerance (+15° to +35° C)
2 ns	1 ns <sup>1</sup>	Within 0.2 div
5 ns	1 ns	Within 0.32 div
10 ns	1 ns	Within 0.24 div
20 ns	2 ns	Within 0.24 div
50 ns	5 ns	Within 0.24 div
.1 <i>μ</i> s	10 ns	Within 0.24 div
.2 μs	20 ns	Within 0.24 div
.5 <i>μ</i> s	50 ns	Within 0.24 div
1 μs	.1 μs	Within 0.24 div
2 μs	.2 µs	Within 0.24 div
5 μs	.5 μs	Within 0.24 div
10 <i>μ</i> s	1 <i>μ</i> s	Within 0.24 div
20 μs	2 μs	Within 0.24 div
50 μs	5 μs	Within 0.24 div
.1 ms	10 μs	Within 0.24 div
.2 ms	20 μs	Within 0.24 div
.5 <b>ms</b>	50 μs	Within 0.24 div
1 ms	.1 ms	Within 0.24 div
2 ms	.2 ms	Within 0.24 div
5 ms	.5 ms	Within 0.24 div
10 ms	1 ms	Within 0.24 div
20 ms	2 ms	Within 0.24 div
50 ms	5 ms	Within 0.24 div
.1 s	10 ms	Within 0.24 div
.2 s	20 ms	Within 0.24 div

<sup>&</sup>lt;sup>1</sup>Check for 1 cycle in 5 divisions at 200 ps/div over center 8 divisions.

This completes the Adjustment and Performance Check procedure.

# **INSTRUMENT OPTIONS**

No options were available for this instrument at the time of this printing.

Information on any subsequent options may be found in the CHANGE INFORMATION section in the back of this manual.

# REPLACEABLE ELECTRICAL PARTS

#### PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

Change information, if any, is located at the rear of this manual.

#### LIST OF ASSEMBLIES

A list of assemblies can be found at the beginning of the Electrical Parts List. The assemblies are listed in numerical order. When the complete component number of a part is known, this list will identify the assembly in which the part is located.

### CROSS INDEX-MFR. CODE NUMBER TO MANUFACTURER

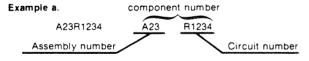
The Mfr. Code Number to Manufacturer index for the Electrical Parts List is located immediately after this page. The Cross Index provides codes, names and addresses of manufacturers of components listed in the Electrical Parts List.

#### **ABBREVIATIONS**

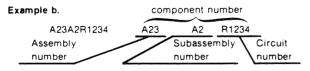
Abbreviations conform to American National Standard Y1.1

#### COMPONENT NUMBER (column one of the Electrical Parts List)

A numbering method has been used to identify assemblies, subassemblies and parts. Examples of this numbering method and typical expansions are illustrated by the following:



Read: Resistor 1234 of Assembly 23



Read: Resistor 1234 of Subassembly 2 of Assembly 23

Only the circuit number will appear on the diagrams and circuit board illustrations. Each diagram and circuit board illustration is clearly marked with the assembly number. Assembly numbers are also marked on the mechanical exploded views located in the Mechanical Parts List. The component number is obtained by adding the assembly number prefix to the circuit number.

The Electrical Parts List is divided and arranged by assemblies in numerical sequence (e.g., assembly A1 with its subassemblies and parts, precedes assembly A2 with its subassemblies and parts).

Chassis-mounted parts have no assembly number prefix and are located at the end of the Electrical Parts List

# TEKTRONIX PART NO. (column two of the Electrical Parts List)

Indicates part number to be used when ordering replacement part from Tektronix.

# SERIAL/MODEL NO. (columns three and four of the Electrical Parts List)

Column three (3) indicates the serial number at which the part was first used. Column four (4) indicates the serial number at which the part was removed. No serial number entered indicates part is good for all serial numbers.

#### NAME & DESCRIPTION (column five of the Electrical Parts List)

In the Parts List, an Item Name is separated from the description by a colon (:). Because of space limitations, an Item Name may sometimes appear as incomplete. For further Item Name identification, the U.S. Federal Cataloging Handbook H6-1 can be utilized where possible.

# MFR. CODE (column six of the Electrical Parts List)

Indicates the code number of the actual manufacturer of the part. (Code to name and address cross reference can be found immediately after this page.)

# MFR. PART NUMBER (column seven of the Electrical Parts List)

Indicates actual manufacturers part number.

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### CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER

Mfr.		William Cope Howell	TO MINIOT NOTONEM
Code	Manufacturer	Address	City, State, Zip Code
00853	SANGAMO WESTON INC COMPONENTS DIV ALLEN-BRADLEY CO TEXAS INSTRUMENTS INC SEMICONDUCTOR GROUP AMPEREX ELECTRONIC CORP FERROXCUBE DIV	SANGAMO RD	PICKENS SC 29671-9716
01121	ALLEN-BRADLEY CO	1201 S 2ND ST	MILWAUKEE WI 53204-2410
01295	TEXAS INSTRUMENTS INC	13500 N CENTRAL EXPY	DALLAS TX 75265
	SEMICONDUCTOR GROUP	PO BOX 655012	
02114	AMPEREX ELECTRONIC CORP FERROXCUBE DIV	5083 KINGS HWY	SAUGERTIES NY 12477
02735	RCA CORP SOLID STATE DIVISION	ROUTE 202	SOMERVILLE NJ 08876
03508	GENERAL ELECTRIC CO	W GENESEE ST	AUBURN NY 13021
04222	AMPEREX ELECTRONIC CORP FERROXCUBE DIV RCA CORP SOLID STATE DIVISION GENERAL ELECTRIC CO SEMI-CONDUCTOR PRODUCTS DEPT AVX CERAMICS DIV OF AVX CORP MOTOROLA INC	19TH AVE SOUTH	MYRTLE BEACH SC 29577
04713	MOTOROLA INC	5005 E MCDOWELL RD	PHOENIX AZ 85008-4229
05397	SEMI-CONDUCTOR PRODUCTS DEPT AVX CERAMICS DIV OF AVX CORP MOTOROLA INC SEMICONDUCTOR PRODUCTS SECTOR UNION CARBIDE CORP MATERIALS SYSTEMS DIV	11901 MADISON AVE	CLEVELAND OH 44101
07263	FAIRCHILD SEMICONDUCTOR CORP		
	NORTH AMERICAN SALES SUB OF SCHLUMBERGER LTD MS 118	10400 RIDGEVIEW C	COLEMINO CA 33014
07716		2850 MT PLEASANT AVE	BURLINGTON IA 52601
12697	CLAROSTAT MEG CO INC	LOWER WASHINGTON ST	DULLE NH U385U
14193	CAL-R INC	1601 OLYMPIC BLVD	SANTA MONICA CA 90406
15454	CLAROSTAT MFG CO INC CAL-R INC AMETEK INC RODAN DIV	721 N POPLAR ST	ORANGE CA 92668
19396	ILLINOIS TOOL WORKS INC	1205 MCCONVILLE RD	LYNCHBURG VA 24502-4535
19701	RODAN DIV ILLINOIS TOOL WORKS INC PAKTRON DIV MEPCO/CENTRALAB A NORTH AMERICAN PHILIPS CO	PO BOX 760	MINERAL WELLS TX 76067-0760
22229	SOLITRON DEVICES INC SEMICONDUCTOR GROUP SAN DIEGO OPERS	8808 BALBOA AVE	SAN DIEGO CA 92123
24546	CORNING GLASS WORKS	550 HIGH ST	BRADFORD PA 16701-3737
27014	NATIONAL SEMICONDUCTOR CORP	2900 SEMICONDUCTOR DR	SANTA CLARA CA 95051-0606
31918	ITT SCHADOW INC	8081 WALLACE RD	EDEN PRAIRIE MN 55344-2224
32293	INTERSIL INC SUB OF GENERAL ELECTRIC CO	10600 RIDGEVIEW COURT	CUPERTINO CA 95014-0704
32997	SOLITRON DEVICES INC SEMICONDUCTOR GROUP SAN DIEGO OPERS CORNING GLASS WORKS NATIONAL SEMICONDUCTOR CORP ITT SCHADOW INC INTERSIL INC SUB OF GENERAL ELECTRIC CO BOURNS INC TRIMPOT DIV HEWLETT-PACKARD CO	1200 COLUMBIA AVE	RIVERSIDE CA 92507-2114
50434	HEWLETT-PACKARD CO OPTOELECTRONICS DIV NEC AMERICA INC STETCO INC MATSUSHITA ELECTRIC CORP OF AMERICA	370 W TRIMBLE RD	SAN JOSE CA 95131
51984	NEC AMERICA INC	2741 PROSPERITY AVE	FAIRFAX VA 22031-4308
52763	STETCO INC	3344 SCHIERHORN	FRANKLIN PARK IL 60131
54473	MATSUSHITA ELECTRIC CORP OF AMERICA	ONE PANASONIC WAY PO BOX 1501	SECAUCUS NJ 07094-2917
57668	ROHM CORP	8 WHATNEY PO BOX 19515	IRVINE CA 92713
58361	GENERAL INSTRUMENT CORP OPTOELECTRONICS DIV	3400 HILLVIEW AVE	PALO ALTO CA 94304-1319
58854	GTE PRODUCTS CORP LIGHTING PRODUCTS GROUP	60 BOSTON ST	SALEM MA 01970-2147
59660	TUSONIX INC	7741 N BUSINESS PARK DR PO BOX 37144	TUCSON AZ 85740-7144
72982	ERIE SPECIALTY PRODUCTS INC	645 W 11TH ST	ERIE PA 16512
74970	JOHNSON E F CO	299 10TH AVE S W	WASECA MN 56093-2539
75042	IRC ELECTRONIC COMPONENTS PHILADELPHIA DIV	401 N BROAD ST	PHILADELPHIA PA 19108-1001
75915	TRW FIXED RESISTORS LITTELFUSE INC	800 E NORTHWEST HWY	DES PLAINES IL 60016-3049
76493	SUB TRACOR INC		
	BELL INDUSTRIES INC JW MILLER DIV	19070 REYES AVE PO BOX 5825	COMPTON CA 90224-5825
80009	TEKTRONIX INC	14150 SW KARL BRAUN DR PO BOX 500	BEAVERTON OR 97707-0001

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#### CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER

Mfr.

Code	Manufacturer	Address	City, State, Zip Code	
91637	DALE ELECTRONICS INC	2064 12TH AVE PO BOX 609	COLUMBUS NE 68601-3632	
TK2042	ZMAN & ASSOCIATES	7633 S 180TH	KENT WA 98032	

Component No.	Tektronix Part No.	Serial/Ass Effective		Name & Description	Mfr. Code	Mfr. Part No.
A1 A1 A2 A3	672-0649-00 672-0649-01 670-5108-00 670-4182-00	8010100 8034303	B034302	CIRCUIT BD ASSY:TIME/CM CIRCUIT BD ASSY:TIME/CM CIRCUIT BD ASSY:TRIGGER CIRCUIT BD ASSY:READOUT	80009 80009 80009 80009	672-0649-00 672-0649-01 670-5108-00 670-4182-00
C3 C5 C7 C10 C12 C37	290-0748-00 290-0748-00 290-0748-00 281-0786-00 281-0599-00 283-0249-00			CAP, FXD, ELCTLT: 10UF, +50-20%, 25wVDC CAP, FXD, ELCTLT: 10UF, +50-20%, 25wVDC CAP, FXD, ELCTLT: 10UF, +50-20%, 25wVDC CAP, FXD, CER DI: 150PF, 10%, 100V CAP, FXD, CER DI: 1PF, +/-0.25PF, 500V CAP, FXD, CER DI: 0.068PF, 10%, 50V	54473 54473 54473 04222 59660 04222	ECE-BIEV100S ECE-BIEV100S ECE-BIEV100S MA101A151KAA 374-018-COK0109C SR305C683KAA
C41 C42 C43 C61 C63 C64	283-0198-00 283-0198-00 281-0572-00 281-0775-00 281-0815-00 281-0786-00			CAP, FXD, CER DI:0.22UF, 20%, 50V CAP, FXD, CER DI:0.22UF, 20%, 50V CAP, FXD, CER DI:6.8PF, 0.5%, 500V CAP, FXD, CER DI:0.1UF, 20%, 50V CAP, FXD, CER DI:0.027UF, 20%, 50V CAP, FXD, CER DI:150PF, 10%, 100V	05397 05397 52763 04222 04222 04222	C330C224M5U1CA C330C224M5U1CA 2RDPLZ007 6P80DC MA205E104MAA MA205C273MAA MA101A151KAA
C65 C90 C95 C98 C108 C113	281-0791-00 281-0611-00 281-0534-00 281-0617-00 283-0111-00 283-0066-00			CAP,FXD,CER DI:270PF,10%,100V CAP,FXD,CER DI:2.7PF,+/-0.25PF,200V CAP,FXD,CER DI:3.3PF,+/-0.25PF,500V CAP,FXD,CER DI:15PF,10%,200V CAP,FXD,CER DI:0.1UF,20%,50V CAP,FXD,CER DI:2.5PF,+/-0.5PF,200V	04222 52763 52763 52763 05397 72982	MA101C271KAA 2RDPLZ007 2P70CC 2RDPLZ007 3P30CC 2RDPLZ007 15P0KC C330C104M5U1CA 8101-047C0J259D
C114 C117 C118 C133 C144 C183	281-0775-00 281-0219-00 283-0066-00 281-0788-00 283-0111-00 281-0773-00			CAP,FXD,CER DI:0.1UF,20%,50V CAP,VAR,CER DI:5-35PF,+2 -2.5%,100V CAP,FXD,CER DI:2.5PF,+/-0.5PF,200V CAP,FXD,CER DI:470PF,10%,100V CAP,FXD,CER DI:0.1UF,20%,50V CAP,FXD,CER DI:0.01UF,10%,100V	04222 59660 72982 04222 05397 04222	MA205E104MAA 513-011 A 5-35 8101-047C0J259D SA102C471KAA C330C104M5U1CA MA201C103KAA
C184 C204 C211 C211 C212 C213	281-0786-00 283-0000-00 283-0641-00 283-0640-00 283-0555-00 285-0683-00		B031514	CAP,FXD,CER DI:150PF,10%,100V CAP,FXD,CER DI:0.001UF,+100-0%,500V CAP,FXD,MICA DI:180PF,1%,100V CAP,FXD,MICA DI:160PF,1%,100V CAP,FXD,MICA DI:2000PF,1%,500V CAP,FXD,PLASTIC:0.022UF,5%,100V	04222 59660 00853 00853 00853 19396	MA101A151KAA 831-610-Y5U0102P D155F181F0 D155F161F0 D195F202F0 223J01PT485
C214 C215 C221 C222 C225 C228	290-0269-00 290-0523-00 283-0111-00 283-0110-00 290-0536-00 290-0534-00			CAP,FXD,ELCTLT:0.22UF,5%,35V CAP,FXD,ELCTLT:2.2UF,20%,20V CAP,FXD,CER DI:0.1UF,20%,50V CAP,FXD,CER DI:0.005UF,+80-20%,150V CAP,FXD,ELCTLT:10UF,20%,25V TANTALUM CAP,FXD,ELCTLT:1UF,20%,35V	05397 05397 05397 59660 05397 05397	T320A224J035AS T368A225M020AS C330C104M5U1CA 855-547-E-502Z T368B106M025AS T368A105M035AZ
C230 C302 C315 C322 C323 C324	281-0786-00 290-0420-00 283-0110-00 283-0204-00 283-0111-00 283-0110-00			CAP,FXD,CER DI:150PF,10%,100V CAP,FXD,ELCTLT:0.68UF,20%,75V CAP,FXD,CER DI:0.005UF,+80-20%,150V CAP,FXD,CER DI:0.01UF,20%,50V CAP,FXD,CER DI:0.1UF,20%,50V CAP,FXD,CER DI:0.005UF,+80-20%,150V	04222 05397 59660 04222 05397 59660	MA101A151KAA T110A684M075AS 855-547-E-502Z SR155E103MAA C330C104M5U1CA 855-547-E-502Z
C338 C352 C361 C362 C364 C365	283-0691-00 283-0111-00 281-0166-00 283-0633-00 295-0172-00			CAP,FXD,MICA DI:650PF,1%,300V CAP,FXD,CER DI:0.1UF,20%,50V CAP,VAR,AIR DI:1.9-15.7 PF,250V CAP,FXD,MICA DI:77PF,1%,100V CAP SET,MATCHED:0.1UF,10UF,898PF,MATCHED (SAME AS C364)	00853 05397 74970 00853 80009	D153F651F0 C33OC104M5U1CA 187-0109-055 D155E770F0 295-0172-00
C366 C371 C376 C386 C388	283-0111-00 283-0111-00 290-0534-00 283-0111-00			(SAME AS C364) CAP,FXD,CER DI:0.1UF,20%,50V CAP,FXD,CER DI:0.1UF,20%,50V CAP,FXD,ELCTLT:1UF,20%,35V CAP,FXD,CER DI:0.1UF,20%,50V	05397 05397 05397 05397	C330C104M5U1CA C330C104M5U1CA T368A105M035AZ C330C104M5U1CA

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Component No.	Tektronix Part No.	Serial/Asse Effective		Name & Description	Mfr. Code	Mfr. Part No.
C405 C409 C457 C461 C483 C795	281-0763-00 283-0616-00 281-0658-00 281-0618-00 281-0811-00 283-0110-00	B020790		CAP, FXD, CER DI: 47PF, 10%, 100V CAP, FXD, MICA DI: 75PF, 5%, 500V CAP, FXD, CER DI: 6.2PF, +/-0.25PF, 500 CAP, FXD, CER DI: 4.7PF, +/-0.5PF, 500V CAP, FXD, CER DI: 10PF, 10%, 100V CAP, FXD, CER DI: 0.005UF, +80-20%, 150V	04222 00853 52763 52763 04222 59660	MA101A470KAA D155E750J0 2RDPLZ007 6P20CC 2RDPLZ007 4P70DC MA101A100KAA 855-547-E-502Z
C810 C820 C821 C830 C840 C844	283-0178-00 290-0745-00 283-0004-00 290-0745-00 290-0745-00 283-0004-00			CAP, FXD, CER DI:0.1UF, 20%, 100V CAP, FXD, ELCTLT:22UF, +50-20%, 25wVDC CAP, FXD, CER DI:0.02UF, +80-20%, 150V CAP, FXD, ELCTLT:22UF, +50-20%, 25wVDC CAP, FXD, ELCTLT:22UF, +50-20%, 25wVDC CAP, FXD, CER DI:0.02UF, +80-20%, 150V	05397 54473 59660 54473 54473 59660	C330C104Z1U1CA ECE-A25V22L 855-558Z5V0203Z ECE-A25V22L ECE-A25V22L 855-558Z5V0203Z
CR74 CR76 CR77 CR145 CR202 CR206	152-0321-00 152-0141-02 152-0141-02 152-0141-02 152-0153-00 152-0141-02			SEMICOND DVC,DI:SW,SI,30V,0.1A,DO-7S SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,10V,50MA,.DO-7 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35	07263 03508 03508 03508 03508 07263 03508	FSA1480 DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152) FD7003 DA2527 (1N4152)
CR208 CR224 CR225 CR226 CR303 CR304	152-0141-02 152-0141-02 152-0141-02 152-0141-02 152-0141-02 152-0141-02			SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35	03508 03508 03508 03508 03508 03508	DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152)
CR336 CR337 CR345 CR375 CR406 CR415	152-0141-02 152-0141-02 152-0322-00 152-0141-02 152-0141-02 152-0141-02			SEMICOND DVC,DI:SW,SI,30V,150MA,30V,D0-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,D0-35 SEMICOND DVC,DI:SCHOTTKY,SI,15V,1.2PF,D0-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,D0-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,D0-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,D0-35	03508 03508 50434 03508 03508 03508	DA2527 (1N4152) DA2527 (1N4152) 5082-2672 DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152)
CR420 CR434 CR452 CR455 CR465 CR725	152-0322-00 152-0141-02 152-0141-02 152-0141-02 152-0141-02 152-0141-02			SEMICOND DVC,DI:SCHOTTKY,SI,15V,1.2PF,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35	50434 03508 03508 03508 03508 03508	5082-2672 DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152) DA2527 (1N4152)
CR751 CR752 CR772 CR773 CR782 CR783	152-0141-02 152-0141-02 152-0141-02 152-0141-02 152-0141-02 152-0141-02			SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35 SEMICOND DVC,DI:SW,SI,30V,150MA,30V,DO-35	03508 03508 03508 03508 03508 03508	DA2527 (1N4152)
DS227 DS227 DS232 DS820 E274 E425	150-1029-00 150-1078-00 150-1033-00 150-0048-01 276-0507-00 276-0507-00	B010100 B034303 B031830 B031830	B034302	LT EMITTING DIO:GREEN,565NM,35MA LT EMITTING DIO:GREEN,565NM,20MA LT EMITTING DIO:AMBER,585NM,40MA MAX LAMP,INCAND:5V,0.06A,#683,AGED & SEL SHLD BEAD,ELEK:FERRITE SHLD BEAD,ELEK:FERRITE	58361 50434 50434 58854 02114 02114	Q6480/MV5274C HLMP 1503 HLMP 1401 683AS15 56-590-65B/3B 56-590-65B/3B
F90 L3 L5 L7 L324 L462	159-0120-00 108-0537-00 108-0537-00 108-0537-00 276-0543-00 108-0240-00			FUSE,CARTRIDGE:0.2A,125V,0.08SEC AXIAL COIL,RF:FIXED,200UH COIL,RF:FIXED,200UH COIL,RF:FIXED,200UH SHLD BEAD,ELEK:FERRITE COIL,RF:FIXED,820UH	75915 80009 80009 80009 80009 76493	279.200 108-0537-00 108-0537-00 108-0537-00 276-0543-00 B5147
L485 LR68 LR88 LR810	108-0509-00 108-0325-00 108-0325-00 108-0537-00			COIL,RF:FIXED,2.45UH COIL,RF:FIXED,489NH COIL,RF:FIXED,489NH COIL,RF:FIXED,200UH		ORDER BY DESCR ORDER BY DESCR ORDER BY DESCR 108-0537-00

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Component No.	Tektronix Part No.	Serial/Asse Effective	-	Name & Description	Mfr. Code	Mfr. Part No.
LR820 LR830 LR840 Q98 Q108 Q114	108-0537-00 108-0537-00 108-0537-00 151-1005-00 151-1005-00 151-1025-00			COIL,RF:FIXED,200UH COIL,RF:FIXED,200UH COIL,RF:FIXED,200UH TRANSISTOR:FET,N-CHAN,SI,TO-106 TRANSISTOR:FET,N-CHAN,SI,TO-106 TRANSISTOR:FET,N-CHAN,SI,TO-92	80009 80009 80009 80009 80009 04713	108-0537-00 108-0537-00 108-0537-00 151-1005-00 151-1005-00 SPF3036
Q118 Q142 Q160 Q164 Q174 Q184	151-0441-00 151-0190-00 151-0221-00 151-0221-00 151-0221-00 151-0427-00			TRANSISTOR:NPN,SI,TO-72 TRANSISTOR:NPN,SI,TO-92 TRANSISTOR:PNP,SI,TO-92 TRANSISTOR:PNP,SI,TO-92 TRANSISTOR:PNP,SI,TO-92 TRANSISTOR:NPN,SI,TO-92 TRANSISTOR:NPN,SI,TO-92	04713 80009 80009 80009 80009 07263	SRF501 151-0190-00 151-0221-00 151-0221-00 151-0221-00 S39287
Q201 Q202 Q203 Q204 Q206 Q210	151-0221-00 151-0325-00 151-0435-00 151-0223-00 151-0223-00 151-0273-00			TRANSISTOR: PNP, SI, TO-92 TRANSISTOR: PNP, SI, TO-92, SEL TRANSISTOR: DARLINGTON, PNP, SI, TO-92 TRANSISTOR: NPN, SI, 625MW, TO-92 TRANSISTOR: NPN, SI, 625MW, TO-92 TRANSISTOR: SELECTED	80009 80009 04713 04713 04713 03508	151-0221-00 151-0325-00 SPS8335 SPS8026 SPS8026 X16E3616
Q214 Q230 Q234 Q242 Q272 Q274	151-0220-00 151-0302-00 151-0301-00 151-0223-00 151-0223-00 151-0223-00			TRANSISTOR: PNP, SI, TO-92 TRANSISTOR: NPN, SI, TO-18 TRANSISTOR: PNP, SI, TO-18 TRANSISTOR: NPN, SI, 625MW, TO-92	80009 04713 04713 04713 04713 04713	151-0220-00 ST899 ST898 SPS8026 SPS8026 SPS8026
Q322 Q324 Q328 Q336 Q342 Q354	151-0220-07 151-0220-07 151-0273-00 151-0354-00 151-0220-00 151-0367-00			TRANSISTOR: PNP, SI, 600MHZ TRANSISTOR: PNP, SI, 600MHZ TRANSISTOR: SELECTED TRANSISTOR: PNP, SI, T0-78 TRANSISTOR: PNP, SI, T0-92 TRANSISTOR: NPN, SI, T0-92	80009 80009 03508 32293 80009 04713	151-0220-07 151-0220-07 X16E3616 ITS-1200-A 151-0220-00 SPS 8811
Q356 Q372 Q376 Q402 Q406 Q410	151-0367-00 151-1036-00 151-0127-00 151-0221-00 151-0221-00 151-0223-00			TRANSISTOR: NPN, SI, TO-92 TRANSISTOR: FET, N-CHAN, SI, TO-71 TRANSISTOR: NPN, SI, TO-18 TRANSISTOR: PNP, SI, TO-92 TRANSISTOR: PNP, SI, TO-92 TRANSISTOR: NPN, SI, 625MW, TO-92	04713 80009 04713 80009 80009 04713	SPS 8811 151-1036-00 SL6073A 151-0221-00 151-0221-00 SPS8026
Q415 Q420 Q425 Q434 Q438 Q454	151-0221-00 151-0223-00 151-0223-00 151-0220-00 151-0220-00 151-0220-07			TRANSISTOR: PNP,SI,TO-92 TRANSISTOR: NPN,SI,625MW,TO-92 TRANSISTOR: NPN,SI,625MW,TO-92 TRANSISTOR: PNP,SI,TO-92 TRANSISTOR: PNP,SI,TO-92 TRANSISTOR: PNP,SI,TO-92 TRANSISTOR: PNP,SI,600MHZ	80009 04713 04713 80009 80009	151-0221-00 SPS8026 SPS8026 151-0220-00 151-0220-00 151-0220-07
Q458 Q460 Q464 Q468 Q476 Q476	151-0325-00 151-0220-07 151-0220-07 151-0325-00 151-0472-00 151-0212-00	B010100 B020207	B020206	TRANSISTOR: PNP,SI,TO-92,SEL TRANSISTOR: PNP,SI,600MHZ TRANSISTOR: PNP,SI,600MHZ TRANSISTOR: PNP,SI,TO-92,SEL TRANSISTOR: NPN,SI,TO-92 TRANSISTOR: NPN,SI,TO-72	80009 80009 80009 80009 51984 04713	151-0325-00 151-0220-07 151-0220-07 151-0325-00 NE41632B SRF 518
Q484 Q496 Q844 R2 R10 R12	151-1113-00 151-0472-00 151-0301-00 315-0273-00 321-0268-00 321-0201-00	B010100	B020206	TRANSISTOR:FE,N CHANNEL,SI,TO-18/TO-92 TRANSISTOR:NPN,SI,TO-92 TRANSISTOR:PNP,SI,TO-18 RES,FXD,FILM:27K OHM,5%,0.25W RES,FXD,FILM:6.04K OHM,1%,0.125W,TC=TO RES,FXD,FILM:1.21K OHM,1%,0.125W,TC=TO	22229 51984 04713 57668 19701 19701	F2873 NE41632B ST898 NTR25J-E27K0 5043ED6K040F 5043ED1K210F
R20 R22 R23 R24	321-0268-00 321-0201-00 321-0099-00 321-0099-00			RES,FXD,FILM:6.04K OHM,1%,0.125W,TC=TO RES,FXD,FILM:1.21K OHM,1%,0.125W,TC=TO RES,FXD,FILM:105 OHM,1%,0.125W,TC=TO RES,FXD,FILM:105 OHM,1%,0.125W,TC=TO	19701 19701 07716 07716	5043ED6K040F 5043ED1K210F CEAD105R0F CEAD105R0F

Component No.	Tektronix Part No.	Serial/Asse Effective		Name & Description	Mfr. Code	Mfr. Part No.
R30 R34 R35 R36 R37 R38	311-0467-00 321-0074-00 315-0163-00 315-0302-00 321-0222-00 321-0332-00			RES,VAR,NONWW:PNL,100K OHM,0.5W RES,FXD,FILM:57.6 OHM,1%,0.125W,TC=T0 RES,FXD,FILM:16K OHM,5%,0.25W RES,FXD,FILM:3K OHM,5%,0.25W RES,FXD,FILM:2.00K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:28.0K OHM,1%,0.125W,TC=T0	01121 91637 57668 57668 19701 07716	W7705B CMF55116G57R60F NTR25J-E 16K NTR25J-E03K0 5033ED2K00F CEAD28001F
R39 R42 R43 R63 R64 R65	321-0184-00 315-0202-00 321-0349-00 321-0270-00 315-0203-00 321-0097-00			RES,FXD,FILM:806 OHM,1%,0.125W,TC=TO RES,FXD,FILM:2K OHM,5%,0.25W RES,FXD,FILM:42.2K OHM,1%,0.125W,TC=TO RES,FXD,FILM:6.34K OHM,1%,0.125W,TC=TO RES,FXD,FILM:20K OHM,5%,0.25W RES,FXD,FILM:100 OHM,1%,0.125W,TC=TO	19701 57668 07716 19701 57668 91637	5033ED806R0F NTR25J-E 2K CEAD42201F 5043ED6K340F NTR25J-E 20K CMF55116G100R0F
R66 R67 R68 R69 R71 R72	321-0248-00 315-0335-00 321-0097-00 301-0301-00 315-0822-00 315-0103-00			RES,FXD,FILM:3.74K OHM,1%,0.125W,TC=TO RES,FXD,FILM:3.3M OHM,5%,0.25W RES,FXD,FILM:100 OHM,1%,0.125W,TC=TO RES,FXD,FILM:300 OHM,5%,0.5W RES,FXD,FILM:8.2K OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W	19701 01121 91637 19701 19701	5043ED3K740F CB3355 CMF55116G100R0F 5053CX300R0J 5043CX8K200J 5043CX10K00J
R74 R76 R77 R79 R84 R85	321-0423-00 315-0105-00 315-0105-00 301-0301-00 315-0181-00 315-0201-00			RES,FXD,FILM:249K OHM,1%,0.125W,TC=TO RES,FXD,FILM:1M OHM,5%,0.25W RES,FXD,FILM:1M OHM,5%,0.25W RES,FXD,FILM:300 OHM,5%,0.5W RES,FXD,FILM:180 OHM,5%,0.25W RES,FXD,FILM:200 OHM,5%,0.25W	19701 19701 19701 19701 57668 57668	5043ED249K0F 5043CX1M000J 5043CX1M000J 5053CX300R0J NTR25J-E180E NTR25J-E200E
R86 R90 R91 R92 R93 R94	315-0151-00 315-0510-00 323-0095-00 323-0095-00 315-0105-00 315-0154-00			RES,FXD,FILM:150 OHM,5%,0.25W RES,FXD,FILM:51 OHM,5%,0.25W RES,FXD,FILM:95.3 OHM,1%,0.5W,TC=T0 RES,FXD,FILM:95.3 OHM,1%,0.5W,TC=T0 RES,FXD,FILM:1M OHM,5%,0.25W RES,FXD,FILM:150K OHM,5%,0.25W	57668 19701 75042 75042 19701 57668	NTR25J-E150E 5043CX51R00J CECT0-95R30F CECT0-95R30F 5043CX1M000J NTR25J-E150K
R95 R96 R97 R106 R107 R110	315-0510-00 315-0335-00 315-0105-00 315-0335-00 315-0105-00 315-0911-00			RES,FXD,FILM:51 OHM,5%,0.25W RES,FXD,FILM:3.3M OHM,5%,0.25W RES,FXD,FILM:1M OHM,5%,0.25W RES,FXD,FILM:3.3M OHM,5%,0.25W RES,FXD,FILM:1M OHM,5%,0.25W RES,FXD,FILM:910 OHM,5%,0.25W	19701 01121 19701 01121 19701 57668	5043CX51R00J CB3355 5043CX1M000J CB3355 5043CX1M000J NTR25J-E910E
R114 R117 R118 R131 R132 R133	315-0511-00 317-0101-00 315-0152-00 321-0260-00 321-0260-00 315-0102-00			RES,FXD,FILM:510 OHM,5%,0.25W RES,FXD,CMPSN:100 OHM,5%,0.125W RES,FXD,FILM:1.5K OHM,5%,0.25W RES,FXD,FILM:4.99K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:4.99K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:4.99K OHM,1%,0.25W	19701 01121 57668 19701 19701 57668	5043CX510R0J BB1015 NTR25J-E01K5 5033ED4K990F 5033ED4K990F NTR25JE01K0
R134 R135 R138 R139 R140 R140	315-0271-00 315-0241-00 315-0103-00 325-0053-00 311-1192-00 311-2303-00	B010100 B034150	B034149	RES,FXD,FILM:270 OHM,5%,0.25W RES,FXD,FILM:240 OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W RES,FXD,FILM:50 OHM,1%,0.05W,TC=T0 RES,VAR,NONWW:PNL,10K OHM,1W,W/SW RES,VAR,NONWW:PNL,10K OHM,10%,LINEAR,0.25W	57668 19701 19701 91637 12697 12697	NTR25J-E270E 5043CX240R0J 5043CX10K00J CMF50-F50R00F 381-CM39695 CM45205
R141 R142 R143 R144 R146 R147	315-0512-00 315-0391-00 315-0102-00 315-0512-00 315-0272-00 311-1234-00			RES,FXD,FILM:5.1K OHM,5%,0.25W RES,FXD,FILM:390 OHM,5%,0.25W RES,FXD,FILM:1K OHM,5%,0.25W RES,FXD,FILM:5.1K OHM,5%,0.25W RES,FXD,FILM:2.7K OHM,5%,0.25W RES,VAR,NONWW:TRMR,50K OHM,0.25W	57668 57668 57668 57668 57668 32997	NTR25J-E05K1 NTR25J-E390E NTR25JE01K0 NTR25J-E05K1 NTR25J-E02K7 3386F-T06-503
R148 R154 R155 R158	315-0471-00 315-0271-00 315-0241-00 315-0103-00			RES,FXD,FILM:470 OHM,5%,0.25W RES,FXD,FILM:270 OHM,5%,0.25W RES,FXD,FILM:240 OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W	57668 57668 19701 19701	NTR25J-E470E NTR25J-E270E 5043CX240R0J 5043CX10K00J

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Component No.	Tektronix Part No.	Serial/Asse		Name & Description	Mfr. Code	Mfr. Part No.
R159 R160 R161 R162 R164 R173	325-0053-00 315-0102-00 315-0202-00 315-0101-00 315-0101-00 315-0102-00			RES,FXD,FILM:50 OHM,1%,0.05W,TC=T0 RES,FXD,FILM:1K OHM,5%,0.25W RES,FXD,FILM:2K OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:1K OHM,5%,0.25W	91637 57668 57668 57668 57668 57668	CMF50-F50R00F NTR25JE01K0 NTR25J-E 2K NTR25J-E 100E NTR25J-E 100E NTR25JE01K0
R174 R182 R183 R184 R185 R201	315-0470-00 315-0330-00 315-0103-00 321-0202-00 321-0260-00 315-0223-00			RES,FXD,FILM:47 OHM,5%,0.25W RES,FXD,FILM:33 OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W RES,FXD,FILM:1.24K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:4.99K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:22K OHM,5%,0.25W	57668 19701 19701 24546 19701 19701	NTR25J-E47E0 5043CX33R00J 5043CX10K00J NA55D1241F 5033ED4K990F 5043CX22K00J92U
R202 R203 R204 R205 R206 R207	315-0202-00 315-0391-00 315-0392-00 315-0471-00 321-0219-00 321-0173-00			RES,FXD,FILM:2K OHM,5%,0.25W RES,FXD,FILM:390 OHM,5%,0.25W RES,FXD,FILM:3.9K OHM,5%,0.25W RES,FXD,FILM:470 OHM,5%,0.25W RES,FXD,FILM:1.87K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:619 OHM,1%,0.125W,TC=T0	57668 57668 57668 57668 07716	NTR25J-E 2K NTR25J-E390E NTR25J-E03K9 NTR25J-E470E CEAD18700F CEAD619R0F
R208 R209 R210 R210 R211 R212	315-0102-00 321-0322-00 311-0467-00 311-2306-00 301-0432-00 321-0373-00	B010100 B034150	B034149	RES,FXD,FILM:1K OHM,5%,0.25W RES,FXD,FILM:22.1K OHM,0.1%,0.125W,TC=TO RES,VAR,NONWW:PNL,100K OHM,0.5W RES,VAR,NONWW:PNL,100K OHM,20%,0.5W RES,FXD,FILM:4.3K OHM,5%,0.5W RES,FXD,FILM:75.0K OHM,1%,0.125W,TC=T0	57668 19701 01121 12697 19701	NTR25JE01K0 5033ED22K10F W7705B CM43489 5053CX4K300J 5033ED75K00F
R213 R214 R215 R216 R217 R218	321-0373-00 321-0327-00 315-0101-00 321-0327-00 321-0292-00 321-0267-00			RES,FXD,FILM:75.0K 0HM,1%,0.125W,TC=T0 RES,FXD,FILM:24.9K 0HM,1%,0.125W,TC=T0 RES,FXD,FILM:100 0HM,5%,0.25W RES,FXD,FILM:24.9K 0HM,1%,0.125W,TC=T0 RES,FXD,FILM:10.7K 0HM,1%,0.125W,TC=T0 RES,FXD,FILM:5.90K 0HM,1%,0.125W,TC=T0	19701 07716 57668 07716 07716 19701	5033ED75K00F CEAD24901F NTR25J-E 100E CEAD24901F CEAD10701F 5033ED5K900F
R219 R222 R224 R225 R226 R227 R227	321-0293-00 315-0472-00 315-0622-00 315-0103-00 315-0393-00 315-0121-00 315-0201-00		B034302	RES,FXD,FILM:11.0K OHM,1%,0.125W,TC=TO RES,FXD,FILM:4.7K OHM,5%,0.25W RES,FXD,FILM:6.2K OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W RES,FXD,FILM:39K OHM,5%,0.25W RES,FXD,FILM:120 OHM,5%,0.25W RES,FXD,FILM:200 OHM,5%,0.25W	07716 57668 19701 19701 57668 19701 57668	CEAD11001F NTR25J-E04K7 5043CX6K200J 5043CX10K00J NTR25J-E39K0 5043CX120R0J NTR25J-E200E
R228 R230 R231 R232 R233 R234	315-0184-00 315-0911-00 315-0432-00 315-0121-00 315-0561-00 315-0430-00			RES,FXD,FILM:180K OHM,5%,0.25W RES,FXD,FILM:910 OHM,5%,0.25W RES,FXD,FILM:4.3K OHM,5%,0.25W RES,FXD,FILM:120 OHM,5%,0.25W RES,FXD,FILM:560 OHM,5%,0.25W RES,FXD,FILM:43 OHM,5%,0.25W	19701 57668 57668 19701 19701	5043CX180K0J NTR25J-E910E NTR25J-E04K3 5043CX120R0J 5043CX560R0J 5043CX43R00J
R240 R241 R242 R243 R244 R271	315-0471-00 315-0331-00 315-0332-00 315-0821-00 315-0432-00 315-0222-00			RES,FXD,FILM:470 OHM,5%,0.25W RES,FXD,FILM:330 OHM,5%,0.25W RES,FXD,FILM:3.3K OHM,5%,0.25W RES,FXD,FILM:820 OHM,5%,0.25W RES,FXD,FILM:4.3K OHM,5%,0.25W RES,FXD,FILM:2.2K OHM,5%,0.25W	57668 57668 57668 19701 57668 57668	NTR25J-E470E NTR25J-E330E NTR25J-E03K3 5043CX820R0J NTR25J-E04K3 NTR25J-E02K2
R272 R273 R274 R275 R300 R300	315-0472-00 321-0243-00 315-0472-00 315-0101-00 311-0467-00 311-2306-00		B034149	RES,FXD,FILM:4.7K OHM,5%,0.25W RES,FXD,FILM:3.32K OHM,1%,0.125W,TC=TO RES,FXD,FILM:4.7K OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W RES,VAR,NONWW:PNL,100K OHM,0.5W RES,VAR,NONWW:PNL,100K OHM,20%,0.5W	57668 19701 57668 57668 01121 12697	NTR25J-E04K7 5033ED3K32F NTR25J-E04K7 NTR25J-E 100E W7705B CM43489
R301 R302 R303	321-0438-00 315-0121-00 321-0363-00			RES,FXD,FILM:357K OHM,1%,0.125w,TC=TO RES,FXD,FILM:120 OHM,5%,0.25w RES,FXD,FILM:59.0K OHM,1%,0.125w,TC=TO	07716 19701 07716	CEAD35702F 5043CX120R0J CEAD59001F

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Component No.	Tektronix Part No.	Serial/Asse Effective	-	Name & Description	Mfr. Code	Mfr. Part No.
R304 R305 R306 R310 R312 R313	321-0360-00 311-1232-00 321-0458-00 311-1232-00 315-0273-00 315-0273-00			RES,FXD,FILM:54.9K OHM,1%,0.125W,TC=TO RES,VAR,NONW:TRMR,50K OHM,0.5W RES,FXD,FILM:576K OHM,1%,0.125W,TC=TO RES,VAR,NONW:TRMR,50K OHM,0.5W RES,FXD,FILM:27K OHM,5%,0.25W RES,FXD,FILM:27K OHM,5%,0.25W	19701 32997 07716 32997 57668 57668	5033ED54K90F 3386F-T04-503 CEAD57602F 3386F-T04-503 NTR25J-E27K0 NTR25J-E27K0
R315 R316 R317 R321 R322 R323	315-0562-00 301-0432-00 315-0101-00 315-0104-00 315-0221-00 315-0103-00			RES,FXD,FILM:5.6K OHM,5%,0.25W RES,FXD,FILM:4.3K OHM,5%,0.5W RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:100K OHM,5%,0.25W RES,FXD,FILM:220 OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W	57668 19701 57668 57668 57668 19701	NTR25J-E05K6 5053CX4K300J NTR25J-E 100E NTR25J-E100K NTR25J-E220E 5043CX10K00J
R328 R333 R334 R336 R337 R338	321-0133-00 315-0221-00 315-0470-00 321-0275-00 321-0301-00 315-0201-00			RES,FXD,FILM:237 OHM,1%,0.125W,TC=TO RES,FXD,FILM:220 OHM,5%,0.25W RES,FXD,FILM:47 OHM,5%,0.25W RES,FXD,FILM:7.15K OHM,1%,0.125W,TC=TO RES,FXD,FILM:13.3K OHM,1%,0.125W,TC=TO RES,FXD,FILM:200 OHM,5%,0.25W	07716 57668 57668 07716 07716 57668	CEAD237R0F NTR25J-E220E NTR25J-E47E0 CEAD71500F CEAD13301F NTR25J-E200E
R341 R342 R343 R344 R346 R352	315-0301-00 315-0392-00 315-0101-00 315-0101-00 315-0112-00 315-0100-00			RES,FXD,FILM:300 OHM,5%,0.25W RES,FXD,FILM:3.9K OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:1.1K OHM,5%,0.25W RES,FXD,FILM:1.1K OHM,5%,0.25W	57668 57668 57668 57668 19701	NTR25J-E300E NTR25J-E03K9 NTR25J-E 100E NTR25J-E 100E 5043CX1K100J 5043CX10R00J
R353 R354 R356 R357 R358 R359	315-0222-00 315-0101-00 315-0101-00 315-0221-00 323-0175-00 315-0122-00			RES,FXD,FILM:2.2K OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:220 OHM,5%,0.25W RES,FXD,FILM:649 OHM,1%,0.5W,TC=T0 RES,FXD,FILM:1.2K OHM,5%,0.25W	57668 57668 57668 57668 75042 57668	NTR25J-E02K2 NTR25J-E 100E NTR25J-E 100E NTR25J-E220E CECTO-6490F NTR25J-E01K2
R361 R362 R363 R364 R371 R373	315-0330-00 315-0330-00 315-0560-00 315-0100-00 315-0470-00 315-0101-00			RES,FXD,FILM:33 OHM,5%,0.25W RES,FXD,FILM:33 OHM,5%,0.25W RES,FXD,FILM:56 OHM,5%,0.25W RES,FXD,FILM:10 OHM,5%,0.25W RES,FXD,FILM:47 OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W	19701 19701 57668 19701 57668 57668	5043CX33R00J 5043CX33R00J NTR25J-E56E0 5043CX10RR00J NTR25J-E47E0 NTR25J-E 100E
R375 R376 R377 R380 R380 R383	315-0101-00 315-0470-00 315-0272-00 311-1781-00 311-2305-00 315-0513-00		B034149	RES,FXD,FILM:100 OHM,5%,0.25W RES,FXD,FILM:47 OHM,5%,0.25W RES,FXD,FILM:2.7K OHM,5%,0.25W RES,VAR,NONWW:PNL,10K OHM,10%,0.5W RES,VAR,NONWW:PNL,10K OHM X 10K OHM RES,FXD,FILM:51K OHM,5%,0.25W	57668 57668 57668 12697 12697 57668	NTR25J-E 100E NTR25J-E47E0 NTR25J-E02K7 388CM40913 CM43490 NTR25J-E51K0
R384 R385 R386 R388 R391 R392	315-0105-00 321-0362-00 321-0289-03 315-0180-00 323-1500-07 323-1500-07			RES,FXD,FILM:1M OHM,5%,0.25W RES,FXD,FILM:57.6K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:10.0K OHM,0.25%,0.125W,TC=T2 RES,FXD,FILM:18 OHM,5%,0.25W RES,FXD,FILM:1.6MEG OHM,0.1%,0.5W,TC=T2 RES,FXD,FILM:1.6MEG OHM,0.1%,0.5W,TC=T2	19701 19701 07716 19701 24546 24546	5043CX1M000J 5043ED57K60F CEAC10001C 5043CX18R00J NE65E1604B NE65E1604B
R393 R394 R395 R396 R397 R398	323-0620-07 323-0806-07 323-1404-07 323-0805-07 323-0802-07 323-1308-07			RES,FXD,FILM:800K OHM,0.1%,0.5W,TC=T9 RES,FXD,FILM:266.7K OHM,0.1%,0.5W,TC=T9 RES,FXD,FILM:160K OHM,0.1%,0.5W,TC=T9 RES,FXD,FILM:80.0K OHM,0.1%,0.5W,TC=T9 RES,FXD,FILM:26.67K OHM,0.1%,0.5W,TC=T9 RES,FXD,FILM:16.0K OHM,0.1%,0.5W,TC=T9	24546 91637 24546 24546 07716 24546	NE65E8003B MFF1226C26672B NE65E1603B NE65E8002B CECE26671B NE65E1602B
R399 R401 R402 R403	308-0586-00 315-0751-00 315-0222-00 315-0101-00	B010100	B031439	RES,FXD,WW:5K OHM,0.25%,3W,TC=10PPM RES,FXD,FILM:50 OHM,5%,0.25W RES,FXD,FILM:2.2K OHM,5%,0.25W RES,FXD,FILM:100 OHM,5%,0.25W	14193 57668 57668 57668	SA31-5001C NTR25J-E750E NTR25J-E02K2 NTR25J-E 100E

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Component No.	Tektronix Part No.	Serial/Asse Effective		Name & Description	Mfr. Code	Mfr. Part No.
R403 R404 R405 R406 R409 R410	315-0161-00 321-0222-07 321-0196-00 315-0162-00 321-0229-00 321-0185-00	B031440		RES,FXD,FILM:160 OHM,5%,0.25W RES,FXD,FILM:2.0K OHM,0.1%,0.125W,TC=T9 RES,FXD,FILM:1.07K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:1.6K OHM,5%,0.25W RES,FXD,FILM:2.37K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:825 OHM,1%,0.125W,TC=T0	57668 19701 07716 19701 19701 07716	NTR25J-E 160E 5033RE2K000B CEAD10700F 5043CX1K600J 5043ED2K37F CEAD825R0F
R415 R419 R421 R422 R425 R426	315-0122-00 315-0121-00 315-0102-00 321-0173-00 315-0272-00 315-0510-00			RES,FXD,FILM:1.2K OHM,5%,0.25W RES,FXD,FILM:12O OHM,5%,0.25W RES,FXD,FILM:1K OHM,5%,0.25W RES,FXD,FILM:619 OHM,1%,0.125W,TC=TO RES,FXD,FILM:2.7K OHM,5%,0.25W RES,FXD,FILM:51 OHM,5%,0.25W	57668 19701 57668 07716 57668 19701	NTR25J-E01K2 5043CX120R0J NTR25JE01K0 CEAD619R0F NTR25J-E02K7 5043CX51R00J
R431 R432 R433 R433 R434 R435	321-0260-00 315-0474-00 321-0263-00 321-0265-00 315-0431-00 315-0242-00	B010100 B032140	B032139	RES,FXD,FILM:4.99K OHM,1%,0.125W,TC=TO RES,FXD,FILM:470K OHM,5%,0.25W RES,FXD,FILM:5.36K OHM,1%,0.125W,TC=TO RES,FXD,FILM:5.62K OHM,1%,0.125W,TC=TO RES,FXD,FILM:430 OHM,5%,0.25W RES,FXD,FILM:2.4K OHM,5%,0.25W	19701 19701 07716 19701 19701 57668	5033ED4K990F 5043CX470K0J92U CEAD53600F 5043ED5K620F 5043CX430R0J NTR25J-E02K4
R438 R439 R451 R452 R454 R455	315-0472-00 315-0510-00 321-0400-00 315-0151-00 321-0190-00 315-0330-00			RES,FXD,FILM:4.7K OHM,5%,0.25W RES,FXD,FILM:51 OHM,5%,0.25W RES,FXD,FILM:143K OHM,1%,0.125W,TC=T0 RES,FXD,FILM:150 OHM,5%,0.25W RES,FXD,FILM:931 OHM,1%,0.125W,TC=T0 RES,FXD,FILM:33 OHM,5%,0.25W	57668 19701 19701 57668 19701	NTR25J-E04K7 5043CX51R00J 5043ED143K0F NTR25J-E150E 5043ED931R0F 5043CX33R00J
R456 R457 R458 R459 R460 R461	315-0103-00 315-0111-00 321-0928-07 315-0473-00 311-1423-00 321-0122-00			RES,FXD,FILM:10K 0HM,5%,0.25W RES,FXD,FILM:110 0HM,5%,0.25W RES,FXD,FILM:250 0HM,0.1%,0.125W,TC=T9 RES,FXD,FILM:47K 0HM,5%,0.25W RES,VAR,NONWW:TRMR,20 0HM,0.5W RES,FXD,FILM:182 0HM,1%,0.125W,TC=T0	19701 57668 19701 57668 32997 19701	5043CX10K00J NTR25J-E110E 5033RE250R0B NTR25J-E47K0 3386F-T04-200 5033ED182R0F
R462 R464 R465 R466 R468 R469	308-0366-00 321-0190-00 315-0820-00 315-0103-00 321-0928-07 315-0473-00			RES,FXD,WW:3.4K OHM,1%,3W RES,FXD,FILM:931 OHM,1%,0.125W,TC=TO RES,FXD,FILM:82 OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W RES,FXD,FILM:250 OHM,0.1%,0.125W,TC=T9 RES,FXD,FILM:47K OHM,5%,0.25W	14193 19701 57668 19701 19701 57668	SA31 3401F 5043ED931R0F NTR25J-E82E0 5043CX10K00J 5033RE250R0B NTR25J-E47K0
R470 R471 R472 R474 R475 R476	311-1226-00 322-0200-00 315-0562-00 321-0071-00 321-0055-00 322-0224-00			RES, VAR, NONWW:TRMR, 2.5K OHM, 0.5W RES, FXD, FILM:1.18K OHM, 1%, 0.25W, TC=TO RES, FXD, FILM:5.6K OHM, 5%, 0.25W RES, FXD, FILM:53.6 OHM, 1%, 0.125W, TC=TO RES, FXD, FILM:36.5 OHM, 0.5%, 0.125W, TC=TO MI RES, FXD, FILM:2.10K OHM, 1%, 0.25W, TC=TO	32997 57668 57668 91637 57668 19701	3386F-T04-252 CRB60FX118 OHM NTR25J-E05K6 CMF55116G53R60F RB14FXE 36E5 5043RD2K100F
R480 R481 R482 R483 R484 R485	311-1232-00 315-0203-00 315-0683-00 315-0103-00 307-0472-00 323-0186-00			RES,VAR,NONWW:TRMR,50K OHM,0.5W RES,FXD,FILM:20K OHM,5%,0.25W RES,FXD,FILM:68K OHM,5%,0.25W RES,FXD,FILM:10K OHM,5%,0.25W RES,THERMAL:100K OHM,5%,DISC NTC RES,FXD,FILM:845 OHM,1%,0.5W,TC=T0	32997 57668 57668 19701 15454 19701	3386F-T04-503 NTR25J-E 20K NTR25J-E68K0 5043CX10K00J 07DE104J-220-EC 5053RD845R0F
R491 R492 R494 R495 R496	322-0200-00 315-0562-00 321-0071-00 321-0055-00 322-0224-00 151-0212-00	B020207		RES,FXD,FILM:1.18K OHM,1%,0.25W,TC=TO RES,FXD,FILM:5.6K OHM,5%,0.25W RES,FXD,FILM:53.6 OHM,1%,0.125W,TC=TO RES,FXD,FILM:36.5 OHM,0.5%,0.125W,TC=TO MI RES,FXD,FILM:2.10K OHM,1%,0.25W,TC=TO TRANSISTOR:NPN,SI,TO-72	57668 57668 91637 57668 19701 04713	CRB60FX118 OHM NTR25J-E05K6 CMF55116G53R60F RB14FXE 36E5 5043RD2K100F SRF 518
R725	311-1590-00	B010100	B034749	RES, VAR, NONWW: PNL, 10K OHM, 1W, COMPOSITION OR CERMET	12697	CM40256
R725 R751	311-2371-00 315-0154-00	B034750		RES,VAR,NONWW:PNL,10K OHM,0.5W RES,FXD,FILM:150K OHM,5%,0.25W	12697 57668	CM45236 NTR25J-E150K

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Component No.	Tektronix Part No.	Serial/Asse Effective		Name & Description	Mfr. Code	Mfr. Part No.
R752 R756 R761 R764 R771 R772	315-0154-00 315-0753-00 315-0154-00 315-0133-00 315-0753-00 321-0356-00			RES,FXD,FILM:150K OHM,5%,0.25W RES,FXD,FILM:75K OHM,5%,0.25W RES,FXD,FILM:150K OHM,5%,0.25W RES,FXD,FILM:13K OHM,5%,0.25W RES,FXD,FILM:75K OHM,5%,0.25W RES,FXD,FILM:49.9K OHM,1%,0.125W,TC=T0	57668 57668 57668 19701 57668 19701	NTR25J-E150K NTR25J-E75K0 NTR25J-E150K 5043CX13K00J NTR25J-E75K0 5033ED49K90F
R773 R779 R781 R782 R783 R784	315-0154-00 315-0151-00 315-0154-00 315-0753-00 315-0154-00 321-0356-00			RES,FXD,FILM:150K OHM,5%,0.25W RES,FXD,FILM:150 OHM,5%,0.25W RES,FXD,FILM:150K OHM,5%,0.25W RES,FXD,FILM:75K OHM,5%,0.25W RES,FXD,FILM:150K OHM,5%,0.25W RES,FXD,FILM:49.9K OHM,1%,0.125W,TC=T0	57668 57668 57668 57668 57668 19701	NTR25J-E150K NTR25J-E150E NTR25J-E150K NTR25J-E75K0 NTR25J-E150K 5033ED49K90F
R793 R794 R821 R841 R842 R844	321-0344-00 315-0154-00 315-0510-00 321-0260-00 321-0289-03 315-0101-00			RES,FXD,FILM:37.4K OHM,1%,0.125W,TC=TO RES,FXD,FILM:150K OHM,5%,0.25W RES,FXD,FILM:51 OHM,5%,0.25W RES,FXD,FILM:4.99K OHM,1%,0.125W,TC=TO RES,FXD,FILM:10.0K OHM,0.25%,0.125W,TC=T2 RES,FXD,FILM:100 OHM,5%,0.25W	19701 57668 19701 19701 07716 57668	5033ED 37K40F NTR25J-E150K 5043CX51R00J 5033ED4K990F CEAC10001C NTR25J-E 100E
\$20 \$50 \$90 \$140 \$230 \$460	263-0016-03 263-0015-00 263-0010-02  263-0016-02 260-1771-00			SWITCH PB ASSY:4 LATCH,7.5MM,6 CONTACTS SWITCH PB ASSY:3 LATCHING,7.5 MM SWITCH PB ASSY:1 PUSH,7.5MM,1 CONTACT (PART OF R140) SWITCH PB ASSY:3 LCH & 1 CANC,7.5MM,5 CONT SWITCH,PUSH:1 BUTTON,2 POLE,SLOPE	80009 80009 80009 80009 31918	263-0016-03 263-0015-00 263-0010-02 263-0016-02 ORDER BY DESCR
S725 S800 T1 U12 U38 U64	263-1166-00 120-0444-00 156-0105-00 156-1149-00 156-0158-00			(PART OF R725) SW CAM ACTR AS:TIME/CM XFMR,TOROID: MICROCKT,LINEAR:OPNL AMPL MICROCKT,LINEAR:OPERATIONAL AMP,JFET INPUT MICROCKT,LINEAR:DUAL OPNL AMPL	80009 80009 01295 27014 04713	263-1166-00 120-0444-00 LM301AP LF351N/GLEA134 MC1458P1/MC1458U
U110 U124 U135 U138 U144 U220	156-1149-00 155-0160-00 156-0158-00 156-0158-00 155-0150-00 155-0049-02			MICROCKT,LINEAR:OPERATIONAL AMP,JFET INPUT MICROCKT,LINEAR:TRIGGER AMPL/CHANNEL SWITCH MICROCKT,LINEAR:DUAL OPNL AMPL MICROCKT,LINEAR:DUAL OPNL AMPL MICROCKT,DGTL:TRIGGER MICROCKT,DGTL:SWEEP CONT,W/LOCKOUT DISABLE	27014 80009 04713 04713 80009 80009	LF351N/GLEA134 155-0160-00 MC1458P1/MC1458U MC1458P1/MC1458U 155-0150-00 155-0049-02
U314 U386 U386 U842 U842 VR118	156-1149-00 156-0067-02 156-0067-00 156-0067-02 156-0067-00 152-0226-00	B034422 B010100	B034421 B034421	MICROCKT,LINEAR:OPERATIONAL AMP,JFET INPUT MICROCKT,LINEAR:OPNL AMPL,SELECTED MICROCKT,LINEAR:OPNL AMPL,SEL MICROCKT,LINEAR:OPNL AMPL,SELECTED MICROCKT,LINEAR:OPNL AMPL,SEL SEMICOND DVC,DI:ZEN,SI,5.1V,5%,0.4W,DO-7	27014 02735 04713 02735 04713 04713	LF351N/GLEA134 85147 MC1741CP1 85147 MC1741CP1 SZ12262RL
VR124 VR134 VR154 VR317	152-0278-00 152-0395-00 152-0395-00 152-0226-00			SEMICOND DVC,DI:ZEN,SI,3V,5%,0.4W,DO-7 SEMICOND DVC,DI:ZEN,SI,4.3V,5%,0.4W SEMICOND DVC,DI:ZEN,SI,4.3V,5%,0.4W SEMICOND DVC,DI:ZEN,SI,5.1V,5%,0.4W,DO-7	80009 04713 04713 04713	152-0278-00 SZG35009K18 SZG35009K18 SZ12262RL

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Schematic Name

and Number

### **DIAGRAMS AND CIRCUIT BOARD ILLUSTRATIONS**

#### Symbols and Reference Designators

Electrical components shown on the diagrams are in the following units unless noted otherwise:

Capacitors = Values one or greater are in picofarads (pF).

Values less than one are in microfarads ( $\mu$ F).

Resistors = Ohms  $(\Omega)$ .

Graphic symbols and class designation letters are based on ANSI Standard Y32.2-1975.

Logic symbology is based on ANSI Y32.14-1973 in terms of positive logic. Logic symbols depict the logic function performed and may differ from the manufacturer's data.

The overline on a signal name indicates that the signal performs its intended function when it goes to the low state. Abbreviations are based on ANSI Y1.1-1972.

Other ANSI standards that are used in the preparation of diagrams by Tektronix, Inc. are:

Y14.15, 1966 Drafting Practices.

Y14.2, 1973 L

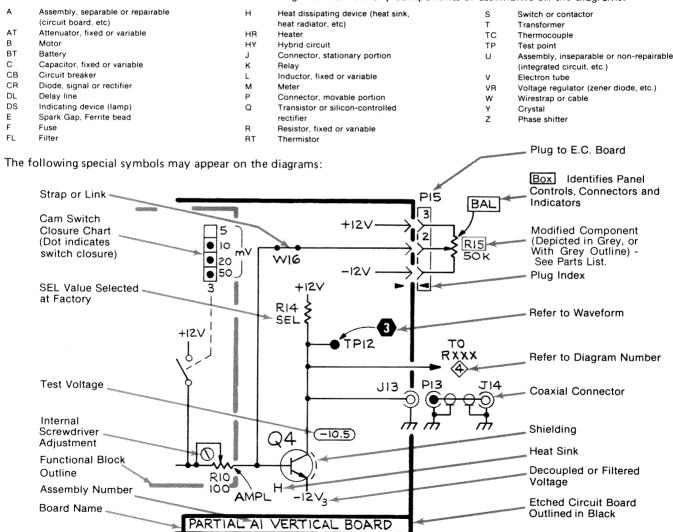
Line Conventions and Lettering.

Y10.5, 1968

Letter Symbols for Quantities Used in Electrical Science and

Electrical Engineering.

The following prefix letters are used as reference designators to identify components or assemblies on the diagrams.



VERTICAL AMPLIFIER &

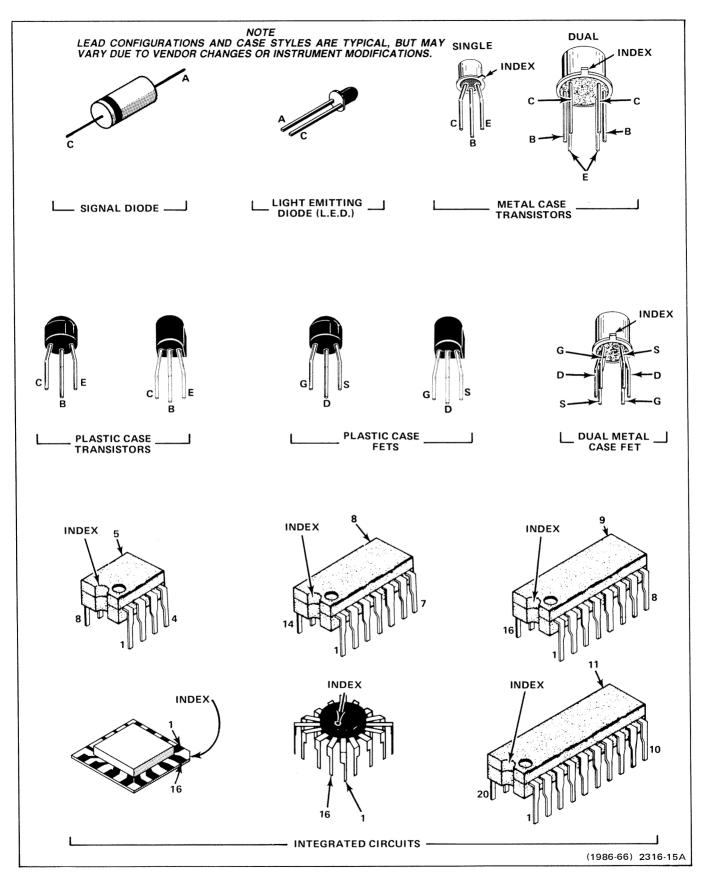


Figure 8-1. Semiconductor lead configurations.

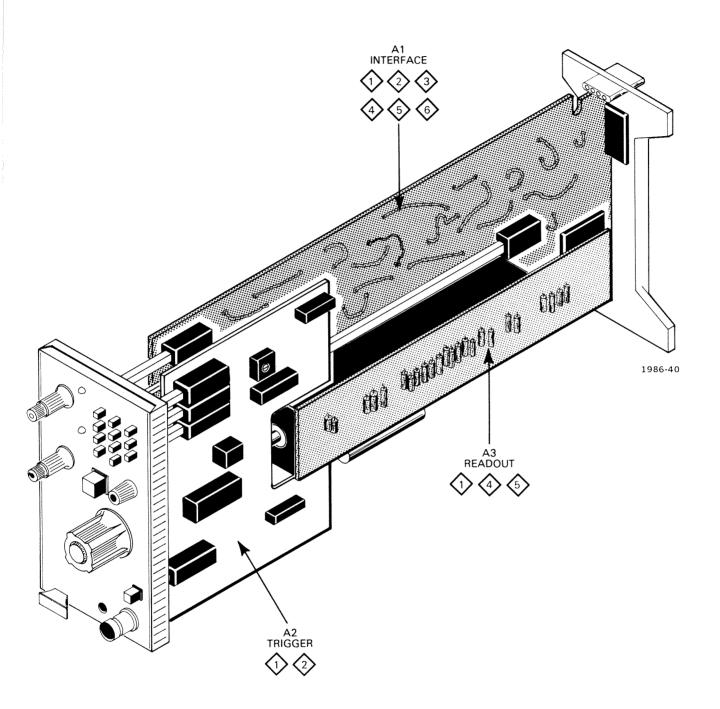
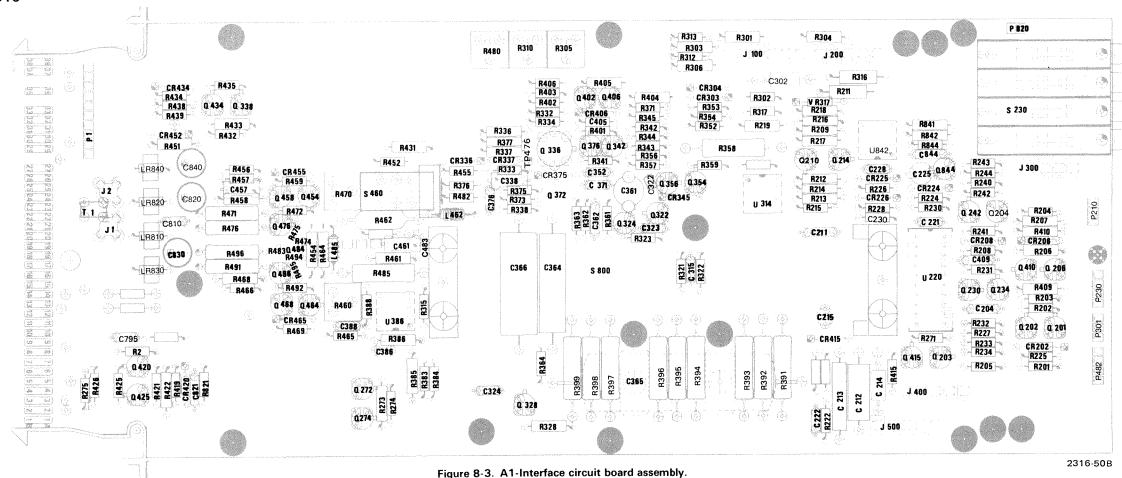


Figure 8-2. Location of circuit boards in the 7B10.





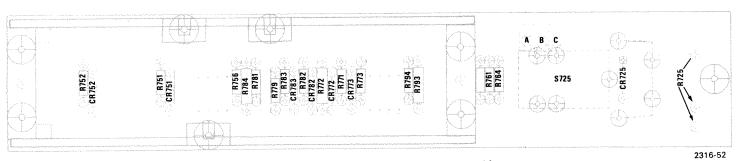
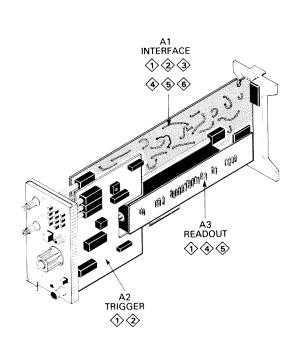


Figure 8-5. A3-Readout circuit board assembly.



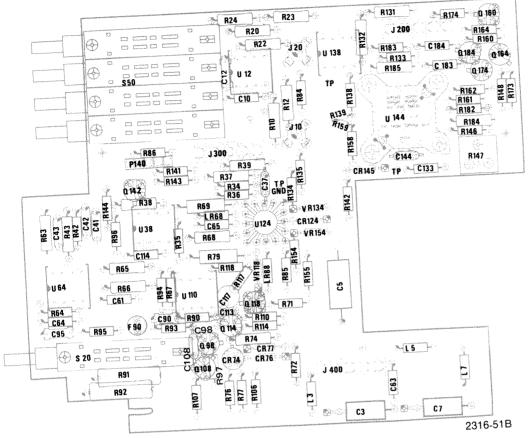


Figure 8-4. A2-Trigger circuit board assembly.

520 COUPLING

550 50URCE

LINE EXT

 $\hat{1}$ 

VARIABLE RT25

PARTIAL AS READOUT BD 5230 MODE

**RUTO** 

NORM

HF SYNC

SINGLE SWEEP RESET 5460 MAG

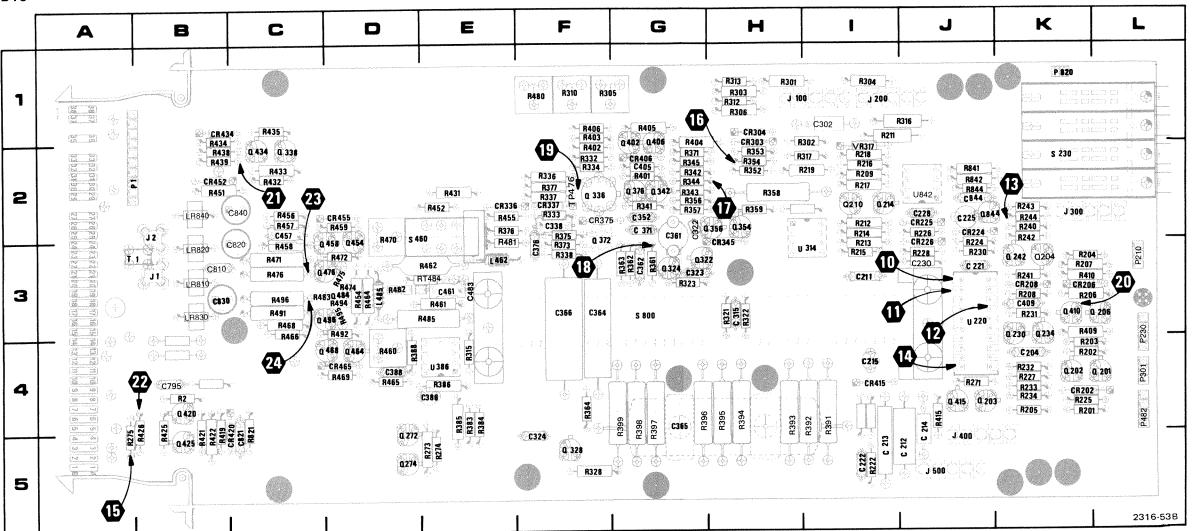
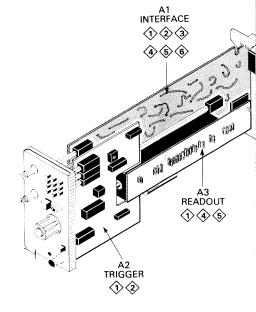
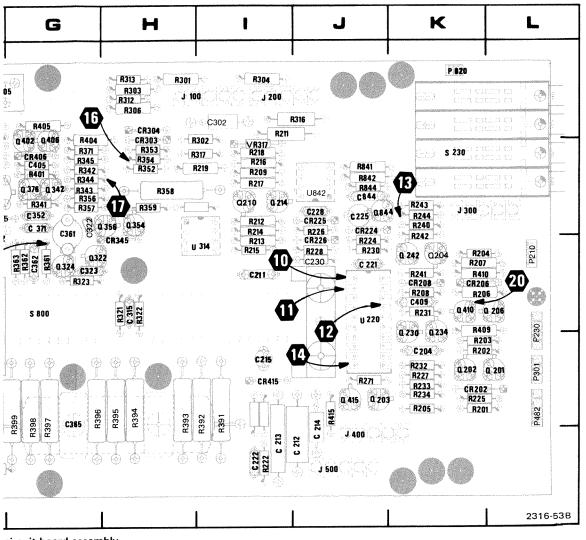


Figure 8-6. A1-Interface circuit board assembly.

CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID
NO	COORD	NO	COORD	NO	COORD	NO	COORD	NO	COORD	NO	COORD	NO	COORD	NO	COORD	NO	COORD	NO	COORD
C204 C211 C212 C213 C214 C215 C221 C222 C225 C228 C230 C302 C315 C322 C324 C338 C352 C361 C362 C361 C365 C366 C371 C376 C483 C386 C388	COORD  4K 3I 5J 5I 5J 4I 3J 5I 2J 3J 11 3H 2G 3G 5F 2F 2G 2G 3G 3G 3F 4G 3F 3E 4E 4D	C409 C457 C461 C795 C810 C820 C821 C830 C840 C844 CR202 CR206 CR208 CR224 CR225 CR303 CR304 CR336 CR304 CR336 CR305 CR305 CR305 CR306 CR375 CR406 CR415 CR420 CR415 CR420	3K 2C 3E 4B 3B 3C 4C 3B 2C 2J 4K 3K 3K 2J 2J 2J 2H 1H 2E 2F 3H 2F 2G 4I 4B 1B 2B	CR465  J001 J002 J100 J200 J300 J400 J500  L462 L485  LR810 LR820 LR830 LR840  P001 P210 P230 P301 P482 P820  C201 C202 C203	4D  3B 2B 1H 11 2K 5J 3E 3D 3B 3B 3B 3B 4L 4L 4L 4L 4L 4L 4J	Q206 Q210 Q214 Q230 Q234 Q242 Q272 Q274 Q322 Q324 Q328 Q336 Q342 Q356 Q372 Q376 Q402 Q406 Q410 Q415 Q420 Q425 Q438 Q438 Q454 Q458	3L 2I 2I 3K 3K 5D 5D 3G 3G 5F 2F 2G 2H 2H 2F 2G 2G 2G 2G 2C 2C 2D 2D	Q468 Q476 Q484 Q496 Q844 R002 R201 R202 R203 R204 R205 R206 R207 R208 R209 R211 R212 R213 R214 R215 R216 R217 R218 R219 R221 R222 R224	4D 3D 3D 3D 2J 4B 4K 4K 4K 3K 3K 3K 3I 21 21 21 21 21 21 21 21 21 4K	R227 R228 R230 R231 R232 R233 R234 R240 R241 R242 R243 R244 R271 R273 R274 R275 R301 R302 R303 R306 R306 R310 R315 R316 R316 R316	4K 3J 3K 4K 4K 2K 3K 2K 2K 5E 5E 4A 1H 1I 1F 1H 1H 1H 4E J	R323 R328 R332 R333 R334 R336 R337 R341 R342 R343 R344 R345 R352 R353 R354 R356 R357 R358 R359 R361 R362 R361 R362 R363 R361 R363 R371 R373 R373 R375 R376	3G 5F 2F 2F 2F 2F 2G 2G 2G 2H 2H 2G 2H 2H 3G 2H 3G 3G 3F 2F 2F	R383 R384 R385 R386 R388 R391 R392 R393 R394 R395 R396 R399 R401 R402 R403 R404 R405 R406 R409 R410 R415 R419 R421 R422 R425 R426	4E 4E 4E 4D 4I 4H 4H 4G 4G 4G 2G 1F 2G 1F 3K 4J 4B 4B 4B 4B 4B 4B 4B	R432 R433 R434 R435 R438 R439 R451 R452 R454 R455 R456 R457 R458 R459 R460 R461 R462 R464 R465 R468 R469 R470 R471 R472 R474 R475 R476	2C 2C 1B 1C 2B 2B 2B 2B 2C 3C 3C 2C 3C 3C 2D 4D 3E 3E 3D 4D 3C 3C 3C 3D 3C 3C 3D 3C 3C 3D 3C 3D 3C 3D 3C 3D 3C	R481 R482 R483 R485 R491 R492 R494 R495 R496 R821 R841 R842 R844 RT484 S230 S460 S800 T001 U220 U314 U386 U842 VR317	2E D3 3D 3E 3C 3D 3D 3D 3C 4C 2J 2J 3E 2K 2D 3G 3B 2L 2J 3E 2L 2J 3E 2L 2D 3C 3D 3C 2D 3C 3D





circuit board assembly.

GRID	CKT	GRID	CKT	GRID	CKT	GRID	CKT	GRID
COORD	NO	COORD	NO	COORD	NO	COORD	NO	COORD
4K 3J 3K 4K 4K 4K 2K 3K 2K 4J 5E 4A 1H 1I 1F 1H 1F 1H 4E 1J 3H	R323 R328 R332 R332 R333 R334 R336 R337 R338 R341 R342 R343 R344 R345 R352 R353 R354 R356 R357 R358 R359 R361 R362 R363 R364 R373 R375 R376 R376	3G 5F 2F 2F 2F 2F 3F 2G 2G 2G 2H 2H 2H 2G 2H 2H 2G 2G 2H 2H 2G 2F 2F 2F 2F 2F 2F 2F 2F 2F 2F 2F 2F 2F	R383 R384 R385 R386 R388 R391 R392 R393 R394 R395 R396 R397 R398 R401 R402 R403 R404 R405 R406 R409 R410 R415 R419 R421 R422 R425 R426 R426	4E 4E 4E 4E 4D 4I 4H 4H 4H 4G 4G 4G 2G 1F 2G 1F 3K 4J 4B 4B 4B 4B 4B 4B	R432 R433 R434 R435 R438 R439 R451 R452 R454 R455 R456 R457 R458 R459 R460 R461 R462 R464 R465 R466 R469 R470 R471 R472 R474 R475 R476 R476 R476 R476 R476	2C 2C 2C 1B 1C 2B 2B 2B 2C 2C 3C 2C 3C 2D 4D 3E 3E 3E 3D 4D 3C 3C 4D 3C 3C 3D 3D 3D 3D 3D 3D 3D 3D 3D 3D 3D 3D 3D	R481 R482 R483 R485 R491 R492 R494 R495 R496 R821 R841 R842 R844 RT484 S230 S460 S800 T001 U220 U314 U386 U842 VR317	2E D3 3D 3E 3C 3D 3D 3C 4C 2J 2J 2J 3E 2K 2D 3G 3B 2L 3G 3B 2L 2L 3E

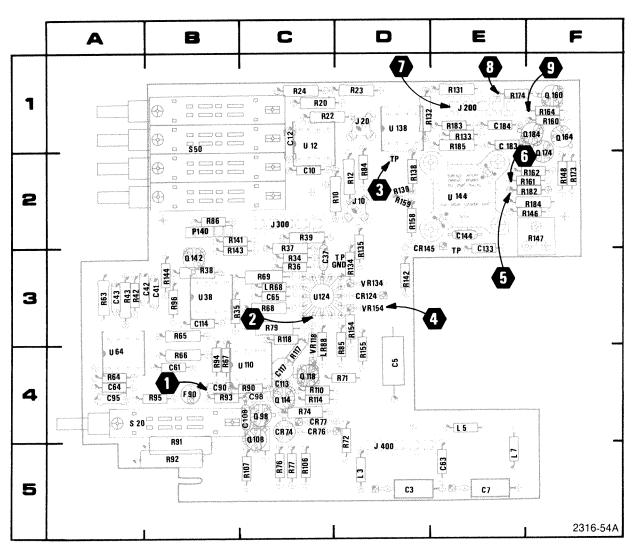
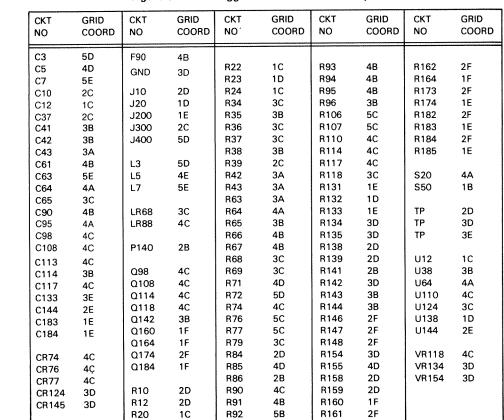
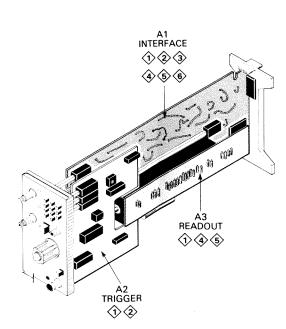


Figure 8-7. A2-Trigger circuit board assembly.





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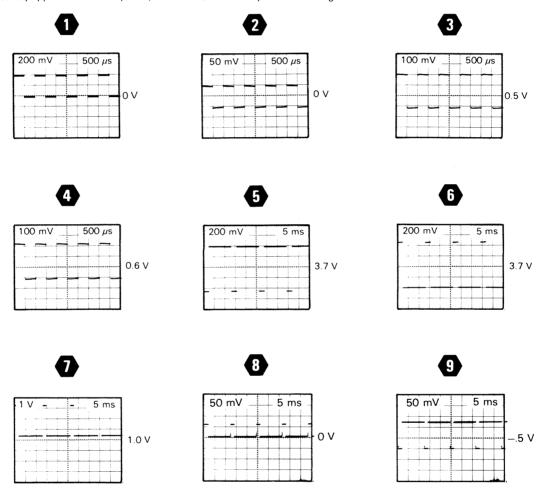
#### **VOLTAGE AND WAVEFORM CONDITIONS**

The voltages and waveforms shown were obtained with the controls set as follows:

TIME/DIV. 1 ms; VARIABLE (CAL IN), knob in; MAG X1 (button in); HOLD OFF, fully counterclockwise; SWP CAL, midranged; POSITION, midranged; TRIGGERING: LEVEL, midranged; MODE, AUTO; COUPLING, AC; SOURCE, EXT; SLOPE,  $\pm$ ; EXT TRIG IN, IN (1 M $\Omega$ ).

Voltage Conditions. The voltages shown on the diagram were obtained using a digital multimeter with a 1 M $\Omega$  input impedance (Tektronix DM 501 Digital Multimeter or Tektronix 7D13 Digital Multimeter used with readout equipped, series oscilloscope).

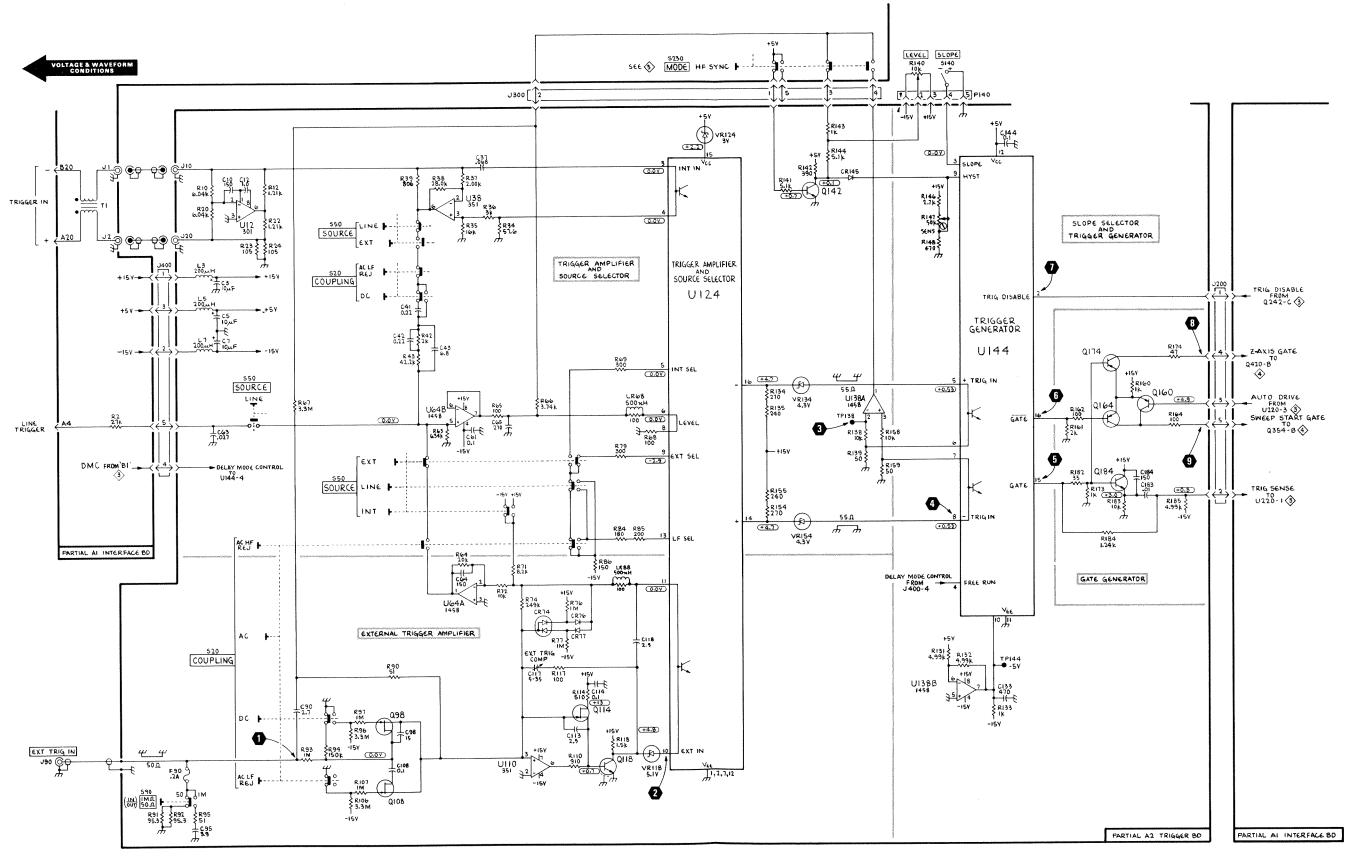
Waveform Conditions. The waveforms shown below were obtained using a test oscilloscope system with 1 M $\Omega$  input impedance and at least 15 MHz bandwidth (Tektronix 7603 Oscilloscope, 7B53A Time Base, and 7A13 Differential Comparator equipped with 10X probe). A 4 volt, 1 kHz square wave signal was fed to the EXT TRIG IN connector.





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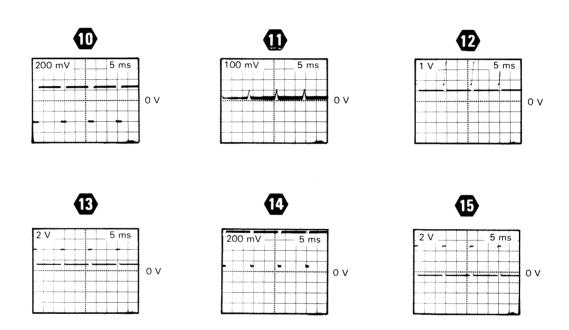
#### **VOLTAGE AND WAVEFORM CONDITIONS**

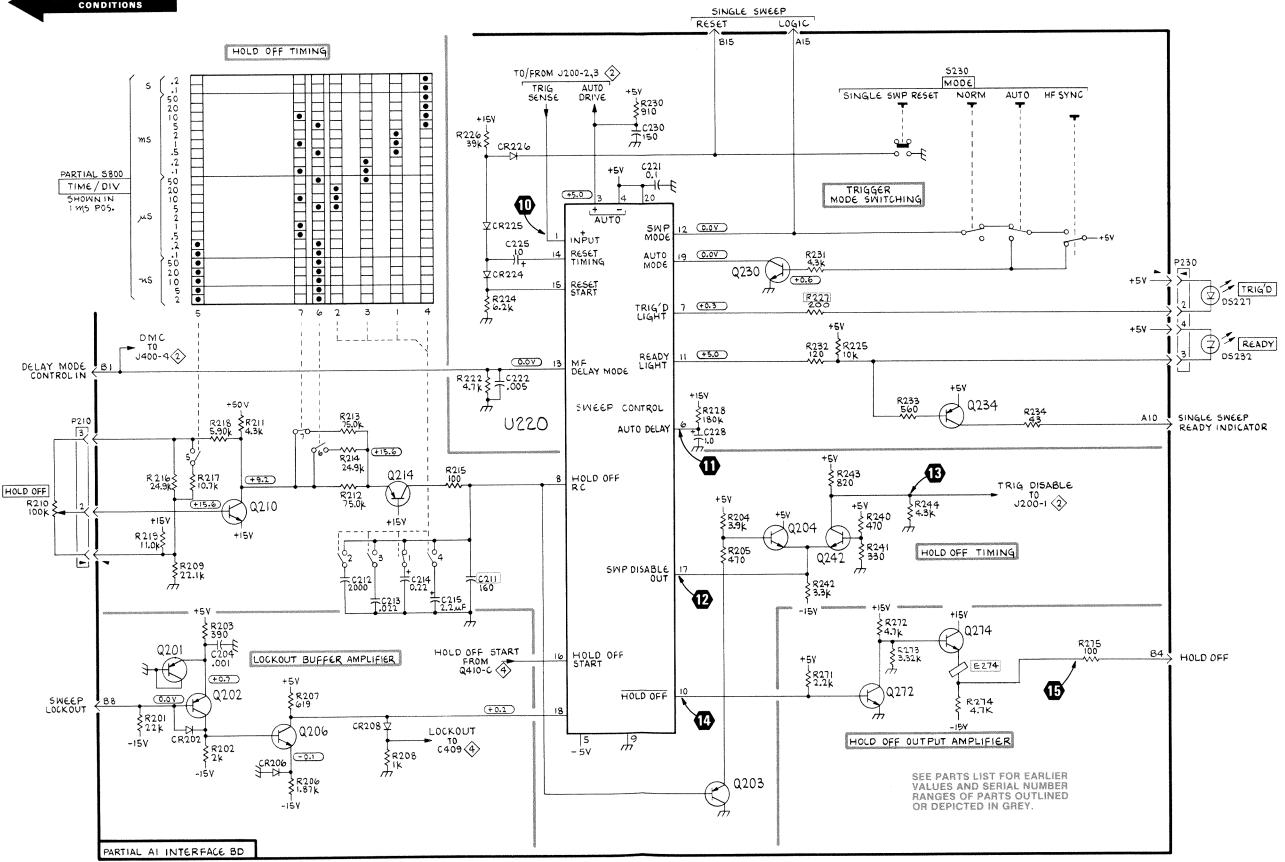
The voltages and waveforms shown were obtained with the controls set as follows:

TIME/DIV, 1 ms; VARIABLE (CAL IN), knob in; MAG XI (button in); HOLD OFF, fully counterclockwise; SWP CAL, midranged; POSITION, midranged; TRIGGERING: LEVEL, midranged; MODE, AUTO; COUPLING, AC; SOURCE, EXT; SLOPE, +; EXT TRIG IN, IN (1 M $\Omega$ ),

Voltage Conditions. The voltages shown on the diagram were obtained using a digital multimeter with a 1 M $\Omega$  input impedance (Tektronix DM 501 Digital Multimeter or Tektronix 7D13 Digital Multimeter used with readout equipped, 7000-series oscilloscope).

Waveform Conditions. The waveforms shown below were obtained using a test oscilloscope system with 1 M $\Omega$  input impedance and at least 15 MHz bandwidth (Tektronix 7603 Oscilloscope, 7B53A Time Base, and 7A13 Differential Comparator equipped with 10X probe). A 4 volt, 1 kHz square wave signal was fed to the EXT TRIG IN connector.





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 $\Leftrightarrow$ 

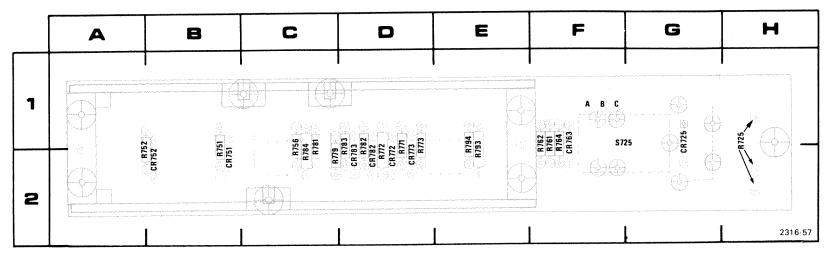
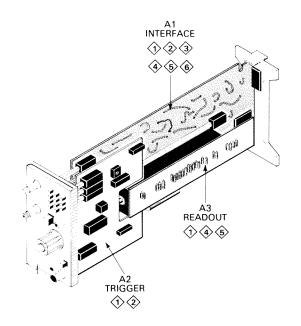


Figure 8-8. A3-Readout circuit board assembly.

CKT NO	GRID COORD	CKT NO	GRID COORD
CR725	2G	R764	2F
CR751	2B	R771	2D
CR752	2B	R772	2D
CR772	2D	R773	2D
CR773	2D	R779	2C
CR782	2D	R781	2C
CR783	2D	R782	2D
		R783	2D
R725	2H	R784	2C
R751	2B	R793	2E
R752	2A	R794	2E
R756	2C		
R761	2F	S725	1F





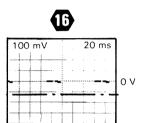
#### **VOLTAGES AND WAVEFORM CONDITIONS**

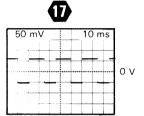
The voltages and waveforms shown were obtained with the controls set as follows:

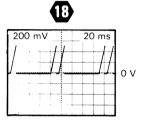
TIME/DIV, 1 ms; VARIABLE (CAL IN), knob in; MAG X1 (button in); HOLD OFF, fully counterclockwise; SWP CAL, midranged; POSITION, midranged; TRIGGERING: LEVEL, midranged; MODE, AUTO; COUPLING, AC; SOURCE, EXT; SLOPE, +; EXT TRIG IN, IN (1 M $\Omega$ ).

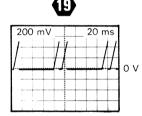
**Voltage Conditions**. The voltages shown on the diagram were obtained using a digital multimeter with a 1 M $\Omega$  input impedance (Tektronix DM 501 Digital Multimeter or Tektronix 7D13 Digital Multimeter used with readout equipped, 7000-series oscilloscope).

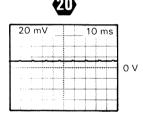
Waveform Conditions. The waveforms shown below were obtained using a test oscilloscope system with 1 M $\Omega$  input impedance and at least 15 MHz bandwidth (Tektronix 7603 Oscilloscope, 7B53A Time Base, and 7A13 Differential Comparator equipped with 10X probe).

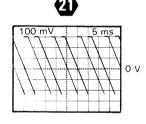


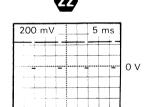


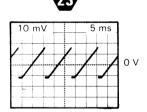


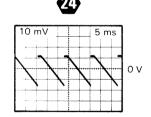




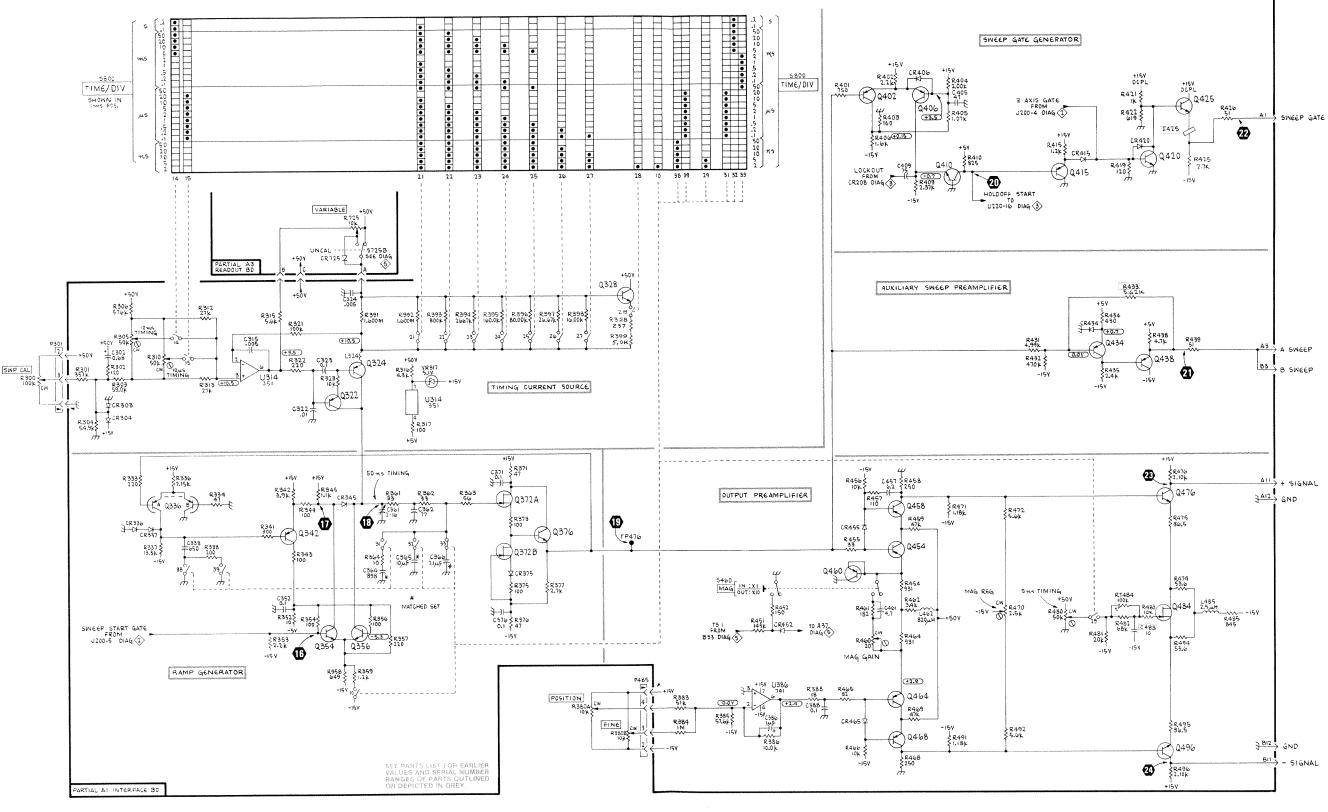


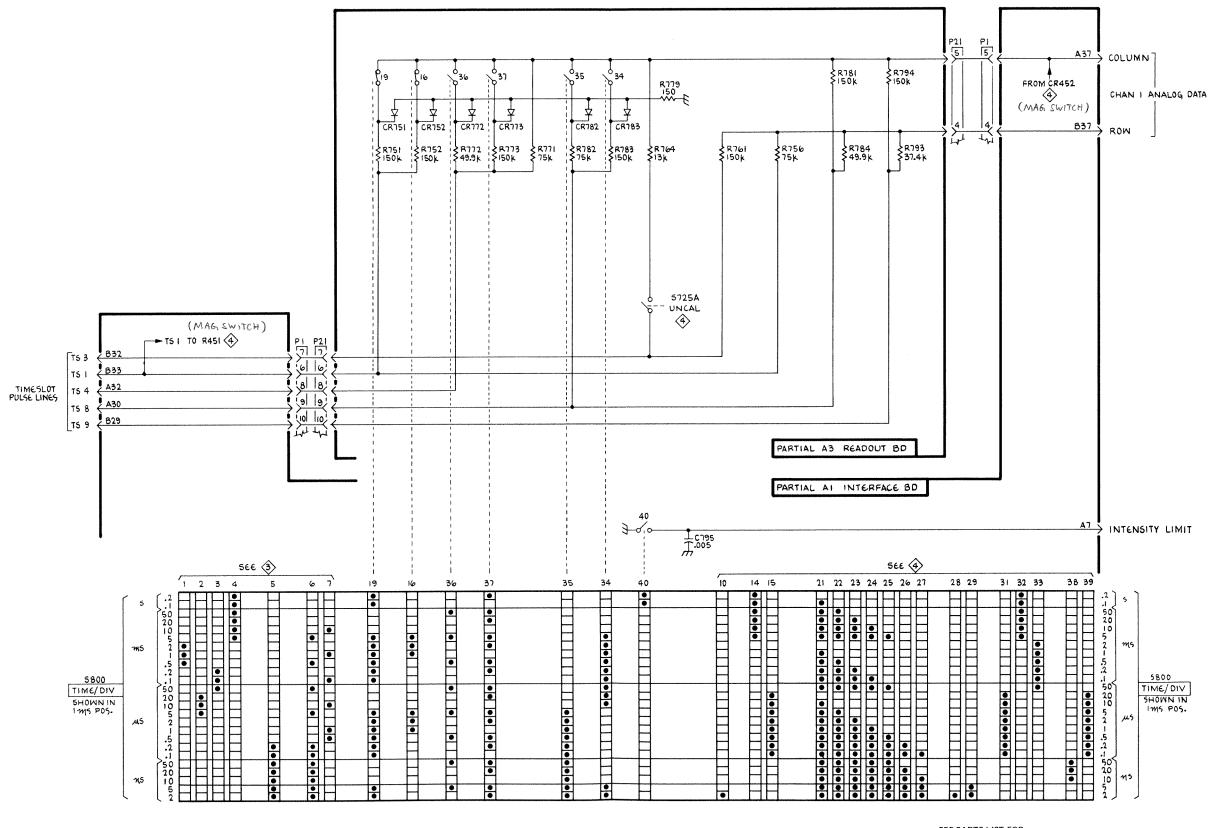




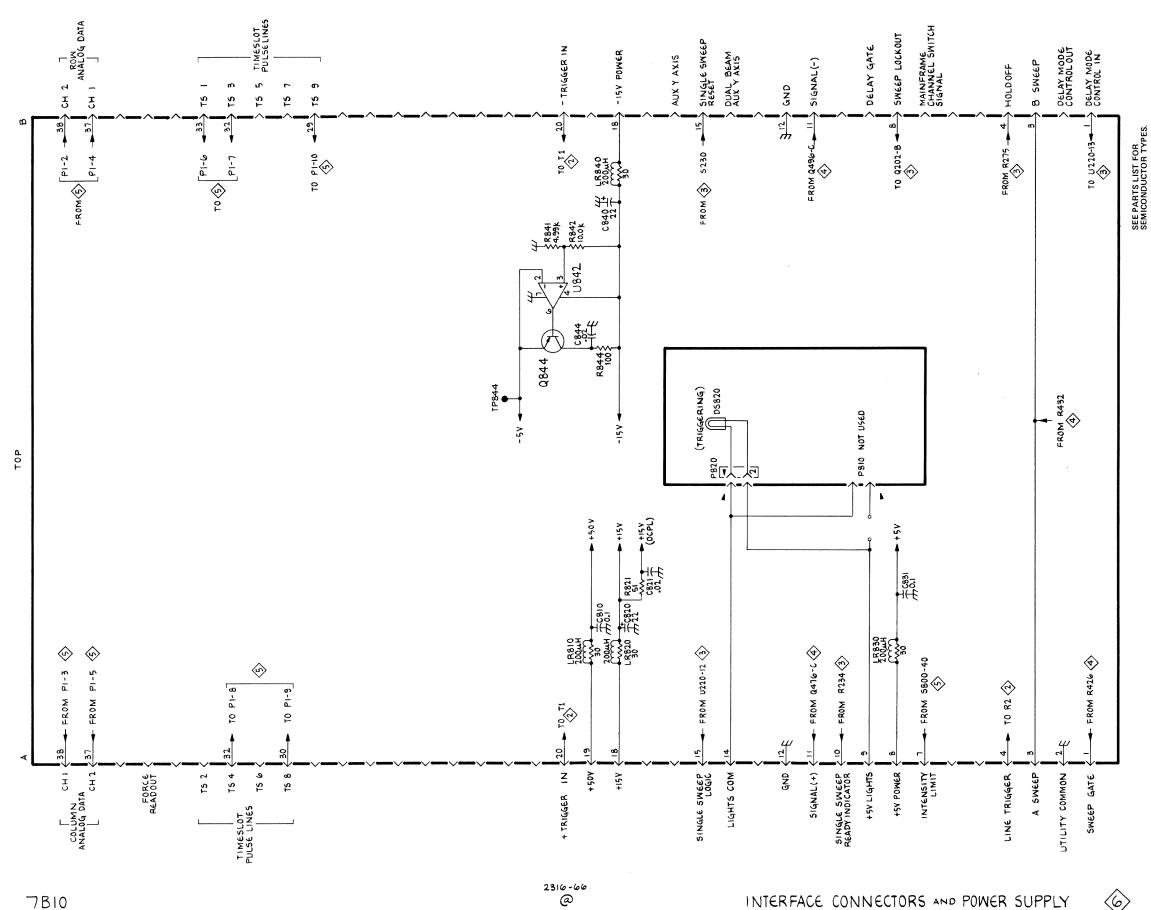








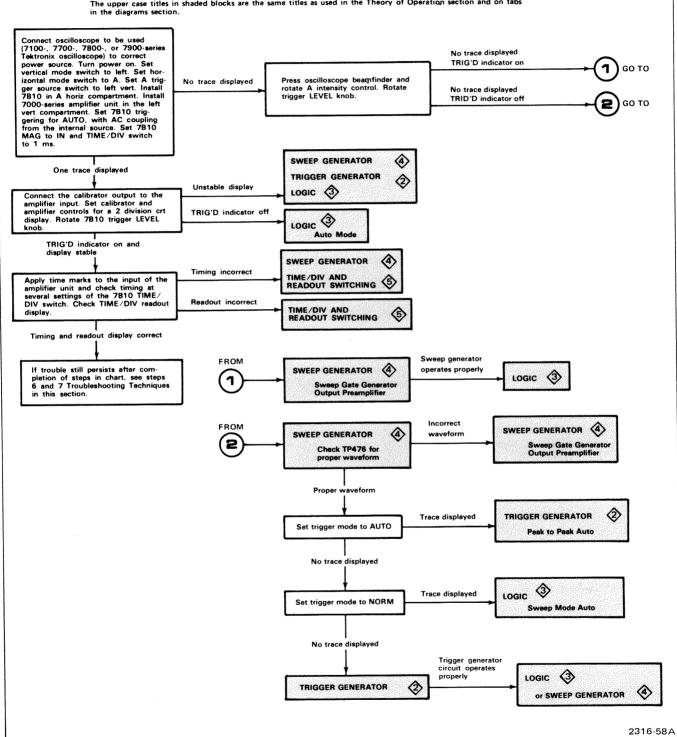
SEE PARTS LIST FOR SEMICONDUCTOR TYPES.



- 1. Proceed down the left side of chart until the instrument does not perform as indicated. Then proceed to the right as the symptom(s) indicates.
- 2. Follow the path(s) of symptoms until a shaded block is reached. The shaded block indicates the circuit(s) that may cause instrument malfunction. Refer to the Theory of Operation section for a detailed discussion of the circuit(s).

#### NOTE

The upper case titles in shaded blocks are the same titles as used in the Theory of Operation section and on tabs



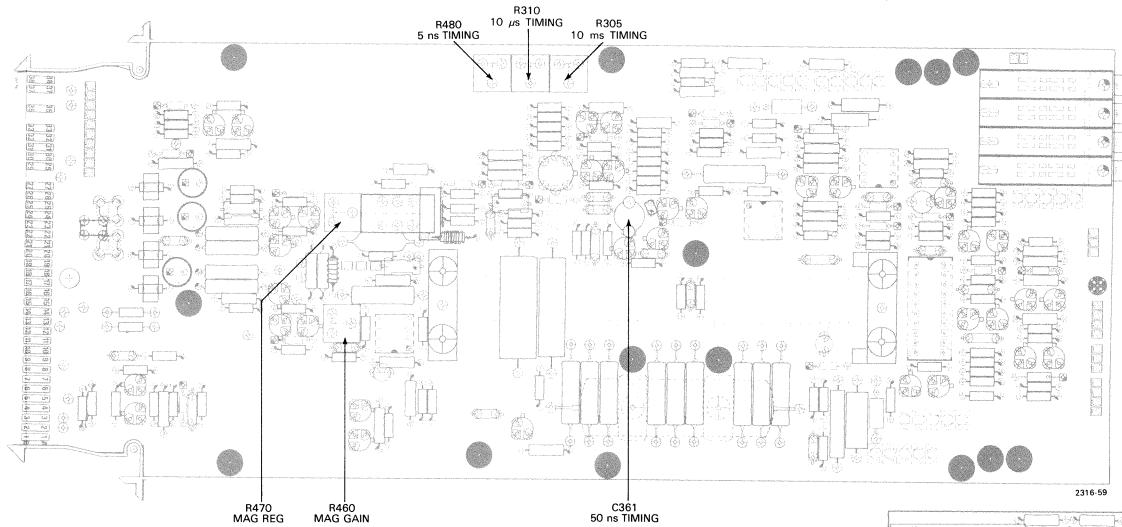
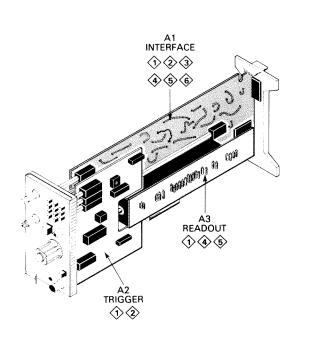
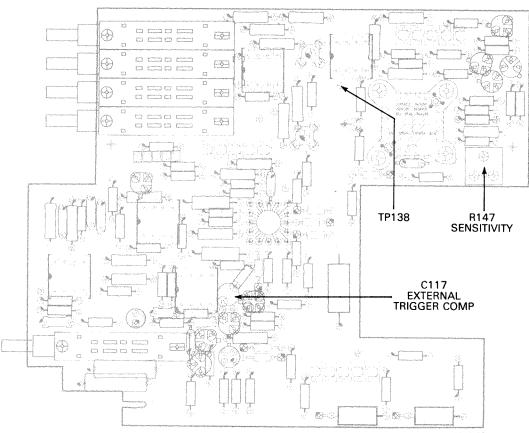


Figure 8-9. Location of Sweep Timing adjustments.





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Figure 8-10. Location of Triggering adjustments.

# REPLACEABLE MECHANICAL PARTS

### PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix. Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number

Change information, if any, is located at the rear of this manual.

#### **ITEM NAME**

In the Parts List, an Item Name is separated from the description by a colon (:) Because of space limitations, an Item Name may sometimes appear as incomplete. For further Item Name identification, the U.S. Federal Cataloging Handbook H6-1 can be utilized where possible.

#### FIGURE AND INDEX NUMBERS

Items in this section are referenced by figure and index numbers to the illustrations.

#### INDENTATION SYSTEM

This mechanical parts list is indented to indicate item relationships. Following is an example of the indentation system used in the description column.

1 2 3 4 5

Name & Description

Assembly and or Component
Attaching parts for Assembly and/or Component
....END ATTACHING PARTS....
Detail Part of Assembly and/or Component

Attaching parts for Detail Part

----END ATTACHING PARTS----

Parts of Detail Part
Attaching parts for Parts of Detail Part
----END ATTACHING PARTS----

Attaching Parts always appear in the same indentation as the item it mounts, while the detail parts are indented to the right. Indented items are part of, and included with, the next higher indentation.

Attaching parts must be purchased separately, unless otherwise specified.

## **ABBREVIATIONS**

	INCH	ELCTRN	ELECTRON	IN	INCH	SE	SINGLE END
#	NUMBER SIZE	ELEC	ELECTRICAL	INCAND	INCANDESCENT	SECT	SECTION
ACTR	ACTUATOR	ELCTLT	ELECTROLYTIC	INSUL	INSULATOR	SEMICON	SEMICONDUCTOR
ADPTR	ADAPTER	ELEM	ELEMENT	INTL	INTERNAL	SHLD	SHIELD
ALIGN	ALIGNMENT	EPL	ELECTRICAL PARTS LIST	LPHLDR	LAMPHOLDER	SHLDR	SHOULDERED
AL	ALUMINUM	EOPT	EQUIPMENT	MACH	MACHINE	SKT	SOCKET
ASSEM	ASSEMBLED	EXT	EXTERNAL	MECH	MECHANICAL	SL	SLIDE
ASSY	ASSEMBLY	FIL	FILLISTER HEAD	MTG	MOUNTING	SLFLKG	SELF-LOCKING
ATTEN	ATTENUATOR	FLEX	FLEXIBLE	NIP	NIPPLE	SLVG	SLEEVING
AWG	AMERICAN WIRE GAGE	FLH	FLAT HEAD	NON WIRE	NOT WIRE WOUND	SPR	SPRING
BD	BOARD	FLTR	FILTER	OBD	ORDER BY DESCRIPTION	SQ	SQUARE
BRKT	BRACKET	FR	FRAME or FRONT	OD	OUTSIDE DIAMETER	SST	STAINLESS STEEL
BRS	BRASS	FSTNR	FASTENER	OVH	OVAL HEAD	STL	STEEL
BRZ	BRONZE	FT	FOOT	PH BRZ	PHOSPHOR BRONZE	SW	SWITCH
BSHG	BUSHING	FXD	FIXED	PL	PLAIN or PLATE	Т	TUBE
CAB	CABINET	GSKT	GASKET	PLSTC	PLASTIC	TERM	TERMINAL
CAP	CAPACITOR	HDL	HANDLE	PN	PART NUMBER	THD	THREAD
CER	CERAMIC	HEX	HEXAGON	PNH	PAN HEAD	THK	THICK
CHAS	CHASSIS	HEX HD	HEXAGONAL HEAD	PWR	POWER	TNSN	TENSION
CKT	CIRCUIT	HEX SOC	HEXAGONAL SOCKET	RCPT	RECEPTACLE	TPG	TAPPING
COMP	COMPOSITION	HLCPS	HELICAL COMPRESSION	RES	RESISTOR	TRH	TRUSS HEAD
CONN	CONNECTOR	HLEXT	HELICAL EXTENSION	RGD	RIGID	V	VOLTAGE
COV	COVER	HV	HIGH VOLTAGE	RLF	RELIEF	VAR	VARIABLE
CPLG	COUPLING	IC	INTEGRATED CIRCUIT	RTNR	RETAINER	W/	WITH
CRT	CATHODE RAY TUBE	1D	INSIDE DIAMETER	SCH	SOCKET HEAD	WSHR	WASHER
DEG	DEGREE	IDENT	IDENTIFICATION	SCOPE	OSCILLOSCOPE	XEMR	TRANSFORMER
DWR	DRAWER	IMPLR	IMPELLER	SCR	SCREW	XSTR	TRANSISTOR

## CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER

Mfr. Code	Manufacturer	Address	City, State, Zip Code
01536	TEXTRON INC CAMCAR DIV SEMS PRODUCTS UNIT	1818 CHRISTINA ST	ROCKFORD IL 61108
07707	USM CORP SUB OF EMHART INDUSTRIES INC USM FASTENER DIV	510 RIVER RD	SHELTON CT 06848-4517
08261	SPECTRA-STRIP AN ELTRA CO	7100 LAMPSON AVE	GARDEN GROVE CA 92642
09772	WEST COAST LOCKWASHER CO INC	P O BOX 3588	
09922	BURNDY CORP	RICHARDS AVE	NORWALK CT 06852
12327	FREEWAY CORP	9301 ALLEN DR	CLEVELAND OH 44125-4632
22526	DU PONT E I DE NEMOURS AND CO INC DU PONT CONNECTOR SYSTEMS DIV MILITARY PRODUCTS GROUP		NEW CUMBERLAND PA 17070-3007
22599	DU PONT CONNECTOR SYSTEMS DIV MILITARY PRODUCTS GROUP AMERACE CORP ESNA DIV	15201 BURBANK BLVD SUITE C	VAN NUYS CA 91411-3532
24546	CORNING GLASS WORKS	550 HIGH ST	BRADFORD PA 16701-3737
46384	PENN ENGINEERING AND MFG CORP	SUITE C 550 HIGH ST OLD EASTON RD PO BOX 1000 HIGHLAND AVE	DANBORO PA 18916
56878	SPS TECHNOLOGIES INC AEROSPACE & INDUSTRIAL PRODUCTS DIV		
73743	FISCHER SPECIAL MFG CO	111 INDUSTRIAL RD	COLD SPRING KY 41076-9749
77900	ILLINOIS TOOL WORKS SHAKEPROOF DIV	ST CHARLES RD	ELGIN IL 60120
78189	SHAKEPROOF DIV ILLINOIS TOOL WORKS INC SHAKEPROOF DIV	ST CHARLES ROAD	ELGIN IL 60120
79136	WALDES KOHINOOR INC	47-16 AUSTEL PLACE	LONG ISLAND CITY NY 11101-4402
80009	SHAKEPROOF DIV WALDES KOHINOOR INC TEKTRONIX INC  ARROW FASTENER CO INC ELCO INDUSTRIES INC FARLEY METALS INC SOUTHERN SCREW DIV CONNOR SPRING AND MFG CO A SLOSS AND BRITTAN INC CO	14150 SW KARL BRAUN DR PO BOX 500	BEAVERTON OR 97707-0001
83294	ARROW FASTENER CO INC	271 MAYHILL ST	SADDLE BROOK NJ 07662-5303
83486	ELCO INDUSTRIES INC	1101 SAMUELSON RD	ROCKFORD IL 61101
87308	FARLEY METALS INC SOUTHERN SCREW DIV	BARKLEY RD P O BOX 1360	STATESVILLE NC 28677-9774
91260	CONNOR SPRING AND MFG CO A SLOSS AND BRITTAN INC CO	1729 JUNCTION AVE	SAN JOSE CA 95112
93907	TEXTRON INC	600 18TH AVE	ROCKFORD IL 61108-5181
TK0392	NORTHWEST FASTENER SALES INC	7923 SW CIRRUS DRIVE	BEAVERTON OR 97005-6448
TK0435	LEWIS SCREW CO	4300 S RACINE AVE	CHICAGO IL 60609-3320
TK0507	O HARA METAL PRODUCTS CO	542 BRANNAN ST	SAN FRANCISCO CA 94107
TK0858	STAUFFER SUPPLY CO (DIST)	810 SE SH <b>ERMA</b> N	PORTLAND OR 97214
TK1326	NORTHWEST FASTENER SALES INC LEWIS SCREW CO O HARA METAL PRODUCTS CO STAUFFER SUPPLY CO (DIST) NORTHWEST FOURSLIDE INC	18224 SW 100TH CT	TUALATIN OR 97062

Fig. & Index No.	Tektronix Part No.	Serial/Asse Effective		Qty	12345 Name & Description	Mfr. Code	Mfr. Part No.
1-1	337-1064-04	B010100	B032122	2	SHIELD, ELEC:SIDE FOR PLUG-IN UNIT SHIELD, ELEC:SIDE FOR PLUG-IN UNIT KNOB:LT GY,0.081 ID X 0.28 OD X 0.32 H .SETSCREW:3-48 X 0.094,STL KNOB:GY,0.079 ID X 0.28 OD X 0.32 H .SETSCREW:3-48 X 0.094,STL	80009	337-1064-04
	337-1064-12			2	SHIELD, ELEC: SIDE FOR PLUG-IN UNIT	80009	337-1064-12
-2	366-1391-02			1	KNOB:LT GY, 0.081 ID X 0.28 OD X 0.32 H	80009	366-1391-02
2	213-0725-00			1 1	.SEISCREW:3-48 X 0.094,SIL	20000 20000	ORDER BY DESCR 366-1319-02
-3	366-1319-02 213-0725-00			1	SETSCREW:3-48 X 0 094 STI	56878	ORDER BY DESCR
-4	366-1077-00			2	KNOB: GRAY W/SETSCREW	80009	366-1077-00
	213-0153-00			2	.SETSCREW:3-48 X 0.094,STL KNOB:GY,0.079 ID X 0.28 OD X 0.32 H .SETSCREW:3-48 X 0.094,STL KNOB:GRAY W/SETSCREW .SETSCREW:5-40 X 0.125,STL FRAME PUSH RTN	TK0392	ORDER BY DESCR
-5	426-0681-00			1	FRAME, PUSH BTN:	80009	426-0681-00
-6	366-1023-01 213-0153-00			1	KNUB:GY,U.12/ ID X U.392 UU X U.531 H	E0009	UBUED BY DESCR
-7	366-1166-00			1	KNOB:RED.O.127 ID X 0.392 OD X 0.4 H	80009	366-1166-00
•	213-0153-00			1	.SETSCREW:5-40 X 0.125,STL	TK0392	ORDER BY DESCR
-8	366-1103-00			1	FRAME, PUSH BTN: KNOB:GY, 0.127 ID X 0.392 OD X 0.531 H .SETSCREW:5-40 X 0.125, STL KNOB:RED, 0.127 ID X 0.392 OD X 0.4 H .SETSCREW:5-40 X 0.125, STL KNOB:GY, 0.525 ID X 1.095 OD X 0.79 H .SETSCREW:5-40 X 0.125, STL KNOB:LATCH, MARKED 7B10 (ATTACHING PADTS)	80009	366-1103-00
-9	213-0153-00			2 1	SETSCREW:5-40 X 0.125,SIL	90000	ORDER BY DESCR
-9	366-1058-76			1	(ATTACHING PARTS)	00003	300-1030-70
-10	214-1095-00			1	PIN,SPRING:0.187 L X 0.094 OD,STL,CD PL		
-11	105-0076-02		B031754	1	(END ATTACHING PARTS) RELEASE BAR, LCH: PLUG-IN UNIT RELEASE BAR, LCH: PLUG-IN UNIT SPRING, HLCPS: 0.14 OD X 1.126 L, TWIST LOOP FRAME, PUSH BTN: SILVER GRAY PLSTC PANEL, FRONT: GUIDE, PUSH BTN: 4 BUTTON GUIDE, PUSH BTN: 3 BUTTON BASE, LAMPHOLDER: 0.29 OD X 0.19 L, BK PLSTC LAMPHOLDER: (1)T-2 UNBASED, WHITE BESISTOR WAR (SEE BLAO AND SIAO BERL)	80009	105-0076-02
10	105-0076-04	B031755		1	RELEASE BAR, LCH: PLUG-IN UNIT	80009	105-0076-04
-12 -13	214-1280-00 426-1072-00			1	SPRING, MLCPS: U.14 UD X 1.120 L, IWIST LOUP EDAME DICH RIN-CITVED GDAY DICTO	80000	426-1072-00
-14	333-1213-11			1	PANEL.FRONT:	80009	333-1213-11
-15	351-0469-00			2	GUIDE, PUSH BTN:4 BUTTON	80009	351-0469-00
-16	351-0469-01			1	GUIDE, PUSH BTN:3 BUTTON	80009	351-0469-01
-17	200-0935-00			3 2	BASE, LAMPHOLDER: 0.29 OD X 0.19 L, BK PLSIC	80009	200-0935-00
-18 -19	352-0157-00			1	(ATTACHING PAPTS)		
-20	210-0583-00			2	NUT, PLAIN, HEX: 0.25-32 X 0.312, BRS CD PL WASHER, LOCK: 0.261 ID, INTL, 0.018 THK, STL WASHER, FLAT: 0.25 ID X 0.375 OD X 0.02, STL	73743	2X-20319-402
	210-0046-00		B032097	1	WASHER, LOCK: 0.261 ID, INTL, 0.018 THK, STL	77900	1214-05-00-0541C
	210-0940-00	B032098	B034149	1	(END ATTACHING PARTS)	12327	OKDEK BY DESCR
-21				1	RESISTOR, VAR: (SEE R380A AND B REPL) (ATTACHING PARTS)		
-22	210-0583-00			2	NUT, PLAIN, HEX: 0.25-32 X 0.312, BRS CD PL	73743	2X-20319-402
-23	210-0046-00			1	WASHER,LOCK:0.261 ID,INTL,0.018 THK,STL (END ATTACHING PARTS)	77900	1214-05-00-0541C
-24				1	RESISTOR, VAR: (SEE R210 REPL)		
				_	(ATTACHING PARTS)		
-25	210-0583-00 210-0583-00		B032097	2	NUL, PLAIN, HEX: 0.25-32 X 0.312, BRS CD PL	/3/43	2X-20319-402
-26	210-0563-00		B032097	1 3	NUT, PLAIN, HEX: 0.25-32 X 0.312, BRS CD PL NUT, PLAIN, HEX: 0.25-32 X 0.312, BRS CD PL WASHER, LOCK: 0.261 ID, INTL, 0.018 THK, STL	77900	1214-05-00-05410
20	210-0076-00		B034149	2	WASHER, LOCK: 0.255 ID SPLIT, 0.065 THK, STL	80009	210-0076-00
					(END ATTACHING PARTS)		
-27	010 0040 00			1	RESISTOR, VAR: (SEE R300 REPL)	77000	1014 05 00 05410
-28 -29	210-0046-00			1	WASHER,LOCK:0.261 ID,INTL,0.018 THK,STL (ATTACHING PARTS)	//900	1214-05-00-0541C
23	129-0290-00	B010100	B034149	1	SPCR, POST: 0.635 L, 0.25-32 THRU, AL, 0.312 HEX	80009	129-0290-00
	129-0252-00			1	SPCR, POST: 0.471 L, 0.25-32 THRU, AL, 0.312 HEX		129-0252-00
-30	358-0342-00			1	BSHG, MACH THD: 0.25-32 X 0.159 0.352 L		358-0342-00
-31	210-0046-00			1	WASHER,LOCK:0.261 ID,INTL,0.018 THK,STL (END ATTACHING PARTS)	//900	1214-05-00-0541C
-32	131-1315-00			1	CONN, RCPT, ELEC:BNC, FEMALE (ATTACHING PARTS)	80009	131-1315-00
-33	210-0012-00	B010100	B034149	1	WASHER,LOCK:0.384 ID,INTL,0.022 THK,STL (END ATTACHING PARTS)	09772	ORDER BY DESCR
-34	348-0235-00			2	SHLD GSKT,ELEK:FINGER TYPE,4.734 L		348-0235-00
-35	348-0067-00			1	GROMMET, PLASTIC: GRAY, ROUND, 0.252 ID		348-0067-00 386-3256-00
-36	386-3256-00			1	SUBPANEL, FRONT: (ATTACHING PARTS)	00009	300-3230 <b>-</b> 00
-37	213-0192-00	B010100	B021124	4	SCREW, TPG, TF:6-32 X 0.5, SPCL TYPE, FILH, STL		ORDER BY DESCR
	213-0793-00	B021125		4	SCREW, TPG, TF:6-32 X 0.4375, TAPTITE, FILH	83486	239-006-406043
-38	386-3439-00			1	(END ATTACHING PARTS) LT CNDCT,PB ILL:10 BUTTON,7.5MM SPACING	20000	386-3439-00
-36 -39	131-1266-00			1	CONTACT, PE ILL: 10 BOTTON, 7.544 SPACING CONTACT, ELEC: GROUNDING, CU BE CU-SN-ZN PL		131-1266-00
-40	214-1061-00			ī	CONTACT, ELEC: GROUNDING, CU BE		214-1061-00

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Fig. &							
Index No.	Tektronix Part No.	Serial/Assa Effective		Qty	12345 Name & Description	Mfr. Code	Mfr. Part No.
1-41	214-1054-00			1	SPRING, FLAT: 0.825 X 0.322, SST		ORDER BY DESCR
-42	105-0075-00			1	BOLT, LATCH:	80009	105-0075-00
-43 -44	378-0074-00 366-1650-00			4 4	PUSH BUTTON:CLEAR, 0.184 X 0.214 X 8.0 PUSH BUTTON:CLEAR, 0.184 X 0.214 X 8.0 PUSH BUTTON:SIL GY,XI X10 PUSH BUTTON:SIL GY,0.18 SQ X 0.43 CONN,PLUG,ELEC:CKT BD,5 CONTACT MALE CKT BOARD ASSY:TRIGGER(SEE A2 REPL)	80009	378-0074-00 366-1650-00
-44 -45	366-1257-93			1	PUSH BUTTON-STU GY Y1 Y10	80009	366-1257-93
-46	366-1559-00			1	PUSH BUTTON: STE GY. O. 18 SO X 0.43	80003	366-1559-00
-47	131-1820-00			3	CONN, PLUG, ELEC: CKT BD, 5 CONTACT MALE	22526	65306-002
-48				1	(ATTACHING DADTE)		
-49	211-0008-00	B010100	B032099	2	SCREW, MACHINE: 4-40 X 0.25, PNH, STL SCREW, MACHINE: 6-32 X 0.375, HEX HD, STL	93907	ORDER BY DESCR
	211-0293-00	B032100		2	SCREW, MACHINE: 6-32 X 0.375, HEX HD, STL	83294	ORDER BY DESCR
					(END ATTACHING PARTS)		
-50	378-0074-00			7	CKT BOARD ASSEMBLY INCLUDES:	90000	279_0074_00
-51	366-1650-00			7	PUSH RUTTON-CUFAR O 184 X O 214 X 8 O	80009	366-1650-00
-52	263-0016-03			1	.SWITCH PB ASSY:4 LATCH,7.5MM,6 CONTACTS	80009	263-0016-03
-53	263-0015-00			1	.SWITCH PB ASSY:3 LATCHING,7.5 MM	80009	263-0015-00
-54	343-0495-04			1	CKT BOARD ASSEMBLY INCLUDES: .REFLECTOR,LIGHT:PUSH BUTTON .PUSH BUTTON:CLEAR,O.184 X 0.214 X 8.0 .SWITCH PB ASSY:4 LATCH,7.5MM,6 CONTACTS .SWITCH PB ASSY:3 LATCHING,7.5 MM .CLIP,SWITCH:FRONT,7.5MM X 4 UNIT .(ATTACHING PARTS)	80009	343-0495-04
-55	210-3050-00			3	EYELET, METALLIC: 0.059 OD X 0.218 L, BRS	80009	210-3050-00
	210-3033-00			1	.EYELET, METALLIC: 0.059 OD X 0.218 L,BRS .EYELET, METALLIC: 0.059 OD X 0.156 L,BRS	07707	SE-25
-56	343-0495-03			1			343-0495-03
<b>-</b> 57	343-0499-04	B010100	B021344	ī	.CLIP.SWITCH:REAR.7.5MM X 4 UNIT	80009	343-0499-04
	343-0499-13	B021545		1	.CLIP, SWITCH: FRONT, 7.5MM X 3 UNIT .CLIP, SWITCH: REAR, 7.5MM X 4 UNIT .CLIP, SWITCH: 7.75MM X 4 UNIT	80009	343-0499-13
F0	040 0050 00				IAIIAI HINE PARISI		
-58	210-3050-00 210-3033-00			3 1	.EYELET, METALLIC: 0.059 OD X 0.218 L, BRS .EYELET, METALLIC: 0.059 OD X 0.156 L, BRS	80009 07707	210-3050-00
	210-3033-00			1	(END ATTACHING PARTS)	0//0/	3E-23
-59	343-0499-03	B010100	B021544	1	.CLIP,SWITCH:REAR,7.5MM X 3 UNIT	80009	343-0499-03
	343-0499-12	B021545		1	.CLIP,SWITCH:REAR,7.5MM X 3 UNIT	80009	343-0499-12
-60	131-0589-00			5	.TERMINAL,PIN:0.46 L X 0.025 SQ PH BRZ	22526	48283-029
-61 -62	131-1003-00 263-0010-02			2 1	CONN,RCPI,ELEC:CKI BD MI,3 PRONG	80009	131-1003-00 263-0010-02
-63	343-0495-01			1	.(END ATTACHING PARTS) .CLIP,SWITCH:REAR,7.5MM X 3 UNIT .CLIP,SWITCH:REAR,7.5MM X 3 UNIT .TERMINAL,PIN:0.46 L X 0.025 SQ PH BRZ .CONN,RCPT,ELEC:CKT BD MT,3 PRONG .SWITCH PB ASSY:1 PUSH,7.5MM,1 CONTACT .CLIP,SWITCH:FRONT,7.5MM X 1 UNIT	80009	343-0495-01
	2.0 0.00 02			-	. (A) (AC) (INC) (AN) (3)		
-64	210-3033-00			1	.EYELET, METALLIC: 0.059 OD X 0.156 L, BRS	07707	SE-25
-65	343-0499-01			1	.(END ATTACHING PARTS) .CLIP,SWITCH:REAR,7.5MM X 1 UNIT	80009	343-0499-01
				-	.(ATTACHING PARTS)		
-66	210-3033-00			1	.ÈYELET,METALLIC:0.059 OD X 0.156 L,BRS .(END ATTACHING PARTS)	07707	SE-25
-67	136-0514-00	B010100	B031699	4	.SKT, PL-IN ELEK: MICROCIRCUIT, 8 DIP	09922	DILB8P-108
-68	214-0579-00			3	.TERM,TEST POINT:BRS CD PL		214-0579-00
-69	136-0252-04			58	.SOCKET, PIN TERM: U/W 0.016-0.018 DIA PINS		75060-007
-70 -71	136-0263-04 426-1337-00			14 1	.SOCKET,PIN TERM:U/W 0.025 SQ PIN .FRAME,MICROCKT:1.22 CM	80009	75377-001 426-1337-00
-/1	420-1337-00			1	.(ATTACHING PARTS)	60003	420-1337-00
-72	211-0259-00			4	.SCR, ASSEM WSHR: 2-56 X 0.437, PNH, STL, POZ	01536	4821-00021
-73	131-1923-00			1	.(END ATTACHING PARTS) .CONTACT,ELEC:MICROCIRCUIT	80009	131-1923-00
-74	220-0797-00			4	.NUT,CAPTIVE:2-56 X 0.218 DIA,STL CD PL		CKF2-256
-75	384-1100-00			1	EXTENSION SHAFT: 6.215 L X 0.187 SQ, PLASTIC		384-1100-00
-76	384-1292-00			1	EXTENSION SHAFT: 2.417 L,GL FILLED NYLON		384-1292-00
-77	129-0198-00			2	SPCR,POST:0.75 L,4-40 EA END,BRS,0.188 HEX (ATTACHING PARTS)	80009	129-0198-00
-78	211-0008-00			2	SCREW,MACHINE:4-40 X 0.25,PNH,STL (END ATTACHING PARTS)	93907	ORDER BY DESCR
-79	384-1382-00			4	EXTENSION SHAFT: 1.09 L, OFFSET, MLD PLSTC		384-1382-00
-80	204-0683-00	DO1 01 00	DAGEGOZ	3	CONN BODY, RCPT:5 FEMALE PV CONTACTS		65058-001
-81	386-1402-00 386-5466-00		B035207	1 1	PANEL,REAR: PANEL,REAR:		386-1402-00 386-5466-00
	JUU-J <del>1</del> UU-UU	2007500		1	(ATTACHING PARTS)	00003	000 D <del>1</del> 00-00
-82	213-0192-00		B021124	4	SCREW, TPG, TF:6-32 X 0.5, SPCL TYPE, FILH, STL		ORDER BY DESCR
_00	213-0793-00	B021125		4	SCREW, TPG, TF:6-32 X 0.4375, TAPTITE, FILH		239-006-406043 361-0326-00
-83	361-0326-00			1	SPACER,SLEEVE:0.1 L X 0.18 ID,AL (NO LONGER USED)	00003	201-020-00
					(END ATTACHING PARTS)		

Fig. & Index No.	Tektronix Part No.	Serial/Ass Effective	sembly No. Dscont	Qty	12345 Name & Description	Mfr. Code	Mfr. Part No.
1-				1	CIRCUIT BD ASSY:TIME/CM		
				1	(SEE A1 REPL)		
-84	200-1362-00			2	.COVER,CAM SW:30 ELEMENTS	80009	200-1362-00
-85	211-0244-00	B010100	B021199	6	.(ATTACHING PARTS)	TYNSES	211_0244_00
-03	211-0292-00		0021199	6	.SCR,ASSEM WSHR:4-40 X 0.312,PNH STL .SCR,ASSEM WSHR:4-40 X 0.29,PNH,BRS NI PL .NUT,PLAIN,HEX:4-40 X 0.188,BRS CD PL	78189	51-040445-01
-86	210-0406-00	DOLILOO		6	.NUT, PLAIN, HEX:4-40 X 0.188, BRS CD PL	73743	12161-50
					.(END ATTACHING PARTS)		
-87				1	.CKT BOARD ASSY:READOUT(SEE A3 REPL)		
-88	211-0678-00			4	.(ATTACHING PARTS) .SCR,ASSEM WSHR:4-40 X 0.281,PNH,STL,POZ	01536	ORDER BY DESCR
-89	211-0008-00			1	.SCREW, MACHINE: 4-40 X 0.25, PNH, STL		ORDER BY DESCR
					.(END ATTACHING PARTS)		
00	101 0004 00				CKT BOARD ASSEMBLY INCLUDES:CONTACT,ELEC:CKT BD SW,SPR,CU BESOCKET,PIN TERM:U/W 0.025 SQ PINTERMINAL,PIN:0.46 L X 0.025 SQ PH BRZ .HOLDER,TERMINAL:8 SQUARE PINS	00000	101 0004 00
-90 -91	131-0604-00 136-0263-04			9 3	CONTACT, ELECTOR BU SW, SPR, CU BE	22526	131-0604-00 75377-001
-92	131-0589-00			10	TERMINAL.PIN:0.46 L X 0.025 SO PH BRZ	22526	48283-029
-93	352-0274-00			1	.HOLDER, TERMINAL: 8 SQUARE PINS	80009	352-0274-00
-94	129-0570-00			1	.SPACER, POST: 0.976 L,4-40 INT EA END, BRS,	80009	129-0570-00
					.0.188 HEX .(ATTACHING PARTS)		
-95	211-0008-00			1	.SCREW,MACHINE:4-40 X 0.25,PNH,STL	93907	ORDER BY DESCR
00	211 0000 00			•	.(END ATTACHING PARTS)		
-96				1	.SWITCH, PUSH: (SEE S460 REPL)		
-97	361-0411-00			2	.SPACER, PUSH SW:0.109 L, BLUE POLYCARBONATE		361-0411-00
-98 -99	384-1417-00 131-0963-00			1 2	.EXTENSION SHAFT:10.275 L X 0.125 OD,AL .CONTACT,ELEC:GROUNDING,PH BRZ,W/BRACKET		384-1417-00 ORDER BY DESCR
55	263-1166-00			1	.SW CAM ACTR AS:TIME/CM		263-1166-00
					(ATTACHING PARTS)		
-100	211-0244-00		B021199	4	.SCR,ASSEM WSHR:4-40 X 0.312,PNH STL .SCR,ASSEM WSHR:4-40 X 0.29,PNH,BRS NI PL	TK0858	211-0244-00
	211-0292-00	R051500		4	.SCR,ASSEM WSHR:4-40 X 0.29,PNH,BRS NI PL .(END ATTACHING PARTS)	78189	51-040445-01
-101	210-0406-00			2	NUT, PLAIN, HEX:4-40 X 0.188, BRS CD PL	73743	12161-50
-102	214-1139-03			2	SPRING,FLAT:0.885 X 0.156 CU BE RED CLR	80009	214-1139-03
-103	214-1752-00			2	ROLLER, DETENT: 0.125 OD X 0.16, SST	80009	214-1752-00
-104	401-0180-00			1	BEARING,CAM SW:FR & REAR,O.80 & O.83 DIA (ATTACHING PARTS)	80009	401-0180-00
-105	354-0390-00			1	RING, RETAINING: BASIC EXT, U/O 0.375 DIA SFT	79136	5100-37-ZD
					(END ATTACHING PARTS)	,	3234 37 22
-106	384-0878-08			1	SHAFT,CAM SW:4.964 X 0.248 OUTER CNCTRC		384-0878-08
-107	105-0755-00 210-0406-00			1 4	ACTUATOR,CAM SW:TIME/CM,DRUM TYPE NUT,PLAIN,HEX:4-40 X 0.188,BRS CD PL		105-0755-00 12161-50
-108 -109	401-0178-04			1	BEARING,CAM SW:CENTER/REAR		401-0178-04
-110	351-0180-00			ī	.GUIDE, SLIDE SW: SWITCH ACTUATOR		351-0180-00
-111	131-0593-00			3	.TERMINAL,PIN:1.15 L X 0.025 SQ BRS TIN PL	22526	47334
-112	214-1136-00				.ACTUATOR,SL SW:VARIABLE CAL		214-1136-00
-113 -114	214-1190-00			1 1	.CPLG,SHAFT,RGD:0.125 OD TO 0.125 OD,AL .RESISTOR,VAR:(SEE R725 AND S725 REPL)	80009	214-1190-00
117				1	.(ATTACHING PARTS)		
-115	210-0583-00			1	.NUT,PLAIN,HEX:0.25-32 X 0.312,BRS CD PL		2X-20319-402
-116	210-0046-00			1	.WASHER,LOCK:0.261 ID,INTL,0.018 THK,STL	77900	1214-05-00-0541C
-117	407-0803-00			1	.(END ATTACHING PARTS) .BRACKET,ELEC SW:BRASS	90000	407-0803-00
-118	263-0016-01			1	.SWITCH PB ASSY:3 LCH & 1 CANC,7.5MM,5 CONT		263-0016-01
-119	343-0495-04			5	.CLIP,SWITCH:FRONT,7.5MM X 4 UNIT		343-0495-04
					.(ATTACHING PARTS)		
-120	210-3033-00			4	.EYELET, METALLIC: 0.059 OD X 0.156 L, BRS	07707	SE-25
-121	343-0499-04	B010100	B021544	1	.(END ATTACHING PARTS) .CLIP,SWITCH:REAR,7.5MM X 4 UNIT	80009	343-0499-04
***	343-0499-13		BULLUTT	1	.CLIP,SWITCH:7.5MM X 4 UNIT		343-0499-13
					.(ATTACHING PARTS)		
-122	210-3033-00			4	.EYELET, METALLIC: 0.059 OD X 0.156 L, BRS	07707	SE-25
-123	136-0252-07	R010100	B082099	4	.(END ATTACHING PARTS) .SOCKET,PIN CONN:W/O DIMPLE	22526	75060-012
-143	136-0252-07		レいしというう	2	SOCKET, PIN CONN: W/O DIMPLE		75060-012 75060-012
-124	352-0196-00	- · · · <b></b> -		2	.HOLDER, RESISTOR: PANEL MOUNT, DELRIN		352-0196-00
-125				1	.CKT BOARD ASSY: INTERFACE(SEE A1 REPL)		
					.(ATTACHING PARTS)		

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Fig. & Index No.	Tektronix Part No.	Serial/Ass Effective		Qty	12345 Name & Description	Mfr. Code	Mfr. Part No.
1-126	211-0008-00	B010100	B032099	6	.SCREW,MACHINE:4-40 X 0.25,PNH,STL	93907	ORDER BY DESCR
	211-0292-00	B032100		6	.SCR,ASSEM WSHR:4-40 X 0.29,PNH,BRS NI PL .CKT BOARD ASSEMBLY INCLUDES:	78189	51-040445-01
-127	131-0608-00			26	TERMINAL, PIN:0.365 L X 0.025 BRZ GLD PL	22526	48283-036
-128	214-0579-00			6	TERM, TEST POINT: BRS CD PL		214-0579-00
-129	136-0514-00	B010100	B031764	3	SKT,PL-IN ELEK:MICROCIRCUIT,8 DIP	09922	DILB8P-108
-130	136-0263-04			15	SOCKET, PIN TERM: U/W 0.025 SQ PIN	22526	75377-001
-131	136-0634-00	B010100	B031764	1	SKT,PL-IN ELEK:MICROCIRCUIT,20 DIP	09922	DILB20P-108
	136-0752-00	B031765		1	SKT,PL-IN ELEK:MICROCIRCUIT,20 DIP		DILB20P-108
-132	136-0252-04			113	SOCKET, PIN TERM: U/W 0.016-0.018 DIA PINS	22526	75060-007
-133	131-0604-00			25	CONTACT,ELEC:CKT BD SW.SPR.CU BE		131-0604-00
-134	131-0566-00			5	BUS, CONDUCTOR: DUMMY RES, 0.094 OD X 0.225 L	24546	OMA 07
-135	131-1003-00			2	CONN, RCPT, ELEC: CKT BD MT, 3 PRONG		131-1003-00
-136	220-0547-01			6	NUT BLOCK:4-40 X 0.282,NI SIL NP		220-0547-01
					(ATTACHING PARTS)		
-137	211-0105-00			6	SCREW, MACHINE: 4-40 X 0.188, FLH, 100 DEG	TK0435	ORDER BY DESCR
					(END ATTACHING PARTS)		
-138	426-0505-11			1	FR SECT, PLUG-IN: TOP	80009	426-0505-11
-139	426-0499-11			1	FR SECT, PLUG-IN: BOTTOM	80009	426-0499-11
	198-3825-00			1	WIRE SET, ELEC:	80009	198-3825-00
-140	131-0707-00			21	.CONTACT, ELEC: 22-26 AWG, BRS, CU BE GLD PL	22526	47439-000
-141	352-0161-05			1	.HLDR,TERM CONN:3 WIRE,GREEN	80009	352-0161-05
-142	352-0168-00			2	.HLDR,TERM CONN:10 WIRE,BLACK	80009	352-0168-00
-143	175-0826-00			AR	.CABLE,SP,ELEC:3,26 AWG,STRD,PVC JKT,RBN	80009	175-0826-00
-144	175-0833-00			AR	.CABLE,SP,ELEC:10,26 AWG STRD,PVC JKT,RBN	08261	111-2699-970
	198-2478-00			1	WIRE SET, ELEC:	80009	198-2478-00
	131-0707-00			18	.CONTACT, ELEC: 22-26 AWG, BRS, CU BE GLD PL	22526	47439-000
-145	352-0169-00			1	.HLDR,TERM CONN:2 WIRE,BLACK	80009	352-0169-00
	352-0161-07			1	.HLDR,TERM CONN:3 WIRE,VIOLET	80009	352-0161-07
-146	352-0162-00			1	.HLDR,TERM CONN:4 WIRE,BLACK	80009	352-0162-00
	352-0162-04			1	.HLDR,TERM CONN:4 WIRE,YELLOW	80009	352-0162-04
-147	352-0163-06			1	.HLDR,TERM CONN:5 WIRE,BLUE	80009	352-0163-06
-148	175-0825-00			AR	.CABLE,SP,ELEC:2,26 AWG,STRD,PVC JKT,RBN	80009	175-0825-00
	175-0826-00			AR	.CABLE,SP,ELEC:3,26 AWG,STRD,PVC JKT,RBN	80009	175-0826-00
-149	175-0827-00			AR	.CABLE,SP,ELEC:4,26 AWG,STRD,PVC JKT,RBN	08261	111-2699-954
-150	175-0828-00			AR	.CABLE,SP,ELEC:5,26 AWG,STRD,PVC JKT,RBN	08261	111-2699-955
-151	175-5712-00			1	CABLE ASSY,RF:50 OHM COAX,3.0 L,9-3	80009	175-5712-00
					STANDARD ACCESSORIES		
	070-2317-00			1	MANUAL.TECH:OPERATORS	80009	070-2317-00
	070-2316-00			ī	MANUAL, TECH: INSTRUCTION		070-2316-00

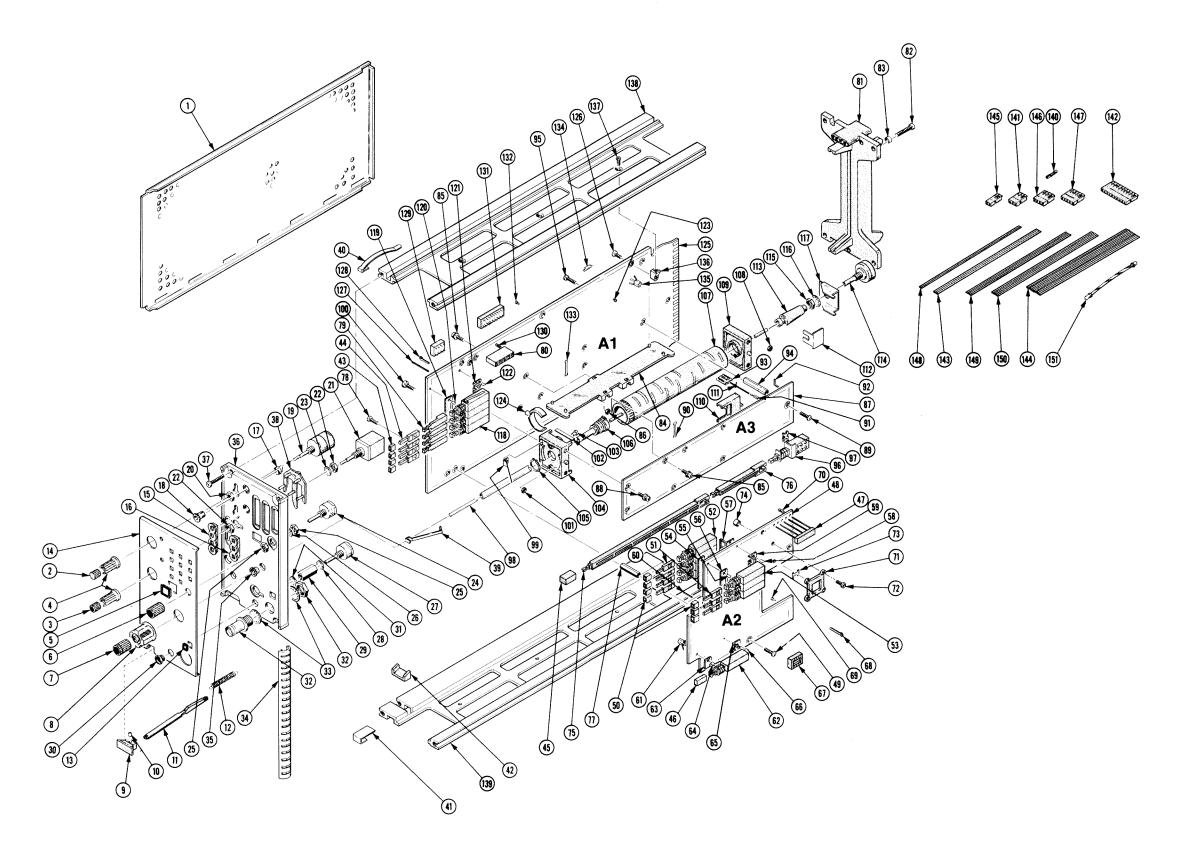


Fig. & Index No.	Tektronix Part No.	Serial, Eff	/Model No. Dscont	Qty	1 2 3 4 5	Name & Description	Mfr Code	Mfr Part Number
	070-2317-0 070-2316-0			1	MANUAL, TECH: OF		80009 80009	070-2317-00 070-2316-00



## MANUAL CHANGE INFORMATION

At Tektronix, we continually strive to keep up with latest electronic developments by adding circuit and component improvements to our instruments as soon as they are developed and tested.

Sometimes, due to printing and shipping requirements, we can't get these changes immediately into printed manuals. Hence, your manual may contain new change information on following pages.

A single change may affect several sections. Since the change information sheets are carried in the manual until all changes are permanently entered, some duplication may occur. If no such change pages appear following this page, your manual is correct as printed.

		- many



Product: .

## MANUAL CHANGE INFORMATION

Date: <u>5/6/88</u>

Change Reference: C110/0588 Rev 2

Manual Part No.:

see product

**Product Group:** 

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**DESCRIPTION** 

Effective for all serial numbers.

All 7000 Service manuals

Revised 9/26/88 Revised 10/13/88

## REPLACEABLE ELECTRICAL PARTS LIST CHANGES

The part number has changed for a transistor which may be used in your 7000-Series product. Part number 151-0220-00 has changed to 151-0220-07. Use the new 151-0220-07 part number when ordering a replacement for transistors listed as 151-0220-00 in your Replaceable Electrical Parts List.

Most berg sockets, part number 136-0252-07, have been removed from this 7000-Series instrument to facilitate assembly and improve reliability.





## MANUAL CHANGE INFORMATION

Date: 1/26/89 Change Reference: M68814 Rev

Manual Part No.: <u>070-2316-00</u>

Product: 7B10 Instruction

Product Group:

These changes are effective at serial number B035849.

Revised 2/17/89

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## REPLACEABLE ELECTRICAL PARTS **CHANGE**

**DESCRIPTION** 

**CHANGE TO:** 

Q376

151-0427-00 TRANSISTER: NPN,SI,TO-18