

Manual

SIGNAL GENERATOR SMS

302.4012.22 302.4012.24 302.4012.26





SIGNAL GENERATOR SMS 0.1/0.4 to 520 (1040) MHz



(IEC625EUE)

SMS

Data sheet 302 401 E - 2

CHARACTERISTICS

Summary

- General-purpose AM-FM synthesizer for 0.4 to 520 MHz (0.1 to 1040 MHz); low noise and excellent AM, FM and @M modulation characteristics.
- Compact, low-priced signal generator for use in development, production and servicing, can be tailored to customer requirements by adding options.
- Keyboard entry and LED display of frequency. modulation and level make it easy to operate.
- Numerical value and unit can be input in ordinary. notation; all the values can be varied in single steps or quasi-continuously with selectable step size.
- Wide output voltage range, exact level setting in dBm, dB(uV), uV or mV.
- Overvoltage protection and IEC-bus programmability (setting time 40 ms) are standard features.

Stable output signal of high accuracy

Frequency. The wide frequency range from 400 kHz (100 kHz with Model 24) to 520 MHz covers all the sound broadcasting ranges from medium wave to shortwave to VHF, as well as the frequencies of the main radiotelephony bands and radio services up to the UHF region. The range can be extended to 1040 MHz by means of the Frequency Range Extension Option SMS-B2 (see page 3).

The crystal-controlled frequency is read out on the display with a resolution of 100 Hz. The Reference Oscillator Option SMS-B1 (aging $< 1 \times 10^{-6}$ /year) further enhances the accuracy. A reference frequency input is provided on the rear panel of the signal generator (photograph lower righthand side). The Δf keys permit easy channel stepping with any desired step size (see also page 6). The output signal features low spurious deviation, only 3 Hz (CCITT) or 15 Hz (30 Hz to 20 kHz). The S/N ratio 20 kHz from the carrier is typically 120 dB at a test bandwidth of 1 Hz, and 145 dB at 1 MHz from the carrier.

Modulation. The SMS is suitable for all types of modulation: AM up to m = 95% and FM up to deviation = 125 kHz are possible with the aid of the internal modulation generator (400 or 1000 Hz) or an external signal. Modulation freguency and modulation depth or frequency deviation can be entered via a keyboard and are read out on a 3-digit display with 0.05/0.5% and 50 Hz/500 Hz/1 kHz resolution.

The maximum frequency deviation of 125 kHz is available over the entire frequency range. The high resolution of the frequency deviation of 50 Hz is helpful when testing transceivers. In addition to AM and FM, the SMS offers the following types of modulation:

- AM + FM together
- phase modulation (@M)
- frequency-shift keying for data transmission (FSK)
- external level control (ALC).

Modulation settings are preserved in the UNMOD, position and can be called up again at the push of a button.

The Signal Generator SMS being used together with the Vector Analyzer ZPV and the Process Controller PPC in a computer-controlled test assembly for s-parameter measurement.





Easy-to-service design of the Signal Generator SMS: plug-in circuit boards used throughout make for ready access.

The output level is adjustable from +13 to -137 dBm with a resolution of 0.1 dB, the error being typically 0.8 dB. Entry is in μ V, mV, dB(μ V) and dBm via keyboards. Its minimum output voltage of 0.03 µV makes the SMS also suitable for measurements on future, extremely sensitive receivers. Continuous level variation over 10 dB in 0.1-dB steps is indispensable for squelch measurements. The output level can be switched off by means of the RE-OFF button so calibration of measuring instruments is very convenient. RF leakage of the SMS is minimal, i.e. even receivers with a sensitivity of $0.2 \,\mu V$ (e.g. paging receivers) will not respond at a distance of 10 cm from the front panel.

> Internal 10-MHz reference frequency output (TTL level) or external reference frequency input (>0.5 V, sinewave or squarewave)

IEC-bus connector

2nd modulation input for ϕM (can be switched over to FM) or AM (DC-coupled)

Openings for relocation of the RF output and ext. modulation input or int. modulation output from the front panel to the rear panel



Low cost -- high versatility

The Signal Generator SMS offers cost-effective RF measurements in production and servicing of sound broadcasting receivers as well as for commercial receivers, such as used in aviation communications and radiotelephony, in telemetry and navigation. Thanks to its compact design the SMS is easily portable and can be installed in test vans. The IEC-bus programmability of the SMS (see page 7) with a setting time of only 40 ms makes it ideal for use in automatic test systems. Plug-in circuit boards and the designed-in signature analysis compatibility facilitate and speed up servicing (see photograph at left).

Options

Several options are available for the SMS to satisfy differing or special customer requirements at minimum cost. The SMS can on request be supplied with these options built in, or they can be retrofitted.

Temperature-controlled Reference Oscillator SMS-B1 improves the frequency stability of the signal generator. The temperature drift of $<\pm1 \times 10^{-6}$ /°C is reduced to $< +1 \times 10^{-7}$ over the entire temperature range. Crystal aging is less than 5×10^{-8} /month.

1.04-GHz Frequency Range Extension SMS-B2 doubles the SMS frequency range (to 1.04 GHz) while the full output level setting range is maintained. The harmonics and subharmonics (1/2f, 3/2f...) are typically 20 dB down. For applications up to 1000 MHz, Option SMS-B2 with the same characteristics but a different ordering number is available; see Specifications on page 8.



OPERATION

Easy to operate

Simple keyboard entry. The function keys and the associated displays for frequency, modulation and level are arranged in three sections on the front panel for useroriented operation. The parameters are entered in ordinary notation, first the numerical value and next the unit. A fourth, general keyboard is provided for the entry of the numerical value.

Example: For entry of a frequency of 360 MHz, a level of –17.9 dBm, and frequency modulation with 125 kHz frequency deviation and 400 Hz internal modulation frequency simply press the following keys:



Modulation setting storage. One AM and one FM setting at a time are stored in the signal generator. Thus only one key needs to be pressed to switch over between AM and FM or unmodulated and modulated operation.

Example: The frequency modulation setting is switched off by means of the key $\underbrace{\text{URMOD}}_{\text{KMOD}}$ and switched back on again by means of the key $\frac{-FM}{|KHZ|}$

Easy variation of all settings. Frequency, level and modulation can be varied by means of the keys $\hat{\square}$ and $\hat{\bigcup}$ which are associated with the various digits of the displays. The selected digit is varied in unit steps with automatic carry to the next digit either by one step per keystroke or (if the key is held down) continuously. Rapid coarse tuning in 10-MHz steps as well as fine tuning in 100-Hz steps is possible.



Keys associated with the various digits permit stepwise or continuous variation of numerical values entered (e.g. frequency as here displayed). Indication of illegal entries. Entries that exceed the given setting range are not considered by the SMS. The last value entered is still indicated. The OVERFLOW LED flashes in the case of a non-valid entry.

Frequency variation with channel stepping. The Δf kHz keys (see photograph below) permit channel stepping with any desired channel step size.

Example: For entry of a frequency step of +12.5 kHz press the following keys on the large, main keyboard:







Key for freely selectable frequency steps in positive or negative direction.

Continuous fine level adjustment. The keys () and () (0.1 dB) permit continuous electronic adjustment of the level in 0.1-dB steps over a range of 10 dB (indispensable for squeich measurements), the corrected value being read out on the level display. The state of the electronic level variation can be seen from an LED array (see photograph below).





The 0.1-dB keys are used for continuous electronic fine level adjustment. The state of the electronic level variation can be seen at a glance from the LED array.

Storage of complete device settings. The SMS can store three complete device settings (each setting including frequency, modulation and level).

Example: Storage of device setting at memory location 3:





IEC-bus programming

The Signal Generator SMS can also be put to use in computer-controlled test assemblies via the IEC-bus interface. Its extremely short setting time of only 40 ms makes it capable of high-speed computer-controlled frequency response measurements — even with high resolution. The control instructions are in accordance with IEC standard 625-1. Each instruction consists of a header, the numerical value and a comma as the delimiter. The numerical value is entered in unformatted form with or without sign and with or without decimal point (see table below).

Examples: Device setting

instructions Frequency: 122.19 MHz A122.19, Level: -23 dBm S-23, Modulation: AM, 30 % B30,

Programming

It is also possible to write several control instructions in one program line:

A122.19, S-23, B30,.

Programming example for Process Controller PPC from Rohde & Schwarz for level, modulation and frequency setting on the Signal Generator SMS (address 28).

 100
 IECOUT28, "S-3.7,"
 Level setting

 110
 IECOUT28, "H2.8, J,"
 Modulation setting

 120
 FOR F=100
 T0
 111.5
 STEP .5
 Frequency loop

 130
 IECOUT28, "A"+STR\$(F)+","
 Frequency setting
 Frequency setting

 140
 NEXT F
 IS0
 END

An LED (REMOTE) lights when the SMS is in the remote control state (REMS). Then all the operating controls, with the exception of the LOCAL key, are disabled. By pressing the LOCAL key the SMS can again be manually operated.

Table of setting instructions

Function	Header	Data	Delimiter
Frequency in MHz	A	Maximum of 8 digits with or without decimal point	, (comma)
Modulation			
AM, m in %	в	Maximum of 3 digits with	,
FM, deviation in kHz	н	or without decimal point	1
UNMOD.	С	None	
INT. 400 Hz	ł	None	
INT, 1 kHz	J	None	
EXT.	К	None	,
Level			
in dBm	S	A maximum of 31/2 digits	,
in dB(μV)	R	with or without decimal point, with negative sign or without	3
īn μV	Р	A maximum of 31/2 digits with	,
in mV	Q	or without decimal point	
Switchover to continuous fine		·	,
adjustment	Х		
RF OFF	Y	0	1
 RF ON	Y	1	,
Pause 15 ms	@	None	

PROGRAMMING, SPECIFICATIONS

Specifications Frequency Frequency range, Model 22: 0.4 to 520/1040 MHz 1 Option SMS-B2 Model 24 0.1 to 520/1040 MHz Specifications the additional second requency readout Resolution Frequency error will ce oscillator Standard < ≟1 × 10 ∜month $\begin{array}{l} \text{Option SMS-B1} \\ \times \pm 5 \times 10^{-9} \text{mor} \\ \prec \pm 1 \times 10^{-7} \end{array}$ month Aging emperature effect <+1 x 10 °/°C 15 to 45 °C) Warm-up period Output/input for inter ternal reference frequency, 10 MH (single connector) Output TTI level >0.5 V (sinewave) or TTL level Spectral purity Harmonics down ≥30 dBc1 Non-network responses Spurious deviation, ms Sourious deviation, ms Sourious deviation, ms Non-harmonic spurious down ≥60 dBc') (≥5 kHz from carrier): \leq 4 Hz inted in accordance with CCITT) 0.03 to 20 kHz ≤ 16 Hz Sperious AM, rms 0.0316/20 kHz, down≥70 dBc3) Single-sideband phase no (see also diagram below) typ: down 120 dBc1) (test bandwidth 1 Hz, 20 KHz from carrier) Single-sideband broadband typ: down 145 dBc1) (test bandwidth 1 Hz, 1 MHz from carrier) 4 -- 100 dBc/Hz 110 -120 Ф -130 -140 ភ្ល៊ ~15f 10³ kHz 10⁴ 10210 Offset from carrier Typical single-sideband phase noise of Signal Generator SMS (f_{earner} – 360 MHz) Level Output level with CW and = 137 to + 13 dBm (0.03 μV to 1 V) into 50 Ω - 137 to = 7 dBm (0.03 μV to 0.5 V) into 50 Ω 3½ - digit LED display in μV, mV, dB(μV), FM with AM Readout dBm Resolution 0.1 dB 0 to - 10 dB with 0.1 dB resoluti without interrupting RF output Error of level readout ≤ ±1 dB + frequency response² Frequency response of ou put level flat ±0.5 dB (8 to 520 MHz) nat ±1 dB (<8 MHz) ≥80 dB Level reduction with BE OFE Output Characteristic impedance N(f) connector 50 Ω VSWR ≤ 1.2 (level ≤ -3 dBm)?). protects the RF output of the SMS from externally applied RF (1 to 1000 MHz) Overvoltage protection or DC voltage 30 W Max input power Max input DC voltage 35 V) dBc = relative level referred to carrier amplitude. With fine level adjustment = 0 dB

SPECIFICATIONS

Page 7 cont d	EC-bus control	EEE 488 and IEC 525-1 for control of all	(
Modulation Amplitude modulation	operating modes and for data	transfer in listener operation AH-1 Acceptor handshake 12 Listener RL 1 Benjote/Local	
Frequency range AM EXT 20 Hz to 20 HFz (6 to 520 MHz) 20 Hz to 5 KHz (0.4 to 8 MHz) AM INT 200 Hz to 5 KHz (0.4 to 8 MHz) Addulation depth m 0 to 95 % Readoul 3 digit LED display Readoul 9 55 %	Selfing time	DC 1 Device clear: 40 ms for all functions (typ. frequency error after 40 ms: <100 Hz)	
$\begin{array}{ll} m=10\ \text{io}\ 95\%;\ 9.5\%\\ \text{Readout error}\\ m<30\%;\ 20\ \text{Hz}\ \text{to}\ 20\ \text{KHz};\ 8\ \text{to}\ 520\ \text{MHz};\ \leq 4\%\ \text{of reading}\ +1\%')\\ < 8\ \text{MHz};\ \leq 7\%\ \text{of reading}\ +1\%')\\ \text{Envelope distortion}\\ (f_{max}=0.471\ \text{KHz})\\ m=80\%;\ \leq 1.5\%\\ m=90\%;\ \leq 3\%\\ \text{Input voltage requirement};\ 1\ \text{Vinto}\ 500\ \Omega\ (V_{ma}). \end{array}$	Specifications differing from A	0/4 70/520 MHz 0:1 to 520 MHz /odel 22	·
Spurious gM (peak value at 30% AM) <0.1 rad Frequency modulation Frequency range FM EXT 20 Hz to 20 kHz (response down 3 dB typ <5Hz/>30 kHz)	Frequency range AM EXT. FM deviation	20 Hz to 2 KHz (0.1 to 8 MHz) ≧t _{carre} – 100 KHz (125 KHz; max)	
FM INT. 400 Hz and 1: KHz, +3% Frequency deviation. 0 to 125 kHz Readout. 3 digit LED display Resolution. 0 to 9.95 kHz. 50 Hz 100 to 99.5 kHz 500 Hz 100 to 125 kHz 1 kHz	General data AF leakage	conforms to VDE 0871 and ML STD 461 A	
Error (20 Hz to 20 kHz) ≤5% of reading FM distortion (fmod 0.47 kHz) deviation 75 kHz) Input voltage requirement 1 V into 600 Ω (V _{mo}) Spurious AM (20 kHz devia	Shock and vibration	RE 02 concerning radio interference and interference on the connecting cables. The SMS also complies with the requirements of VDE 0875 (limit values of radio inter- ference grade K)	<u> </u>
tion, FM-INT.) <0.1% Output voltage with internal AM and FM IV into 600 Q. (V _{ma}) Additional modulation input at the rear of the SMS for phase modulation (pM), 20 Hz to 8 kHz, FM, 20 Hz to 20 kHz, toonsection can be internally obanged) or ALC (AM), DC to 20 kHz	Sonock and violation Tresistance	shock proof in accordance with DIN 40046, Part 7 (30 g; 11 ms); vibration-tested in accordance with DIN 40046, Part 8 (11 to 55 Hz, 2 g); corresponds to IEC Publications 68-2-27	
Required input voltage into 600 Ω ALC (AM), DC coupled 0 to ±2,83 V for 0 to ±40 dB of V for 5 rad FM (V _{res}) 1. V for 5 rad FM (V _{res}) 1. V for 700 kHz deviation	Bated temperature range Shelt temperature range AC supply Overall dimensions (W × H × D); weight	and 68-2-6 >5 to +45 °C -40 to +70 °C 115/125/220/235 V ± 10 % 47 to 420 Hz (55 VA) 347 mm × 198 mm × 370 mm 14.6 kg	
Options: 1.04-GHz Frequency Range Extension Option SMS-82			
Frequency range 0.110.4 to 1040 MHz Performance specifications of the SMS fifted with Option SMS-B2 over the frequency range 520 to 1040 MHz Resolution of the frequency indication 2000 Hz Harmonics and sub-	Ordering information	 Signal Generator SMS [0.4 to 520 MHz] 302 4012 22 Signal Generator SMS (0.1 to 520 MHz). 	
harmonics typ.:20 dBc1 down. (subharmonics ?/af, ?/af.:) Non-harmonic responses >200 kHz from carrier. >5 kHz from carrier.	Accessories supplied Power cord, manual Options Reference Oscillator SMS-B1	302 8018 22 302 8018 02	
Spurious deviation, rms 0.3 to 3 kHz (weighted in accordance with CCITT) 0.03 to 29 kHz 3 kHz ≤ 32 Hz Single-sideband phase noise 20 kHz from carrier 20 kHz from carrier down typ: 115 dBc;1 (test bandwidth 1 Hz)	1 04-GH2 Frequency Range Extension SMS-B2 1 0. GH2 Frequency Range Extension SMS-B2 Recommended extras 19° Adapter, SMS-21	335.0016.02 335.0016.04 302.8860.00	C
20 kHz from carrier down typ. 115 dBc ⁺) (test bandwidth 1 H2). 1 MHz from carrier down typ. 140 dBc ⁺) (test bandwidth 1 H2). Error of output level $\leq \pm 1$ dB + frequency response ²) Frequency response of level [Hat ± 1 dB Error of modulation-depth in- dication with AM (m < 90%). $\leq 7\% \pm 1\%^2$) of reading Ervelope distortion			
Criverope distortion m = 80 %), ≥ 5 % Other specifications same as for basic unit 1-GHz Frequency Range Extension Option SMS-B2 Frequency range			
Option SMS-B2 Reference Oscillator Option SMS-B1 see under "Frequency" specifications	 dBc = relative level referred With the level adjustment 		

GmbH & Co. KG · D-8000 München 80 · Mühidorfstr. 15 · Tel. (089) 4129-1 Int. +49894129-1 · Telex 523703 Printed in the Fed. Rep. of Germany · Subject to change · Data without tolerances: order of magnitude only

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Model 26 of the SMS has modulation characteristics which make it suitable for stereo transmissions. Compared to model 22 the follwoing data are new or have been changed.

Frequency range

Distortion with stereo modulation (10.7; 87 to 108 MHz) at f_{mod} 50 Hz to 10 kHz and 40 kHz deviation

Stereo crosstalk (10.7; 87 to 108 MHz) 50 Hz; 1 kHz; 10 kHz

S/N ratio

(RMS, wideband weighting filter 31.5 Hz to 16 kHz) referred to 40 kHz deviation 0.1 to 520 MHz

< 0.4%; typ. 0.2%

typ. 40; 45; 45 dB

typ. 65 dB

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Preparation for Use and Operating Instructions

2.1 Legend for Front- and Rear-panel Views

2.

Ref. No.	Labelling	Function
1	MHz	Key for entry of frequency after frequency has been entered from keyboard 15.
3	MHz kHz	8-digit display for indication of selected carrier frequency.
2		Two rows of six keys each for carrier frequency variation. Each pair of keys is associated with the digit immediately above it. If a key is pressed once, the corresponding digit is increased or de- creased one step. If the keys are kept depressed, the frequency is varied con- tinuously. With the upper keys, the digits are increased and with the lower ones decreased.
4	% kHz	2 LEDs for indication of the unit of the modulation readout.
5		3-digit display for readout of the modu- lation-depth or frequency-deviation.
<u>6</u>		Two rows of two keys each for modulation variation. Each pair of keys is associa- ted with the digit immediately above it. If a key is pressed once, the correspond- ing digit is increased or decreased one step. If the keys are kept depressed, the modulation is varied continuously. With the upper keys, the digits are increased and with the lower ones decreased.
Z	µV mV dBµV dBm	4 LEDs for indication of the unit of the output-level readout.
<u>8</u>		4-digit display for readout of the output level.
2	O VAR.	Row of LEDs for indication of fine level adjustment over a range of 0 to -10 dB.

Ref. No.	Labelling	Function
<u>10</u>	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	Two rows of 3 keys each for output-level variation. The keys permit output-level variation in steps of 10, 1 and 0.1 dB. With the upper keys, the level is increased and with the lower ones decreased. In mode of indication dB μ V or dBm, each pair of keys is associated with the digit above it. If a key is pressed once, the corresponding digit is increased or decreased one step. If the keys are kept depressed, the output level is varied continuously.
<u>11</u>	OVERFLOW	LED for signalling illegal entry.
<u>12</u>	REMOTE	LED for indication of remote-control operation with IEC-bus programming (if this option is fitted).
<u>13</u>	LOCAL	Key for switching from IEC-bus programmed operation.
<u>14</u>	µV mV dBµV dBm	4 keys for entry of level. They define the desired unit of the level entered (keyboard <u>15</u>).
<u>15</u>		Data-entry keyboard for carrier frequency (in conjunction with key <u>1</u>), modulation depth (in conjunction with key <u>26</u>) fre- quency deviation (in conjunction with key <u>21</u>), RF level (in conjunction with keys <u>14</u>) and channel stepping (in conjunction with pair of keys <u>27</u>).
<u>16</u>	STO RCL	Pair of keys for storing (STO) and re- calling (RCL) 3 complete instrument settings. Subsequently enter address 1 to 3.
<u>17</u>	POWER (OFF)	Power switch.
<u>18</u>	CE	Key for erasing the numerical values entered (e.g. in the case of illegal entry) and for switching off overflow indication.
<u>19</u>	RF OFF	Key for switching off the RF level. In off-condition, the red LED lights. It also lights if the overload protection (option) responds on account of an ex- cessive external signal at the RF output.

Ref. No.	Labelling	Function
<u>20</u>	$0.1 \ \mu V - 1 \ V$ $50 \ \Omega \qquad \bigoplus$	RF output N female connector.
<u>21</u>	FM kHz	Key for entry of frequency deviation after the numerical value has been entered from keyboard <u>15</u> .
<u>22</u>	EXT.	Key for switching over to external modu- lation. Entry from keyboard.
<u>23</u>	50 Hz - 20 kHz	Modulation socket Internal modulation output. External modulation input.
<u>24</u>	400 Hz 1 kHz	Keys for switching over to internal modulation with 400-Hz or 1-kHz sinewave signal.
<u>25</u>	UNMOD.	Key for switching off selected type of modulation.
<u>26</u>	AM %	Key for entry of modulation depth after the numerical value has been entered from the keyboard <u>15</u> .
27.	<u>∆f</u> kHz + -	Pair of keys for entry of frequency steps after the numerical value has been entered from the keyboard <u>15</u> . Whenever a key is pressed, the frequency step entered is repeated. The - key decreases and the + key increases the frequency by one step.
<u>28</u>	REMOTE CONTROL	Connector for IEC-bus programming (option),
<u>29</u>	REF. FREQ. 10 MHz	Socket for output of internal crystal reference frequency. Can be internally changed to allow the input of an external reference frequency.
<u>30</u>	φ M EXT. (FM) ALC	Socket for external modulation input, AM (DC to 20 kHz) or' M (FM) depending on the type of modulation selected by means of the keys <u>20</u> and <u>26</u> .
<u>31</u>		Air filter.
<u>32</u>		Opening, provided for relocation of the RF output to the rear panel.
33		AC supply receptacle.

Ref. No.	Labelling	Function
<u>34</u>	T 1.25 N 220 V~ 235 V~ 0 115 V~ 125 V~ T 2.50	Fuse holder and voltage selector.
35		Opening provided for relocation of the modulation input from the front panel to the rear panel.

2.2 Preparation for Use

The Signal Generator SMS is suitable for operation from AC supply voltages of 115 V, 125 V, 220 V and 235 V. It is factory-adjusted for an operating voltage of 220 V.

To adapt the instrument to other AC supply voltages, unscrew the fuse from the voltage selector <u>34</u> (Fig. 14), remove the cover plate and replace it such that the fuse can be inserted at the point which is marked with the desired voltage. The instrument is now ready for operation from the new AC supply voltage. The fuses for all the AC supply voltages listed above are contained in the voltage selector.

For 220 V and 235 V, fuse T 1.25 B (1.25 A) is inserted and for 115 V and 125 V, fuse T 2.5 D (2.5 A).

The Signal Generator SMS has the width of 3/4 of a 19" set. It is also suitable for mounting in 19" racks. For this purpose, rack adapters are available (see recommended extras). After removal of the screws on both sides, lift off the two covers and take off the side strips and the carrying handle. The threaded holes for mounting the rack adapters are provided on the set. The necessary openings for relocating the inputs and outputs from the front panel to the rear panel are also already provided on the rear panel. When rerouting the RF cable, make sure that the bending radius is not less than 10 mm and avoid sharp bends altogether. The cable must not be shortened.

2.3 Operating Instructions

Manual setting and operation of the set is accomplished exclusively via keys. The data-entry keys and digital displays form four neatly arranged groups. The first group, on the far left, comprises all keys and the display for entry and readout of the carrier frequency. The second group, to the right of the first, comprises the keys for modulation entry, the display for the modulation and the external input. Next is the group for entry and display of the RF level with the RF output. The fourth group, on the far right, constitutes a common data-entry keyboard for all three parameters. It also comprises the LEDs for REMOTE, LOCAL and OVERFLOW indication as well as the power switch. Irrespective of whether carrier frequency, modulation or RF level is to be entered always enter first the numerical value from keyboard <u>15</u> (Fig. 13). Subsequently, the desired unit is selected by pressing one of the keys in the other three groups controlling the functions of the set.

Illegal entries are rejected. If, for example, a frequency outside of the frequency range of the set is entered, the LED OVERFLOW <u>11</u> starts to flash. The same is true for the modulation and the level.

The flashing LED OVERFLOW <u>11</u> signals that the last entry has been rejected. If then another legal value or instruction is entered, the LED goes off. It also goes off if the key CE <u>18</u> is pressed.

If a wrong numerical value has been entered, the complete entry can be cleared by means of the key CE 18. Now the correct value can be entered.

2.3.1 Switch-on State

When switching on the set by means of the power switch <u>16</u> (Fig. 12), the set assumes a defined initial state. The frequency and modulation readouts are 0. The level display reads +1000. No frequency setting occurs and the output attenuator is set to maximum attenuation.

2.3.2 Frequency Setting

2.3.2.1 Entry .

The frequency is entered by entering the desired numerical value of the frequency in MHz from the keyboard 15 (Fig. 13). Then press the key MHz 1 to define that the numerical value entered is a frequency. The frequency is now immediately set. Zeroes at the end of the numerical value entered need not be keyed in after the decimal point. Entries, such as 423.2 MHz or 423 MHz are possible.

On first-time actuation of the power switch, the output level must be entered. Otherwise, no output level is present.

2.3.2.2 Frequency Variation

The frequency entered from keyboard <u>15</u> (Fig. 13) can be varied either in decade steps or in freely selectable channel steps. Each digit of the frequency display with the exception of the hundreds place is associated with a key pair <u>3</u>. By pressing a key, the corresponding digit is increased or decreased one step. When the key is kept depressed, these steps are repeated continuously, slowly at first and automatically speeding up after three steps.

Any desired channel step can be set by entering the respective numerical value in kHz from keyboard <u>15</u> and subsequently pressing one of the two keys Δf kHz <u>27</u>. The selected channel step is stored. By pressing key + or - <u>27</u>, the frequency can be increased or decreased by the stored value as often as desired. All entries from 0.1 kHz up to the maximum step over the entire frequency range are permissible. The entry must, however, be in kHz.

If the variation by decade or channel stepping exceeds the limits of the frequency range, the LED OVERFLOW <u>11</u> starts to flash, signalling that the last instruction cannot be executed. At 515 MHz, for example, a step of +10 MHz is not possible, but the LED goes off if the frequency is varied by only +1 MHz or the key CE <u>18</u> is pressed.

2.3.2.3 Frequency Setting with Option SMS-B2 1.04-GHz Frequency-range Extension

If the Signal Generator SMS is fitted with the option for extending the frequency range to 1040 MHz, proceed in the same way as above for frequency entry and variation. The range limit at which the LED OVERFLOW <u>11</u> (Fig. 13) starts to flash is automatically extended to 1040 MHz. The resolution of the readout is 200 Hz in the frequency range from 520 to 1040 MHz. The frequency to be set is rounded off to the next 200-Hz value with decade variation, frequency entry from the keyboard or variation by channel steps.

2.3.2.4 Reference Frequency

The Signal Generator SMS contains its own 10-MHz crystal reference oscillator from which the output frequency is derived by a synthesizer circuit. This reference frequency is available at the BNC socket REF. FREQ. 29 on the rear panel of the set with TTL level for external control purposes. The SMS may be switched over internally for operation from an external reference frequency of 10 MHz, for example, from another instrument or a central standard frequency. In this case, this same BNC socket 29 is used as input.

A sinewave signal of 10 MHz with 0.5-V amplitude is sufficient. TTL level can, however, also be applied.

When the set is supplied from the factory, the BNC socket $\underline{29}$ is connected as output. To use it as reference input, disconnect links $\mathrm{ER1}^{+)}$, $\mathrm{ER4}$ and $\mathrm{ER5}$ on the reference circuit board 302.6215 and connect link $\mathrm{ER3}$ (see Fig. 1). To do this, remove the screws on both sides and take off the lower cover. Subsequently open the cover of the large shielding can. The location of the circuit board 302.6215 is marked on the cover. After pulling out the circuit board, the contact pins to be re-connected at the lower righthand corner become accessible.



Fig. 1a) Position of link for reference frequency output.



Fig. 1b) Position of link for reference frequency input.

' (when option B1 is built in, ER1 is invisible and cannot be connected).

2.3.3.1 Entry

The available RF carrier can be amplitude- or frequency modulated from the front panel.

For setting the modulation, the desired numerical value of modulation depth or frequency deviation is entered in % or kHz, respectively, from the keyboard 15 (Fig. 13). Then pressing the key AM % 26 or FM kHz 21 defines this numerical value as a modulation parameter. As a result, the modulation is immediately set. Zeroes at the end of the numerical value to be entered need not be keyed in after the decimal point. Entries such as 55% or 2 kHz are possible. The readout is always in three digits. The possible resolution of the modulation setting and readout is, therefore, dependent on the value entered.

With AM it is:

for m	=	0	to	9•95%				0.05%
m	=	10	to	99%				0.5%
With F	М:							
for de	viat	tior	1 =	0 to	9•95	kHz		0.05 kHz
				10 to	99.5	kHz		0.5 kHz

100 to 125 kHz

If more finely graduated values are entered, the readout is rounded off to the next suitable value.

1 kHz

The modulation is switched off by pressing the key UNMOD. <u>25</u>. The entered value is stored. If now the key FM kHz <u>21</u> is pressed, the same modulation setting is obtained as before.

2.3.3.2 Variation of Modulation

The modulation entered from the keyboard <u>15</u> (Fig. 13) can be varied by pairs of keys associated with each digit of the readout with the exception of the first one. By pressing a key, the corresponding digit of the modulation readout is increased or decreased by one. When keeping the key depressed, the variation is continuous as for the carrier frequency.

When advancing from 9.99 to 10.0, the decimal point is automatically shifted. The variation keys remain associated with the same digits, i.e. the size of

the variation steps is at the same time switched over. If data entry or variation reaches the setting range limits, the lamp OVERFLOW <u>11</u> begins to flash.

2.3.3.3 Modulation Sources

When selecting a modulation mode, INT. 1 kHz modulation is automatically switched on. The RF carrier is modulated with 1 kHz by means of the internal modulation generator. The modulation signal is at the same time available at the BNC socket 23. The output voltage is 1 $V_{\rm rms}$. The pushbutton INT. 400 Hz 24 permits the internal modulation generator to be switched over to 400 Hz.

If the frequency required for INT modulation differs from the standard nominal frequency of 400 Hz, it can be changed by replacing a resistor on the circuit board modulation control 302.7011. To do so, remove the upper cover after loosening the screws at both sides. The circuit board can then be withdrawn from the cassette. The resistor R6 to be replaced is mounted on soldering pins. The following resistor is required for 300 Hz: 113 kn $\pm 1\%$ 0.35 W (Order No. RL 082.2248). After it has been replaced, make fine adjustment of the modulation frequency by means of the potentiometer R2 (see 5.3.11).

For external modulation pushbutton EXT. <u>22</u> must be pressed and a signal in the modulation frequency range from 50 Hz to 20 kHz applied to BNC socket <u>23</u>. In the case of sinewave signals, the input voltage must be 1 $V_{\rm rms}$. Departures from this voltage bring about corresponding changes in the modulation selected.

The second modulation input on the rear panel permits simultaneous use of two different types of modulation. The function of this input is controlled by the settin on the front panel of the set. If FM has been selected by means of the front-panel keys, AM is possible at the same time via the input on the rear panel, which is then DC coupled. Thus it can also be used for external level control and as ALC input.

The input voltage requirement is between 0 and 2.83 V. 0 V corresponds to the full carrier level. At +1.41 V, the level is attenuated by 6 dB. Maximum attenuation of the RF signal is obtained at +2.83 V. An rms voltage of 1 V superimposed on a DC voltage of 1.41 V is required for 100% AM.

If AM has been selected by means of the front-panel keys, ρM or FM is possible at the same time via the input on the rear panel. The set is factory-adjusted for phase modulation via the modulation input on the rear panel. It can be readily changed over to FM by reconnecting a link. To do this, dismount the circuit board modulation control 302.7011 (as described above) and reconnect the link located approximately in the centre of the circuit board as shown below.



Fig. 2a) Position of link for phase modulation via the MODULATION input on the rear panel.



via the MODULATION input on the rear panel.

The rms input voltage requirement is 1 V for a phase deviation of 5 rad or 100 kHz frequency deviation.

Modulation via the socket on the rear panel is also switched off by pressing the key UNMOD. <u>25</u> on the front panel.

2.3.4 Setting the Output Level

2.3.4.1 Entry

For setting the output level, enter the desired numerical value from the keyboard 15 (Fig. 13). The numerical value entered is defined as level by pressing one of the four keys 14μ V, mV, dB μ V or dBm. At the same time, the unit of the numerical value entered is defined. In the case of negative dBm or dB μ V enter a - sign ahead of the numerical value. It is not necessary to fill in the zeroes at the end of the numerical value following the decimal point.

Entries such as

 $80 \ \mu V$, $-3 \ dBm$ or $0.7 \ mV$

are permissible. The unit chosen is indicated by an LED which lights up. The permissible ranges for level entry in the UNMOD or FM modes are as follows:

-137 dBm to +13 dBm 0.03 μ V to 1000 mV -30 dB μ V to +120 dB μ V

In the AM mode, the following ranges apply:

-137 dBm to +7 dBm 0.03 µV to 500 mV -30 dBµV to +114 dBµV.

2.3.4.2 Variation of the Output Level

The output level entered from the keyboard <u>15</u> (Fig. 13) can be varied by means of pairs of keys in 10-dB, 1-dB and 0.1-dB steps. By pressing a key, the level is raised or lowered accordingly. If the key is kept depressed, the variation is continuous, slowly at first and speeding up automatically after three steps.

If variation reaches the limits of the setting range, the lamp OVERFLOW $\underline{11}$ starts to flash.

The pair of keys with the engraving 0.1 dB permits continuous fine level adjustment without interruption of the RF level. If a level entered from the keyboard <u>15</u> is reduced by means of the lower 0.1-dB key <u>10</u>, this variation is accomplished by electronic control. The entire setting range is -10 dB. The approximate value of the actual attenuation is indicated by the row of LEDs <u>9</u>. The level read out by the display <u>8</u> is, however, always correct and is also changed during fine adjustment of the level. The indication by means of the row of LEDs is merely for information about the setting range and need not be taken into consideration as a correction. The level can be freely increased or decreased within this setting range from 0 to -10 dB. If the limits of the setting range are exceeded, the RF attenuator is switched over which interrupts the RF level for a short time. The indication of the row of LEDs is reset. Variation by -10 dB is again possible without interrupting the RF level.

If the unit dBm or dBµV has been selected for the level entered, the level is varied by one digit in the place of the digital readout corresponding to the variation key <u>10</u> depressed. When selecting µV or mV, the 0.1-dB, 1-dB or 10-dB level steps are set after automatic conversion into the corresponding voltage steps and the new voltage is immediately read out. The unit of the readout is automatically switched from mV to µV or from µV to mV if the output level is taken below 0.1 mV or above 1000 µV.

2.3.4.3 Units of the Level Readout

All output level entries and readouts of the set are based on the voltage or power into a resistive termination of 50 Ω .

The actual output voltage is always indicated. The EMF is twice the value. The unit dBµV as a logarithmic measure refers to the output voltage above 1 μ V. It can be calculated from the output voltage as follows:

$$V_{(dB\mu V)} = 20 \text{ lg} \frac{V_{out}(\mu V)}{1 \mu V}$$

Hence, 1 μV output voltage corresponds exactly to 0 dBµV. Voltages above 1 μV yield positive values and voltages below 1 μV negative values in dBµV.

The unit dBm defines the output power which would flow into a resistive termination as a logarithmic measure above 1 mW.

It is calculated from the power according to the following expression:

$$P_{(dBm)} = 10 \ lg \ \frac{P(mW)}{1 \ mW}$$

or from the voltage:

 $P_{(dBm)} = 10 \ lg \ \frac{V_{out}^2 \ (V) \ 20}{1 \ mW}$

In the case of a mismatch of the connected test item or load, the delivered power is reduced by reflection. Thus the output is given by the following equation as a function of the VSWR (s):

$$P(\text{load}) = P(\text{reading}) \frac{4 \text{ s}}{(1 + \text{s})^2}$$

2.3.4.4 Level with AM

With amplitude modulation, the maximum available output level is reduced by 6 dB. When entering the level or varying the output level, the LED OVERFLOW <u>11</u> (Fig. 13) starts to flash when 500 mV or 7 dBm or 114 dB μ V are exceeded. Level setting above this limit is not possible. If a higher level is set in unmodulated operation and then amplitude modulation switched on, the LED OVERFLOW <u>11</u> starts to flash and AM is rejected.

For level settings below this limit, the level reduction is irrelevant. Entry and readout of the level with AM are for the carrier mean value.

2.3.4.5 Level Switch-off

The RF level can be switched off with the key RF OFF <u>19</u> (Fig. 13) without affecting any of the settings and readouts of frequency, modulation or level. When the RF level is switched off, the red LED lights. By pressing again key <u>19</u>, the RF level is switched back on.

2.3.5 Storing Instrument Settings

The two keys STO (= store) and RCL (= recall) <u>16</u> (Fig. 13) can be used for storing three complete settings of the signal generator and for recalling the settings whenever required with a simple keyboard entry.

STO 1, STO 2 or STO 3 causes the carrier frequency, channel step size, modulation type, deviation or modulation depth and output level to be stored. The corresponding command RCL 1, RCL 2 or RCL 3 is used to restore this combination of settings as often as required. When the equipment power switch is switched off, the stored information is erased.

2.3.6 Overload Protection

The overload protection cuts off the ouput whenever an excessive level is applied to the output. This condition is signalled by the lighting of the red LED in the RF OFF key. When the signal responsible for the response of the cutout circuit is removed, the output signal is automatically switched on again.

The response of the overload protection in remote-control operation causes the command SRW (SERVICE REQUEST) to be issued to the controller.

It should be taken into consideration in programming that the Signal Generator SMS only functions as a listener and therefore cannot reply to serial or parallel polls.

2.3.7 IEC-bus

The Signal Generator SMS can be remote controlled. The setting instructions are then transmitted via an interface which complies with the standards IEC 625-1 (formerly IEC 66.22), IEEE 488 1975 and DIN IEC 66.22. Data transfer takes place over a byte-serial bus system.

Connection is made at the rear of the instrument using REMOTE CONTROL socket <u>28</u> (Fig. 14). Fig. 3 shows the contact occupancy.



Fig. 3 Contact occupancy of REMOTE CONTROL socket

The American national standard 488-1975 specifies a different socket than the international IEC standard. The SMS is fitted with the most frequently used socket, viz. the one referred to in the 488-1975 standard. Connection to equipment fitted with a 25-pole socket according to the IEC standard is readily possible with the aid of an adapter. Control functions and data transfer are identical.

The standard interface features three groups of bus lines:

Data bus - 8 lines identified as DI/0 1 to DI/0 8.
 Data transfer is bit-parallel and byte-serial, the characters being transferred in the ISO 7-bit or ASCII code.

DI/O 1 is the least significant bit and DI/O 8 the most significant.

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2. Control bus of 5 lines

This is used for the transfer of control functions.

ATN (attenuation) is active low during the transfer of an address to the connected equipment.

REN (remote enable) is used for switching the instruments to the remote-control mode.

SRQ (service request). By activating this line, a connected instrument can request the intervention of the controller.

IFC (interface clear) is activated to bring connected instruments into a defined initial condition.

EOI (end or identify). This signal can be used to identify the end of a polls data transmission and is also used for following a service request. The SMS does not process this signal.

3. Handshake bus of 3 lines

This is used for controlling the data transfer sequence.

NRFD (not ready for data). Active low on this line indicates to the controller that one of the connected instruments is not ready for data transfer.

DVD (data valid) is activated by the controller shortly after a new data byte has been applied to the data bus.

NDAC (not data accepted) is kept active low by the connected unit until it has read in the data present on the data bus.

In the IEC-bus system, the Signal Generator SMS functions only as a listener, meaning that it is capable of accepting and executing data and commands from the controller. It cannot provide output of measured values nor reply to polls.

2.3.7.1 Setting the Address

Before the signal generator is connected to the IEC bus, a suitable address must be set within the unit.

The coding switch is mounted on the microprocessor board 302.7111. To set the address, the upper cover must be taken off after removing the side screws. The microprocessor board is fitted at the far right of the rack and can be simply pulled out. Figure 4 shows the arrangement of the coding switch.



Fig. 4 Arrangement of coding switch

Table 1 shows the settings required for the various possible addresses. The SMS leaves the factory with the decimal address 28.

Table 1

ASCII Chai	acter	Binary	
Listen Address	Talk Address	Address Switches A5 A4 A3 A2 A1	Decimal Equivalent
(SPACE)	Q	0 0 0 0 0	0
1	A	0 0 0 0 1	1
71	в	0 0 0 1 0	2
#	с	0 0 0 1 1	3
\$	D	0 0 1 0 0	4
ę	Е	00101	5
&	ŕ	0 0 1 1 0	6
1	G	0 0 1 1 1	7
(Н	0 1 0 0 0	8
)	I	0 1 0 0 1	9
•	J	0 1 0 1 0	10
+	к	0 1 0 1 1	11
,comma	L	0 1 1 0 0	12
-	М	0 1 1 0 1	13
a	N	0 1 1 1 0	14
1	0	0 1 1 1 1	15
0	Р	1 0 0 0 0	16
1	Q	1 0 0 0 1	17
2	R	1 0 0 1 0	18
3	S	1 0 0 1 1	19
4	Т	10100.	20
5	U	1 0 1 0 1	21
6	v	1 0 1 1 0	22
7	W	1 0 1 1 1	- 23
8	х	1 1 0 0 0	24
9	Y	1 1 0 0 1	25
:	Z	1 1 0 1 0	26
;		1 0 1 1	27
<		1 1 1 0 0	28
		1 1 1 0 1	29
>		1 1 1 1 0	30

When data are being entered, the limits of the setting ranges of the instrument must be respected. When range limits are exceeded, the OVERFLOW LED $\underline{11}$ starts to flash. If values are entered with a greater resolution than the instrument is capable of setting the values will be rounded to the nearest acceptable figure.

2.3.7.2 Data-transfer Format

In accordance with the IEC Draft Standard data transfer to the SMS uses the format shown in table 2.

Each setting instruction consists of at least an initial character (header) and a final character (delimiter). When setting data are being transferred, the value is contained between these two limiting characters. All characters are transmitted in ISO 7-bit (ASCII) code.

Function	Header	Data	Delimiter
Frequency in MHz	А	max. 8 decimal places with or without decimal point	(comma)
Modulation		· · · · · · · ·	
AM m in % FM ∆f in kHz	B H	max. 3 decimal places with or without decimal point	: و
UNMOD	с	none	,
INT 400 Hz	I	none	9
INT 1 kHz	J	none	,
EXT	К	none	· •
Level			
in dBm	S	max. 3 1/2 digits with or without decimal point,	•
in dBµV	R	with or without negative sign	9
in μV	Р	max. 3 1/2 digits with or	\$
in mV	ବ	without decimal point	9
Switchover for continuous fine variation	X	none	و و
RF OFF	Y	0	و
RF ON	Y	1	9
Pause 15 ms	@	none	9

Table 2

The channel stepping (Δf) and store/recall (STO/RCL) key functions cannot be remote controlled. These settings are obtained by a new command with other setting values.

2.4 Examples

The form of the commands differs according to the calculator make. Table 3 lists examples of commands for setting certain parameters using the most popular desk-top calculators. The decimal address of the SMS is 28.

Table 3

_	Tektronix ⁺) 4051,4052	ћр 9825	hp 9835/9845	Commodore PET 2001/3001	R&S PPC
Ready	8			Open 1,28	H T
Frequency 123.5 MHz	PRINT@28."A123.5,"	wrt728, "Å123.5,"	OUTPUT728;"A123.5,"	PRLNT #1,"A123.5,"	IECOUT28,"A125.5,"
Frequency as variable	LET F=123.5 PRINT @ 282" & "5" 5" " "	F=123.5 wrt728,"A",F,","	LET F=123.5 OUTPUT728; "A"; F;","	LLET F=123.5 FRINT #1,"A";STR\$(F);","	LET F=123.5 PRINT #1,"A";STR\$(F);"," LECOUT28,"A"+STR\$(F)+","
Level -24,8 dBm	PRINT @ 281"5-24.8,"	wrt728, "S-24.8"	ourPUT728,"S-24.8,"	PRINT #1, "S-24.8,"	IECOUT28, "S-24.8,"
Level -23,7 dBm with continuous fine variation	PRINT@28."X,S~23.7,"	wrt728, "X, S-23.7;"	0UTFUT728;"X,S-23.7,"	PRINT #1, "X, S-23.7,"	IECOUT28, "X, S-23.7, "
FN 2,8 kHz dev. with 1 kHz INT.	FILNT @ 28 # #2 . 8 . J , #	wrt728, "H2.8,J,"	0UTPUT728 # #2.8+J.	PRINT #1, "H2.8,J,"	IECOUT28, "H2.8, J.
RP OFF	PRINT(0 28: "YO,"	wrt728,"Y0,"	ourpur728;"YO."	PRINT #1, "TO,"	IECOUT28, "TO,"

⁺When operating the SMS together with the Desk-top Calculator 4051 of Tektronix, the bus line REN (contact 17) must be connected to chassis (contact 18). This can be done by means of a shorting plug.

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Special setting commands

Level variation:

The SMS permits the output level to be electronically varied over a range of -10 dB without interrupting it by switching over. To make use of this function in remote-control operation, the following commands are required:

First the output level which is to be varied must be set in the usual manner.

E.g.: "P 24.5,"

A preceding "X" in the next command triggers the electronic variation.

E.g.: "X, P 24.2,"

Every new command for continuous level variation should be preceded by an X. If the output is not preceded by an X, the level is again normally set with the aid of the attenuator.

The variation range of -10 dB must never be exceeded from the first setting without X. Otherwise switchover will occur.

Level programming with the SMS model .04 (rackmount model without attenuator)

In the UNMOD. and FM modes, output levels of +3 to +13 dBm can be set.

In the AM mode, output levels of -3 to +7 dBm can be set.

The maximum level of the selected mode must be reprogrammed every time the UNMOD. or FM mode is changed over to the AM mode or vice versa. The subsequent level setting commands must be preceded by an X.

Examples with the Tektronix Desktop Calculator 4051:

Changing over from the UNMOD. mode to the AM mode:

PRINT 28: "S7, B55, X, S-2,"

(AM 55%, level -2 dBm)

Other setting commands in the AM mode:

PRINT 28: "B70,X,S-3," PRINT 28: "X,S7,"

Changing over from the AM mode to the FM mode:

PRINT @ 28: "H100,S13,X,Q500,"

Other setting commands in the FM mode:

PRINT @ 28: "H12.5,X,Q100," PRINT @ 28: "X,R115," (AM 70%, level -3 dBm) (same AM, level +7 dBm)

(FM 100 kHz, level 500 mV)

(FM 12.5 kHz, level 1000 mV) (same FM, level 115 dBµV) When switching on the SMS, it is set to the UNMOD. mode. First program again the corresponding maximum level:

PRINT 28: "S13,X,S5,"

(UNMOD., level 5 dBm)

Other setting commands in the UNMOD. mode:

PRINT @28: "X,S3," PRINT @28: "X,Q700," (UNMOD., level 3 dBm) (UNMOD., level 700 mV)

Changing over from the AM mode to the UNMOD. mode:

PRINT@28: "C,S13,X,S12,"

(UNMOD., level -12 dBm)

Pause:

The time required by the SMS for setting the programmed parameters is approximately 40 ms. There is the danger of one of the units used in an automatic test system already proceeding to the measurement before the SMS has been completely set. To avoid this, an additional pause of 15 ms can be called up in the SMS using this command. Thus bus traffic is halted for the time it usually takes the SMS to set.

E.g.: "A 100, ,"

This pause can be called up several times in a row.

REMOTE/LOCAL

When a controller is connected to the SMS, it is automatically set to REMOTE (for remote control). This state is maintained even after termination of the commands. The LED <u>12</u> (Fig. 12) lights and all operating controls on the front panel are disabled. To change over to manual setting, first stop the program run of the controller. Then press the key LOCAL <u>13</u> for local operation of the SMS.

Changeover to LOCAL operation can also be accomplished through the controller. To this end, the instruction GTL (GO TO LOCAL) is issued. For resetting to REMOTE operation, the instruction REMOTE must be issued. It is automatically issued with the next instrument setting instruction.
Changeover to LOCAL operation by pressing key <u>13</u> can be inhibited by issuing, preferably at the beginning of the program run, the non-recurrent instruction LLO (LOCAL LOCKOUT) via the IEC bus.

RESET

Pressing the power key causes a defined basic setting of the instrument (see 2.3.1). This basic setting can also be obtained through the controller by means of the instruction RESET.

Table 4 lists examples of the above instructions. 28 is the decimal address of the SMS. It corresponds to the full decimal equivalent of 60.

IECREN or by system tem-reset IECLAD28 IECSDC IECUNL IECLAD28 IECGTL IECUNL TECLLO R&S PPC Only in connection with an instruction No such capability No such capability No such capability Commodore PET 2001/3001 LOCAL LOCKOUT7 (für alle Geräte) LOCAL728 oder LOCAL7 REMOTE728 oder REMOTE7 hp 9835/9845 RESET728 llo7 (für alle Geräte) rem728 oder rem7 **clr**728 1o1728 hp 9825 WBYTE @ 60, 17: oder WBYTE @17: WBYTE 060,4: WBYTE @ 60,1: WBYTE @ 60: Tektronix 4051/4052 Selected **device** clear Local lockout Go to Local Remote

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Table

3. Maintenance

3.1 Mechanical Maintenance

The SMS does not require any maintenance.

3.2 Electrical Maintenance

Tolerances in this section (if not stated otherwise): Voltages: <u>+5%</u> Frequencies: see data sheet

3.2.1 Measuring Instruments Required

Ref.No.	Instrument	Performance ratings	R&S type	See section
1	RF counter	Range 0.4 to 520 MHz Resolution 10 Hz	part of FAM	3.2.2.2
2	Power meter	Range 0.4 to 520 MHz 3 to 20 mW Z = 50 Ω Error < 0.1 dB	NRS 100.2433.92	3.2.2.3 3.2.2.4 3.2.2.16
3	Standard attenuator	Range 0.4 to 520 MHz O to 120 dB, Z = 50 Ω	DPVP 214.8017.52	3.2.2.5
4	Test receiver	Range 25 to 520 MHz Inherent noise < -10 dB/µV	ESU 2 100.1143.02	3.2.2.5
	Frequency controller		EZK 255.0010	
- 5	RF wave analyzer	Range 0.4 to 1100 MHz Dynamic range > 70 dB		3.2.2.6 3.2.2.7
6	AF generator	Range 50 Hz to 20 kHz Output voltage > 1 V $Z_{out} = 600 \Omega$ Distortion < 0.2%	SRB 100.4094	3.2.2.9 3.2.2.10 3.2.2.11 3.2.2.12 3.2.2.13 3.2.2.14

Ref.No.	Instrument	Performance ratings	R&S type	See section
7	Demodulator	RF range 0.4 to 520 MHz AF range 50 Hz to 20 kHz AM: 0 to 90% FM: 0 to 125 kHz deviation Distortion < 0.2%	FAM 334.2015	3.2.2.9 3.2.2.10 3.2.2.11 3.2.2.12 3.2.2.14
8	Distortion meter	Range 50 Hz to 20 kHz Measurement range 0.1 to 10%	part of FAM	3.2.2.11 3.2.2.13
9	AF counter	Range 0.1 to 2 kHz Resolution 1 Hz	part of FAM	3.2.2.8
10	DC power supply	V > 5 V I > 100 mA	NGM 117.7110 or NGR 100.5084	3.2.2.16 3.2.2.19
11	Deviation meter	Range 0.4 to 520 MHz Inherent spurious deviation < 1.5 Hz (CCITT)	FAM 334.2015	3.2.2.15
12	Psophometer	Min. input voltage 0.1 V with CCITT weighting filter and rms-responsive recti- fier	part of FAM	3.2.2.19
13	Precision exten- sion cable	$Z = 50 \Omega$	SWOB-Z 100.3598.50	3.2.2.17
14	RF millivoltmeter with insertion unit	Range 1 to 520 MHz Sensitivity 100 mV	URV 216.3612	3.2.2.7
15	Controller	Interface according to IEEE 488 and IEC 625.1	Process Controller PPC 343.3510	3.2.2.18
16	Power signal generator	Frequency range 25 to 1000 MHz Power ≥ 2 W Z = 50 Ω	SMLU 200.1009	3.2.2.19

Table 5

Performance Check 3.2.2

3.2.2.1 Checking the Functioning of Display and Keyboard

Check the switch-on state: After switching on the following must be displayed:

Frequency	Modulation	Level
0000000	000	+1000

None of the signal lamps should light.

To check the keyboard, make entries according to section 2.3 and observe display.

3.2.2.2 Checking the Frequency Setting and Accuracy

Settings on the SMS: UNMOD, level approximately 100 mV.

Test setup: Connect the frequency meter to the RF output of the SMS.

Test: Set the following frequencies on the SMS and check with the frequency counter:

110	320.0024	445.75
150	340.0048	445.775
280	340,0049	445.8
281	340.0050	445.85
285	340.0052	445.95
293	340.0056	446.55
297.9901	340.0064	447.35
309	340.0080	448.95
318.0001	340.0112	452.165
318.0152	340.0176	458,6
	340.0304	445.725
	399.8727	

Permissible relative frequency error:

 1×10^{-6} /month + 1 x 10^{-6} /°C (standard reference oscillator) 1×10^{-6} /year + 1 x 10^{-7} (optional reference oscillator) in the range 0 to 50° .

After warmup, the relative frequency error must be the same at all frequencies.

3.2.2.3 Checking the Error and Frequency Response of the Output Level

a) UNMOD, level 13 dBm Settings on the SMS:

b) AM, m = 0, level 5.1 dBm.

Test setup: Connect power meter to RF output of the SMS.

Test: Measure output level between 0.4 and 520 MHz. The permissible frequency response (difference between maximum and minimum level)

is \leq 1.8 dB (0.4 to 8 MHz)

 \leq 0.8 dB (8 to 520 MHz).

The permissible error of the output level (departure from level setting)

is $\leq +1.8$ dB (0.4 to 8 MHz) $\leq +1.3$ dB (8 to 520 MHz).

3.2.2.4 Checking the Fine Level Adjustment

Settings on the SMS: UNMOD, level 11.1 dBm, frequency 130 MHz.

Test setup: Connect the power meter to the RF output of the SMS.

Test: Reduce level to 1.1 dBm by means of 0.1-dB key and check the level steps on the power meter. The permissible deviation is +0.5 dB at 1.1 dBm.

If the level has dropped below 1.1 dBm by accident during fine adjustment, re-enter level of 11.1 dBm and subsequently reduce by means of the 0.1-dB key.

3.2.2.5 Checking the RF Attenuator Error

Settings on the SMS: UNMOD., level 13 dBm, frequency 131 MHz. Test setup:



Test: Set

Set the attenuator to 112 dB.

Set test receiver to a frequency of 131 MHz and linear averagevalue indication and select a -10 dB level and 15-kHz bandwidth. Then check the level at the following settings:

Level SMS (dBm)	Attenuation DPVP (dB)
13	112
11	110
9	108
5	104
3	102
-7	92
-27	72
-67	32
-87	12

Table 6

The permissible level error referred to 13 dBm level setting on the SMS is \leq +0.2 dB.

3.2.2.6 Checking the Suppression of Non-harmonic Spurious Signals Settings on the SMS: UNMOD., level 3 dB, frequency 0.4 to 520 MHz. Test setup: Connect the RF wave analyzer to the RF output of the SMS. Test: The suppression of non-harmonic spurious signals in the range 0.4 to 520 MHz is preferably checked at the following frequencies:

Frequency setting on the SMS	Frequency of non-harmonic spurious signals
Range 0.4 to 129.9999 MHz 129	$\begin{cases} 380 + f_{SMS} \\ 380 \\ 251 \end{cases}$
Range 110 to 129.9999 Range 75 to 129.999 319 320 439 440	380 -2 f _{SMS} 380 -3 f _{SMS} 40, 340 80, 300 80, 460 40, 420

Table 7

Minimum permissible suppression of non-harmonic spurious waves: $\geq 60~\mathrm{dB}.$

3.2.2.7 Checking the Suppression of Harmonics

Settings on the SMS: UNMOD., level 3 dBm, frequency 0.4 to 520 MHz.

Test setup: Connect the RF wave analyzer to the RF output of the SMS.

Test: Minimum permissible suppression of harmonics in the range 0.4 to 520 MHz: \geq 30 dB.

3.2.2.8 Checking the Internal Modulation Frequencies

Settings on the SMS: AM INT a) 400 Hz b) 1000 Hz.

Test setup: Connect the AF counter to the modulation socket on the front panel.

Test: The permissible frequency error is +3%. The output voltage should be between 0.95 V and 1.05 V.

3.2.2.9 Checking the Modulation Attenuator

Settings on the SMS: FM EXT., deviation 100 kHz, level 3 dBm, frequency 460 MHz.

Test setup: See illustration in section 3.2.2.11.

Modulation signal 1 kHz. Vary voltage until deviation of 100 kHz is measured.

Test:

Set the following deviations and check by means of modulation meter:

9, 16, 16.5, 17, 18, 20, 24, 32, 64 kHz.

Permissible error: +2%.

NOTE: Select small enough AF bandwidth (e.g. 3 kHz) on the demodulator to ensure that no additional error due to noise is obtained when measuring small deviations.

3.2.2.10 Checking the FM Error

Settings on the SMS: FM, deviation 100 kHz, level 3 dBm, $150, 250, 460, 500, 920^{+}$, 1000^{+} MHz

Test setup: See illustration in section 3.2.2.11. Modulation voltage 1 V +1%.

Test: Permissible error of the frequency deviation at modulation frequencies of 400 Hz and 1000 Hz internal and i and 20 kHz external: $\leq +5$ %.

+) If fitted with Option SMS B-2.

3.2.2.11 Checking the FM Distortion

Settings on the SMS: FM, level 3 dBm.

Test setup:



Test: Check the modulation distortion at the following settings:

Frequency of the SMS (MHz)	Modulation frequency	Deviation	Permissible distortion
150; 520	400/1000 Hz internal 1 kHz external	75 kHz 75 kHz	$ \leq 1\% \\ \leq 1\% \\ \leq 1\% $

Table 8

3.2.2.12 Checking the AM Error

Settings on the SMS: AM 1 to 90%, level 5.1 dBm

a) 0.4 MHz, 5 MHz b) 350 MHz

Test setup: See illustration in section 3.2.2.11.

Test: Check error of the modulation depth at the following frequencies:

Frequency of the SMS	Modulation frequency
a) 0.4/5 MHz b) 350 MHz	$\begin{cases} 0.4/1 \text{ kHz internal} \\ 1 & /5 \text{ kHz external} \\ \{0.4/1 \text{ kHz internal} \\ 1 & /20 \text{ kHz external} \end{cases}$

Permissible errors: a) $< \frac{+8\%}{+5\%}$ b) $< \frac{+5\%}{+5\%}$

Table 9

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3.2.2.13 Checking the Envelope Distortion

Settings on the SMS: AM 80%, level 5.1 dBm

- a) 0.4/5 MHz
- -b) 350 MHz

Test setup: See illustration in section 3.2.2.11.

Test: Measure the envelope distortion at the following frequencies:

	Frequency of the SMS	Modulation frequency	Permissible distortion
a)	0.4/5 MHz	1 kHz internal/ external	≦1.5 %
ъ)	350 MHz	1 kHz internal/ external	<u>≤</u> 1,5%

Table 10

3.2.2.14 Checking the Phase Modulation

Settings on the SMS: AM external 0%, level 3 dBm, frequency 130 MHz. Test setup: See illustration in section 3.2.2.11.

Test: Apply AC voltage of 1.00 V +1% (50 Hz to 3 kHz) to the modulation socket on the rear panel. This must produce a phase modulation of 5 rad (+5%). This corresponds to a deviation of 5 kHz at a modulation frequency of 1.00 kHz.

3.2.2.15 Checking the Spurious Deviation

Settings on the SMS: UNMOD., level 3 dBm, frequencies 129 MHz, 520 MHz. Test setup:



Test: Measure spurious deviation by means of CCITT weighting filter (0,3 to 3 kHz) and rms-responsive rectifier. (The inherent spurious deviation of the deviation meter used must be < 1.5 Hz) Permissible spurious deviation: \leq 4 Hz.

3.2.2.16 Checking the External ALC

Settings on the SMS: FM EXT., O kHz deviation, level 13 dBm, frequency 130 MHz.

Test setup: Connect power meter to the RF output of the SMS.

Test: Apply DC voltage to give a reduction of the RF level of 6 dB. The applied DC voltage should be $\pm 1.41 \text{ V} (\pm 5\%)$.

3.2.2.17 Checking the VSWR

Settings on the SMS: AM 0%, level -3 dBm Frequency 100 to 520 MHz

Test setup:



Test: Vary the test frequency until a voltage maximum is measured. Subsequently vary the frequency ($\Delta f = 7.5$ MHz at a cable length of 10 m) until the adjacent voltage minimum is measured.

$$VSWR = \frac{V_{max}}{V_{min}}$$

3.2.2.18 Checking the Interface Functions

Operate the SMS via a controller, such as Desktop Calculator PPC. Program all setting commands given in section 2.3.7 and check the correct execution of the commands by the SMS on the front panel displays.

3.2.2.19 Checking the Response Threshold of the Overload Protection

Settings on the SMS: UNMOD., level -27 dBm, frequency 130 MHz.

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Test setup 1: Feed a DC voltage of 0 to 6 V to the RF output of the SMS (any polarity).

Test: Increase DC voltage starting from O V. At > 1 V, the overload protection should respond. If so, the LED of the RF OFF key <u>19</u> lights.

Test setup 2: Apply frequency between 25 and 1000 MHz to the RF output from a power signal generator such as the SMLU, RF power 0 to 2 W.

At a power level +25 $\leq P \leq$ +30 dBm the overload protection should respond. When this happens, the LED of the RF OFF Key 19 lights.

Performance Test Report 3.2.2.20

R&S Signal Generator SMS Id. No. 302.4012.02 Serial No.

Name

Ref.No.	Characteristic		Measurement acc. to section	Min.	Actual	Max.	Unit	
, ,	Functional check of display and keyboard	splay and		J		1		
Q	Frequency error at 500	MHz	3.2.2.2					[
	a) Temperature	Standard		1		+500	$Hz/^{O}K$	
	ellect	Option SMS B-1	· · · · · · · · · · · · · · · · · · ·	ş		-20	Hz	
	b) Aging	Standard		ţ		+500	Hz/mon	
		Option SMS B-1		I		+25	Hz/mon	
Ŕ	Spurious deviation with CCITT weighting filter	1 CCITT	3.2.2.15					
		129 MHz		1		4	Hz	
		520 MHz		T		7	Hz	
	with Option SMS B-2 1	2HM 0401	***	ł		ω	Hz	
4	Suppression of non-harmonic spurious signals	nonic	3.2.2.6					
±	a t	108 MHz		60		1	, đb	
		119 MHz		60		1	dB	
		129 MHz		60		ľ	đb	
	with Option SMS B-2	530 MHz		20		t	đB	
		1040 MHz		Q		1	цr	

Ref.No.	Characteristic	Measurement acc. to section	Min.	Actual	Max.	Unit
Ŋ	Suppression of harmonics	3.2.2.7				
	0.4 to 520 MHz		9Ê		t	dB
	with Option SMS B-2 520 to 1040 MHz		20		1	dB
9	Error of output level	3.2.2.3				
	0.4 to 8 MHz		-1,8		+1.8	dB ·
	8 to 520 MHz		-i-3		+1.3	dB
	with Option SMS B-2 520 to 1040 MHz		-1.8		+1.8	dB
7	Error of output level with fine level adjustment by means of 0.1-dB keys	3.2.2.4	یں 0		-0 - 0+	đb
8	Frequency response	3.2.2.3				
	0.4 to 8 MHz 8 to 520 MHz	······································	1		1.8 0.8	ម្មភ្
6	Internal modulation frequencies	3.2.28				n an
	400 Hz		388		412	Hz
	1000 Hz		970		1030	Hz
	Output voltage		0.95		1.05	Λ

Max. Unit		43.2 %	<u></u>		94,5 %		1.5	ש שייים ריים		5		9.18 kHz		16.83 kHz	17.34 kHz			24.48 kHz	32.64 kHz	
n. Actual		36.8	82.8		85,5					 		8.82	15.68	16.17	16.66	17.64	19.60	23.52	31.36	62,72
Measurement Min. acc. to section	J.2.2.12	36	82	38	85	3.2.2.13	1			 1	3. 2,2,9	°	15	16	16	17	19	23	31	62
Characteristic	AM error	т т = 40% 1 МН-	%06 = m 7117, T	лео мнят = 40% Збо мнят	%об = ш		0.4 MHz, m = 80% at 1 kHz	at' 1 kHz	808 = m "zhw occ	1040 MHz, m = 80% at $1 KHz$	Error of modulation attenuator	460 MHz deviation = 9 kHz	16 kHz	16.5 kHz	17 kHz	18 kHz	20 kHz	24 kHz	32 kHz	64 kHz
Ref.No.	10					Г,					12									

•••

Ref.No.	Characteristic	Measurement acc. to section	Min.	Actual	Max.	Unit
13	FM error	3.2.2.10				
	100 kHz deviation 150 MHz		95		105	kHz
	250 MHz		95	4	105	kHz
-	2HM 09t		95		105	kHz
	500 MHz		35		105	kHz
	with Option SMS B-2 920 MHz		95		105	kHz
	1000 MHz		95		105	kHz
14	FM distortion	3.2.2.11		· · ·		
	150 MHz $f_{mod} = 1$ kHz					R
	520 MHz deviation = $75 kHz$		1		г	R
						-
15	Phase modulation	3.2.2.14				
	$520 \text{ MHz f}_{mod} = 1 \text{ kHz}$		4.75		5.25	rad
16	ALC sensitivity	3.2.2.16				
	for $\Delta P = 6 dB$ 130 MHz		1.34	-	1.48	٨
·		وتعمينه فيترقد بالشعد والبلاي يتبالك مستركي كالبرية المعيدين المعارفتان فالمستعملية والمسابح المتعاركم		كملكك المتركبين والمتركب والمتوسية والمراجعة المراجعة		

17VSWRacc. to sectionacc. to section 17 VSWR 270 MHz $3.2.2.17$ $ 270$ MHz 570 MHz $ 1.2$ 510 MHz $ 1.2$ 1.2 18 Checking the interface function $3.2.2.18$ $ 19$ Response threshold of overload $ (v)$ 19 Response threshold of overload $ (v)$ 10 Response threshold of overload $ (v)$ 10 Response threshold of overload $ 10$ Response threshold of $ 11$ $ -$	Ref. No.	Ref. No. Characteristic	Measurement	Min.	Min. Actual Max.	Max.	Unit
VSWR 270 MHz 270 MHz 510 MHz 510 MHz 510 MHz 510 MHz - 1.2 1.2 1.2 1.2 7.0 MHz 510 MHz - 1.2 7.5 1.2 1.2 1.2 1.2 1.2 1.2 1.2 1.2 1.2 1.2			acc. to section				
270 MHz - 1.2 510 MHz - - 1.2 Checking the interface function 3.2.2.18 - 1.2 Response threshold of overload 3.2.2.18 - - - Response threshold of overload DC - - - - Response threshold of overload T -<	17	VSWR	3.2.2.17				
510 MHz - 1.2 Checking the interface function 3.2.2.18 - 1.2 Response threshold of overload - - - protection DC - 5 RF RF - 1	-			1		1,2	
Checking the interface function 3.2.2.18		510 MHz		1		7.2	
Response threshold of overload protection DC RF 1	18	Checking the interface function	3.2.2.18				and a second and a s
Response threshold of overload protection DC RF - 5 - 1				1		1	(A)
ι ι ι ι	19	Response threshold of overload protection					
		2	,	 I		Ś	Λ
		RF		.1		1	M

4.1 Overall Function

The output frequency of the Signal Generator SMS is derived by frequency synthesis from a 10-MHz reference frequency of crystal stability. The central frequency-processing unit is the phase-locked loop of the two main oscillators 260 to 380 MHz and 380 to 520 MHz (PC boards Y3 and Y4). A reference frequency obtained by frequency addition in the functional group "mixer oscillator" (Y7) from the signals of the two interpolation oscillators (Y7, Y8) is fed to the phase detector of the functional group "master oscillators". One of the four fixed frequencies 300, 340, 420 or 460 MHz is fed to the mixer in the feedback path of the phaselocked loop of the main oscillators (Y3). The fixed frequencies are obtained by mixing the signals of the two auxiliary oscillators, 80 MHz (or 40 MHz) (Y6) and 380 MHz (Y5).

The frequency of the signal in the feedback path of the phase-locked loops of the main oscillators at the phase detector input is given by

$$\mathbf{f} = \left| (\mathbf{f}_{H} - \mathbf{f}_{osc}) \right| \frac{1}{M} - \mathbf{f}_{osc}$$

where f_H is one of the four fixed frequencies and M is the division factor of the frequency divider. At synchronization, the frequencies of the two signals at the phase detector inputs are equal, i.e.

$$f_{ref} = |(f_H - f_{osc})| \frac{1}{M}$$
.

Accordingly,

$$f_{osc} = f_{H} + M f_{ref}$$

where f_{ref} is the frequency derived from the interpolation oscillators. The oscillator frequency f_{osc} is always so adjusted by the phase-locked loop that this relation is fulfilled.

By addition of the two interpolation frequencies in the functional group "mixer oscillator" (Y7) the signal f_{ref} with a frequency from 2.0 to 2.2 MHz is obtained at the output of the board Y7. This signal can be varied by varying the P divider in 100-Hz/M steps and the N divider in 50-kHz/M or 25-kHz/M steps.

<u>4.</u>

The 50-kHz/M step size applies if M > 19 and the 25-kHz/M step size if $M \leq 19$. These step sizes are obtained by the 2:1 frequency divider connected in cascade with the M divider on Y6 being switched into circuit only for division factors $M \leq 19$. The dependency of the step sizes on the division factor M is due to a frequency divider with the same division factor M as in the phase-locked loop of the main oscillators being connected ahead of the interpolation oscillators on the board Y6. This ensures constant 100-Hz and 50-kHz step sizes of the main oscillators independent of the division factor M.

Variation of the P and the N dividers of the interpolation oscillators permits the frequency of the main oscillators to be varied in 100-Hz steps over a range of 2 MHz.

The frequency division factor M is adjustable between 10 and 30. By each variation of M by one step the frequency of the main oscillator is varied by a constant step size of 2 MHz over a range of 40 MHz.

The main oscillators are tunable in the frequency ranges $f_{H} \pm (20 \text{ to } 60)$ MHz (f_{H} = one of the four fixed frequencies 300, 340, 420, 460 MHz) by setting the P, N and M frequency dividers accordingly.

Fig. 5 shows the relation between the four fixed frequencies, the division factor M and the frequency of the main oscillators.





From 260 to 520 MHz the output frequency of the SMS is the frequency of the main oscillators. The range 130 to 260 MHz is produced on the board 2 by halving the frequency. The output frequencies from 0.4 to 130 MHz are obtained by mixing with the 380-MHz signal on board Y1.

The functional group "fixed 80-MHz oscillator" (Y6) represents a phasesynchronized frequency modulator. With this type of modulator the limit frequency of the phase-locked loop is far below the lowest modulation frequency. Thus synchronization is maintained even when the oscillator signal is modulated. The reference value for the modulation is produced on the board Y10 "modulation control". The modulation of the fixed 80-MHz oscillator is superimposed on the phase-locked loop of the main oscillators by means of the mixer.

Level control and amplitude modulation are combined in one control loop on the boards Y1 and Y2, the modulation signal being superimposed on the reference value of the level control. The reference value of the level control and the AM modulation signal are produced on the board Y10 "modulation control".

The output attenuator can be mechanically set in steps of 2 dB up to a maximum attenuation of 138 dB. The O.1-dB steps are electronically set via the level control. Level control permits a total reduction of the RF level by 10 dB in steps of O.1-dB steps, starting from a maximum level of +13 dBm.

All settings of the SMS are controlled by the microprocessor on board Y11. These settings include e.g. the setting of the M, N and P frequency dividers, the setting of the modulation and level dividers on board Y10 for producing the reference values for modulation and level control, setting of the output attenuator and switchover of the main oscillators 260 to 380 MHz and 380 to 520 MHz.

The microprocessor constantly interrogates the keyboard and the IEC bus. From the data entered it calculates the necessary settings and executes them. Furthermore the microprocessor controls the readouts.



Fig. 6 Block diagram of the oscillator Y3.

The oscillator unit Y3 contains two varicap-tuned RF oscillators. One of the two is always switched on and the other one switched off. The frequency range of oscillator 1 is 260 to 380 MHz and that of oscillator 2 380 to 520 MHz. The two oscillator outputs are taken to the common output "RF output 260 to 520 MHz" via switching diodes.

The signal derived from the oscillator signal by conversion and available at the output "RF output 20 to 60 MHz" is taken to a phase detector for phase control of the oscillators. The phase detector and the associated phase control circuit are mounted on the phase control board. The functioning of the phase control is described in section 4.3.

Both oscillators use a FET as active device. They are of the negative impedance type. A varicap-tuned (GL1, GL2) inductance (L5, L6) is connected in parallel with the gate terminal. The oscillator performance is controlled by the drain current. The optimum operating point is set by means of the potentiometers R1, R2. The oscillator is inductively coupled out via the coils L7, L8.

The level is boosted by 6 dB in the following amplifier stages (T3, T4), which are also used for decoupling. The subsequent lowpass filters enhance the suppression of harmonics of the oscillator signal to 40 dB.

The oscillators and amplifier stages are switched on and off via the transistors T5, T7, which connect the oscillators to the +20-V operating voltage. The transistors T5, T7, in turn, are switched by means of a TTL signal at the oscillator switchover input 2b. If the input 2b is at low level the oscillator 1 is switched on, T7 and the switching diode GL6 are conducting and T5 and the switching diode GL5 are cut off. If the input 2b is at high level the oscillator 2 is switched on, T5 and the switching diode GL5 are cut off.

The tuning voltage for both oscillators is applied via a common line.

The signal level at output 4a, b "RF output 260 to 520 MHz" is between -2 dBm and +4 dBm. The suppression of harmonics is approximately 40 dB.

The integrated broadband amplifier B1 is high-impedance coupled to the "RF output 260 to 520 MHz". The amplifier output signal boosted to 4 dBm is available at the L0 input of the mixer B50. One of the fixed frequencies (300, 340, 420 or 460 MHz) is present at the RF input of the mixer. The signal level at the "RF input 300, 340, 420, 460 MHz" is about -20 dBm. It is boosted by 6 dB in the amplifier stage T50.

Utilizing both conversion sidebands a signal between 20 and 60 MHz is obtained at the mixer output. The following lowpass filter with a cutoff frequency of 70 MHz removes unwanted mixture products. The level of the signal at the RF output 20 to 60 MHz is about 0 dBm.

4.3 Phase Control Loop Y4



Fig. 7 Block diagram of the phase control loop.

The phase control board together with the oscillator board forms a phase-locked loop holding oscillators 1 and 2 in phase synchronization with the reference frequency derived from the two interpolation oscillators.

The oscillator signal is converted down to 20 to 60 MHz in the mixer B1 in the feedback branch of the control loop. Frequency division by the factor M is accomplished in the M divider which is also connected in the feedback path. M is adjustable to between 10 and 30. At synchronization both input signals at the phase detector B7 are of equal frequency. The pulse-shaped output signals of the phase detector are applied to the integrator B10 where the control voltage is produced. The tuning voltage for the two master oscillators is obtained from the control voltage and a fixed DC voltage in the summing amplifier B11.

For the frequency ranges 260 to 320 MHz and 380 to 440 MHz the frequency available at the output of the M divider is $(f_H - f_{osc})$:M and for the frequency ranges 320 to 380 MHz and 440 to 520 MHz it is $(f_{osc} - f_H)$:M.

Hence when changing the oscillator frequency, in one case the change in frequency at the divider output is in the same direction and in the other case in the opposite direction. In order to obtain the correct direction for the lock-in of the control loop in both cases the inputs of the phase detector B7 are reversed by the gate module B6.

When the control inputs 10, 12 of B6 are at low/high levels the input 9 is connected through to the output 15 and input 4 to output 2. When the control inputs 10, 12 are at high/low levels the input 9 is connected through to the output 2 and the input 4 to the output 15.

The M divider consists of the two programmable counter modules B2 and B3, the gate module B51 for decoding the counter outputs and of the latch flipflop B4. The counters operate as down counters, i.e. they count from the preset figure M corresponding to the bit pattern present at the data inputs D_0 to D_3 down to zero. The division factor is present at the data inputs in binary code, the least significant bits (ISB) being allocated to B2 and the most significant bit (MSB), which is 24, to B3. Every positive edge of the clock signal decrements the counter. When the counter B2 has reached the count 0000 the carryover output C OUT goes low for one clock period. The carry is carried over to the following counter B3 via the input C IN, the counter B3 being decremented by 1 with each carryover. The condition for counter presetting is decoded from the bit pattern present at the counter outputs Q_0 to Q_3 of B2 and B3 two clock pulses prior to reaching the count 0000. The pulse trains thus obtained are shown in Fig. 8, where the divider is preset for the division factor 12.

The control inputs S1 are at high level during the count phase. If the count 2 is reached the D input of the flipflop B4 goes high. With the next clock pulse the flipflop also goes high and, as a result, the control inputs S1 and the D input go low. The following clock pulse sets the counter and supplies a low signal to the flipflop so that the control inputs S1 again go high and the count cycle is restarted.

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Fig. 8 Pulse train of the counter outputs Q_0 to Q_3 .

Line amplifier B1 connected ahead of the frequency divider amplifies and converts the 20-to-60-MHz input signal to ECL level.

The digital phase detector B7 is an edge-triggered flipflop. Triggering is accomplished by the positive edges of the signals at the inputs 6 and 9. The outputs 3 and 12 supply pulses the duty cycle of which depends on the phase or frequency difference of the two input signals. If the frequency of the two input signals differs the integrator voltage across C43 is corrected by means of the output pulses of the phase detector until the frequencies of the two input signals at the phase detector input are equal. When the frequencies are equal the integrator is only pulsed so as to ensure a constant integrator voltage.

The Schmitt trigger circuits Bl2I/II prevent the control from running to the operating limits of the integrator Bl0 during the lock-in process.

. The dynamic range of B10 is ±6 V. The switch-on thresholds of the Schmitt trigger circuits C12I/II are -12 V and ±12 V. The switch-off thresholds of both Schmitt trigger circuits are 0 V. As soon as the output voltage of B10 reaches ±12 V or -12 V, the Schmitt trigger B13/II or B12/I causes the diode GL4 or GL3 to conduct. The current through these diodes recharges the integrator to 0 V.

The resistive dividers R109/R111 to R113 and R109/R114 to R117 permit four fixed voltages per oscillator to be set. B19 and B20 each contain four FET switches. The switching information for the FET switches is obtained in the decoder B18 from the control signals A, B, C. The oscillator tuning voltage is produced by adding the fixed voltage and the control voltage of the integrator B10 in B11.

In order to keep the gain of the control loop constant, the control voltage gain is changed over. By switching over the resistors R143, 144, 145 depending on the setting of the M divider - the gain variation of the frequency divider in the feedback path of the control loop is compensated for. By switching over the resistors R94 to R98 - depending on the control signals A, B, C - the variation of the tuning sensitivity of the oscillators is compensated for.



4.4 100-Hz Interpolation Oscillator Y8

Fig. 9

Block diagram of the interpolation oscillator.

The 100-Hz interpolation oscillator unit comprises a phase-locked loop with the programmable frequency divider P in its feedback path. The frequency at the output can be varied between 100 and 135 kHz with a frequency step size of 1 kHz/M by variation of the division factor P. M equals the division factor of the frequency divider of the phase control loop Y4. The digital phase detector B5 is a slope-triggered flipflop with three-state output. Triggering is accomplished by the positive slopes of the signals at the signal input 14 and at the reference input 3. Output 13 supplies pulses with a voltage level (low or high) and duty cycle dependent on the phase and frequency difference between the input signals. If the frequencies of the two input signals differ the integrator voltage across Cl is corrected by means of the output pulses of the phase detector until both signals at the phase detector have the same frequency. As soon as this is the case C1 is only charged to the extent necessary to keep the integrator voltage constant. The oscillator is tunable through the range from 25 MHz to 33.7 MHz with the aid of the triple varicap GL3.

The P divider consists of three programmable counter modules B7, B8, B9 and the latch flipflop B10. B7, B8 and B9 are connected in cascade, operating as an asynchronous down counter. The division factor is present in binary code at the 12 data inputs, the LSB being present at B9 and the MSB at B7. The borrow outputs of the counters B7 and B8 supply a negative pulse with a pulse duration corresponding to the input pulse at the count down inputs as soon as the counters have reached the count 0000.

The borrow outputs of B7, B8 and the counter output Q_D of B9 are combined via an OR circuit. In this way the condition for presetting the counters is decoded. As soon as the borrow outputs of B7, and the Q_D output of B9 go low the flipflop B10 is cleared via the preset input. At the count 3 the counter output Q of B9 goes low. With the next clock pulse the low signal is transferred to the flipflop B10I.

At the count 1 the Q and \overline{Q} outputs of B10/II go low and high, respectively. A low signal is supplied to the load inputs of the counter modules, resetting the counters. The high signal present at the Q output of B10/II, B10/I is reset via the preset input. After the load inputs have gone high again the new count cycle starts.

The data inputs of the counter modules are set by the I/O expander Bl1. The setting data are read in at the input port P2, transferred to the output ports P4 to P7 and stored. The control signals E, F, G and H, the functions of which are listed in the Tables 11 and 12, are present at the output port P4.

Control signal	HIGH	LOW	
E F G H	Lowpass filter 260 MHz Mixer on Doubler on AM slow Y10	Lowpass filter 190 MHz Mixer off Doubler off	¥2 ¥1

Tabl	е	1	1

		0.4 to 5 MHz	5 to 130 MHz	130 to 190 MHz	190 to 260 MHz	260 to 520 MHz	520 to 1040 MHz		
-	E	L.	L	L	Н	L	Ĺ		
	F	Н	Н	Ŀ	L	L	L		
	G	\mathbf{L}	L	L	Ľ	L	Н		
	Н	H	L	L	L	L	L		
1									

Table 12

4.5 50-kHz Interpolation Oscillator Y7

The output signal of the 100-Hz interpolation oscillator board Y8 is applied to the input ST7.11a, b. The frequency 500/M (kHz) for M > 19 or 250/M (kHz) for $M \leq 19$ is applied to the input ST7.4a, b, M being the division factor of the frequency divider of Y4. The programmable divider N is connected in the feedback path of the phase-locked loop of the 50-kHz interpolation oscillator. The division factor N permits the frequency of the 50-kHz interpolation oscillator to be varied in steps of 500/M (kHz) or 250/M (kHz).

The frequencies of the 100-Hz interpolation oscillator (Y8) and of the 50-kHz interpolation oscillator (Y7) are added in the phase-locked loop of the mixer oscillator. The sum frequency equals the frequency of the mixer oscillator.



Fig. 10 Block diagram of the 50-kHz interpolation oscillator Y7.

The digital phase detectors B2 and B8 are edge-triggered flipflops with three-stage outputs. Triggering is accomplished by means of the positive edges of the signals at the signal input 14 and at the reference input 3. Output 13 supplies pulses with a voltage level (low or high) and duty cycle dependent on the phase or frequency difference of the input signals. If the frequencies of the two input signals differ the integrator voltage at C1 or C11 is corrected by means of the output pulses of the phase detector until the two signals at the phase detector have the same frequency. As soon as this is the case the integrator capacitors are recharged only to the extent necessary to keep the integrator voltage constant. The integrator voltages serve as oscillator tuning voltages.

The Schmitt trigger circuit BlO prevents the control from running to the lower operating limit of the integrator B9 during the lock-in process. As soon as the tuning voltage drops below the response threshold of the Schmitt trigger circuit (4 V) the integrator is recharged to the maximum tuning voltage (25 V) via the diode GL10.

The divider N consists of three programmable counters B4, B5 and B6 connected in cascade and the latch flipflop B3. The counters operate as down counters. The division factor is present in binary code at the 12 data inputs, the LSB being present at B6 and the MSB at B4. At the count 0000 the borrow outputs of B4 and B5 supply a negative pulse which has the same pulse width as the input pulses at the count down inputs.

The borrow outputs of B4, B5 and the counter outputs Q_A , Q_C and Q_D of B6 are combined via an OR circuit. In this way the condition for presetting the counters is decoded.

At the count 0 of B4 and B5 and at count 2 of B6 the D input of the flipflop B3 goes low. With the next positive clock edge the low signal is transferred to the flipflop. While a low signal is present at the load inputs the counters are reset. With the next positive clock edge the high signal of the Q output fed back to the D input is transferred to the flipflop. The next positive clock edge starts the countdown from the new counter state.

The data inputs of the counters are set by the I/O expander B13. The setting data are read in at the input port P2, transferred to the outputs ports P4, P7 and stored.

The control signals A, B, C, D (A, B: range identification, C: oscillator switching, D: divider switching) are present at the output port P4.

A	В	с	Frequen	cy range (MHz)	С	Scillator Y3
L H L H	L L H H	L L L L	260 to 280 to 320 to 360 to	320 360	2	260 to 380 MHz 260 to 380 MHz 260 to 380 MHz 260 to 380 MHz
L H L H	L L H H	H H H	380 to 400 to 440 to 480 to	440 480	27	80 to 520 MHz 80 to 520 MHz 80 to 520 MHz 80 to 520 MHz
	0.4 to 130 MHz 130 to 2		130 to 260 MH	Iz	260 to 1040 MHz	
D	D L			H		L
	Divider on Y2					

4.6 Converter Y5

(See circuit diagram 302.6015 S)

A 380-MHz signal (ST5.19ab) and a conversion frequency 300, 340, 420 or 460 MHz (BU14) are produced on the converter board Y5. The 380-MHz signal is used on the circuit board Y1 as beat signal for conversion of the range 380.4 to 510 MHz to the output frequency range 0.4 to 130 MHz. From the conversion frequency the IF for the phase control is obtained on the PC board Y4 by mixing it with the signal of the main oscillator.

The 380-MHz signal is produced by an oscillator, the output signal of which is reduced to 1 MHz by 380:1 frequency division and which is synchronized with the 1-MHz reference frequency available at ST5.15a by means of a phase comparison circuit. The four conversion frequencies are obtained by mixing the 380-MHz signal with the reference frequency of 40 or 80 MHz applied to ST5.11a,b using the upper or the lower sideband.

The 380-MHz oscillator is based on the FET T1. The elements of the tuned circuit are L3-C3-C4-C7 to C10 and the tuning diode GL1. The oscillator voltage is inductively coupled out by means of L3 and boosted by about 10 dB in a two-stage amplifier (T2 to T3). A resistive branching network (R13 to R16) is connected between the first and the second amplifier stages, via which the 380-MHz signal is coupled out (ST5.19a.b). Likewise a branching network (R23 to R31) is connected to the output of the second amplifier stage, via which the signal passes to a 10:1 ECL divider (B1). The 38-MHz output signal of this divider is converted to TTL level in a level converter (T5-T6). Subsequently, the frequency is again divided, first in a 2:1 divider (B2/II) and then in a programmable divider circuit (B3 to B4) with fixed division ratio 19:1. The output signal of the divider - after division from 380 MHz to 1 MHz - is taken to a phase discriminator (B5), at whose reference input the 1-MHz signal from ST5.15a is present. The pulses delivered by this phase discriminator following phase comparison are integrated into a DC voltage by means of a differential integrator (B6) which, after removal of the reference frequency by means of a lowpass filter (R46-R47-C49), is supplied to the varicap GLI as tuning voltage. Tuning voltage control ensures that the two 1-MHz signals at the phase discriminator are always in phase. The operating point of the tuning voltage can be adjusted by means of the trimmer C9.

The conversion frequency is obtained by coupling out the 380-MHz signal at the second output of the branching network R22 to R26, boosting it in an integrated RF amplifier (B10) to about 5 dBm and applying it to the mixer Bll as mixer signal, where it is mixed with the 40- or 80-MHz reference signal present at ST5.11a, b. As a result the frequencies 340 MHz and 420 MHz or 300 MHz and 460 MHz are obtained. Since the conversion frequency must be of high spectral purity (suppression of nonharmonic spurious signals > 70 dB) it is always necessary to suppress one of the sideband frequencies developing during mixing. This is accomplished by means of four two-circuit bandpass filters tuned to 300 MHz (L26-L27), 340 MHz (L20-L21), 420 MHz (L24-L25) and 460 MHz (L22 to L23). Switching diodes (GL10 to GL27) connected to the inputs and outputs of the filters always switch one filter on while the remaining three remain cut off. The switching states are controlled by the signals present at ST5.13b and 15b with the aid of the switching stage comprising B12 and T8 to T11.

4.7 Reference Board Y6

(See circuit diagram 302.6215 S)

The reference signals for the interpolation oscillators of the circuit boards Y7 and Y8 and for the fixed 80-MHz and 380-MHz oscillators of the circuit boards Y5 and Y6 required for frequency synthesis are produced on the reference board Y6.

All signals are derived from the frequency of a 10-MHz oscillator. The 1-MHz signal is obtained by 10:1 frequency division. The 80-MHz signal is produced by an oscillator whose frequency is divided down from 80 MHz to 10 kHz and synchronized with the crystal frequency which is also divided down to 10 kHz. Changeover to 40 MHz is accomplished by 2:1 frequency division. The reference signals for the circuit boards Y7 and Y8 are obtained by frequency division of the 1-MHz signal by means of a divider programmable to between 10:1 and 30:1.

The 10-MHz crystal oscillator comprises the transistor T10 and the crystal Q1 as selective feedback element between collector and emitter. C62 permits the oscillator frequency to be slightly varied. The frequency is coupled out via a capacitive divider (C60-C61). By inserting BR1 to BR5 accordingly a temperature-compensated crystal oscillator (option) can be connected and the internal reference brought out or an external reference frequency fed to contact 4ab.

The crystal oscillator is followed by a buffer amplifier (T6 to T8) and a 10:1 divider (B11). The 1-MHz signal of this divider is brought out at ST6.19ab and at the same time applied to a 100:1 divider (B10/I and II) via a decoupler (T5). The 10-kHz output signal of the 100:1 divider is the reference frequency for the phase discriminator (B12) of the phase-locked loop of the 80-MHz oscillator.

The 80-MHz oscillator includes the FET T1 and the tuning elements GL1-GL2 (varicaps). The tuning voltage of the phase-locked loop is applied to GL1 which synchronizes the oscillator with 80 MHz. Frequency modulation is accomplished via GL2. The oscillator voltage is coupled out via C9 and boosted to TTL level via the amplifier T2, T3. The signal then passes through a frequency divider chain (B5I 2:1, B5II 2:1, B6 10:1, B7 2:1, B8 100:1) and after division from 80 MHz to 10 kHz is applied to the phase discriminator B3 for comparison. The tuning criterion (MP1) obtained by integration (integrator B13) of the pulses produced in the discriminator is such that comparison signal and reference signal at the phase discriminator are in the same phase.

Either 40 MHz or 80 MHz are required as reference at the output ST1/11ab depending on the frequency of the output signal of the set. The reference signal is either derived from the transistor T3 (80 MHz) or from the first frequency divider stage B51 (40 MHz). Switchover is effected by means of the gates B3/II and B4/IV and the switching diodes GL5 and GL6. In a lowpass filter for 40 MHz (L10 to L13-C22 to C24) and a lowpass filter for 80 MHz (L5 to L8-C18 to C20) the TTL signals are converted into sinewave signals.

The frequency modulation is produced in the 80 MHz oscillator by means of the reactance diode GL2. To prevent the modulation from being eliminated by the phase control the phase-locked loop has been slowed down by means of an RC section (R24-C12). The modulation voltage passes from ST6.13b via a switchable resistive divider to the reactance diode GL2. The frequency deviation of the oscillator is doubled by this divider if the reference frequency is 40 MHz as this passes through a 2:1 divider which also halves the frequency deviation. The deviation sensitivity is set by means of R9 (for 80 MHz) and by means of R12 (for 40 MHz). For setting the operating point of the oscillator R7 is used.

The deviation switch and the 40/80-MHz switch are controlled by logic combination of the switching signals at ST6.66 and .68 with the gates B1 and the amplifier B2.

The reference signal for the circuit board Y8 (ST6.66) is obtained by frequency division of the 1-MHz signal by means of a 2:1 divider (B14/I) and the M divider connected in cascade. This divider consists of the ICs B14 to B16. The input signal is derived from the collector of T5. The division ratio is adjustable to between 10 and 30 via the inputs ST6.13a, .15ab, .17ab with a binary signal. The output frequency thus obtained lies between 16.66 and 50 kHz.

From the reference signal for the circuit board Y8 the reference signal for the circuit board Y7 is produced (ST6.8a). It remains unchanged if the M divider setting > 19. If the M divider setting lies between 10 and 19, 2:1 frequency division will take place. For this purpose a 2:1 frequency divider is provided (B17), which is switched on or off depending on the binary triggering by a logic circuit (B18).

4.8 Divider Y2

(See circuit diagram 302.5419 S)

The divider board Y2 is connected into the RF signal path between the main oscillator (Y3) and the output stage (Y1). It comprises an RF switch, a 2:1 frequency divider and the amplitude modulator.

For output frequencies from 260 to 520 MHz the signal of the main oscillator passes directly via the RF switch to the amplitude modulator and on to the output stage Y1. For output frequencies 130 to 260 MHz the main oscillator also operates in the range 260 to 520 MHz, but its output signal is in this case taken to the modulator via the 2:1 frequency divider and then on to the output stage Y1. Either the switching diodes GL1-GL3-GL6-GL55 (without divider) or the switching diodes GL10-GL2-GL4-GL56 (with divider) of the RF switch conduct, the other group is cut off. The RF switch is controlled via B3/I and the switching stage T70-T71. The divider B1 is controlled together with the RF switch. Its supply voltage is only switched on by means of T72 if the frequency is divided.

The input signal of the divider is applied via GL10 and R11. The attenuator pad R10 to R12 match-terminates the main oscillator since input 1 of the divider exhibits a high impedance. R13 influences the response threshold and consequently the input sensitivity (to a minor degree). The divided-down output signal (contact 6 B1) is boosted by about 15 dB by means of an integrated RF amplifier (B2). Since this signal is almost rectangular an efficient suppression of harmonics is effected by means of the two cascaded lowpass filters. The upper lowpass filter as shown in the circuit diagram is effective in the range 130 to 190 MHz and the lower one in the range from 190 to 260 MHz. The lowpass filters are switched on and off by means of the switching diodes GL20-GL21 and GL40 to GL42, respectively. The switching diodes are driven from B3/III.

The RF signal passes then to the amplitude modulator, which constitutes the control element for level control and amplitude modulation. The two integrated circuits B4 and B5 each contain three PIN diodes in π -connection. The necessary attenuation is set by the control voltage which is produced in the output stage and passes via ST2.17b to the divider. The inherent attenuation of the modulator is about 8 dB. Prior to reaching the output stage Y1 via ST2.19ab the RF signal is boosted by about 15 dB in the broadband amplifier B6.

4.9 Output Stage Y1

(See circuit diagram 302.5219 S)

On the output stage board the RF signal arriving from the divider Y2 is boosted to the maximum level of 13 dBm available at the output of the set.

The signal passes via an RF switch directly to the final amplifier in the frequency range 130 to 520 MHz. The output frequency range 0.4 to 130 MHz is obtained by mixing the range 380.4 to 510 MHz with a signal of 380 MHz.

The RF detector for level measurement is provided at the output of the final amplifier. The rectified voltage is compared with a reference quantity in the control amplifier and the voltage obtained is the control voltage required for control of the amplitude modulator.

The RF switch consists of the switching diodes GL2 to GL6 for the direct path and GL1-GL8-GL9 for signal flow via the mixer.

The input signal is applied to the mixer (B1) via a lowpass filter (C2 to C7-L1-L2) and an attenuator pad (R1 to R3) connected in series. The balanced mixer is of printed circuit design. The frequency separation of the input signal and the output signal present at the same contact (3) is accomplished by means of a filter (C8 to C14-L3-L5). The mixer signal (380 MHz) is applied via BU12 and is boosted to about 14 dBm in a tuned amplifier. The trimmer C91 permits maximum suppression of non-harmonic spurious signals in the conversion range to be adjusted. The output signal of the mixer is taken to a two-stage amplifier (T1-T2) where it is boosted to about 0 dBm. The following lowpass filter with a cutoff frequency of 140 MHz is used for suppression of the mixer signal and of the nonharmonic spurious signals whose frequencies are above 140 MHz.

The RF switch and the 130-MHz amplifier are controlled from the switching stage B2/I-T5-T6. The amplifier is only connected to the operating voltage during the mixing process.

Subsequently, the RF signal passes to the two-stage final amplifier where it is again boosted by about 17 dB. On account of the required harmonic suppression (> 30 dB) the power dissipation is so high in both stages that additional cooling must be provided. This is mainly obtained through the upper PC board cover. For this reason, the stage must not be operated without the cover over a longer period of time. R36 and R51 permit the collector currents to be adjusted such as to ensure satisfactory harmonic suppression.
The detector diode GL12 connected ahead of the resistor R71, which acts as signal generator output impedance, is used for measuring the output level. The resulting rectified voltage is applied to the positive input of the control amplifier B5. By comparison with the control quantity applied to the negative input via R96 a control voltage is obtained (ST1.13b) which so adjusts the attenuation in the amplitude modulator that the rectified voltage and the control quantity are equal, i.e. the level ahead of the network R71-R73-C72 acting as output impedance is thus kept constant. The signal generator thus functions as a source with 50 Ω internal impedance.

Moreover, the detector circuit compensates for the frequency response caused by the RF attenuator and the connecting cables between the output of the final amplifier and the output of the set. L22-R60-R61 are effective between 0.4 and 20 MHz and R73-C72 between 20 and 150 MHz. The compensation in the range 200 to 500 MHz is adjustable by means of C64.

The value of the charging capacitor C66 permits the rectified voltage to follow an amplitude modulation up to about 25 kHz. At carrier frequencies below 5 MHz the capacitance of the charging capacitor is increased by connecting C67 in parallel with the switching diode GL13. In this range AM is, therefore, only possible up to 5 kHz. A switchable RC section for suppression of the carrier (R70-R72-C69-C70) is connected between the detector and the control amplifier. It prevents the residual carrier still existing after peak rectification from being fed back to the modulator which would give rise to distortions of the RF signal.

The control amplifier is fed back through a switchable RC section (R92-R93-C74-C76) which determines the loop gain and ensures a stable control effect.

Charging capacitor, carrier suppression and loop gain are together switched over and controlled via the line "AM slow" (ST1.6b). The control voltage passes from the output of the control amplifier B5 via T8 and ST1.13b to the modulator on the circuit board Y2.

The control quantity for the level control is fed in at ST1.2b via the amplifier B6. The diode GL18 compensates for the temperature effect of the detector diode. Potentiometer R101 permits adjustment of an offset which linearizes the slightly bent characteristic curve of the detector diode at low RF levels.

4.10 Modulation Control Y10

(See circuit diagram 302.7011 S)

4.10.1 Signal Flow

The signals for frequency modulation via the reactance diode (ST10.4b) and the control quantity for the level control (ST10.11a) are produced on the modulation control board Y10.

The modulation signal is either produced in the internal modulation generator or is externally applied to the modulation input 1 (ST10.19b) and passes to the input switch (B3/I and II) of the modulation attenuator where it is so attenuated by means of an attenuator with binary stepping as to obtain the desired modulation depth or frequency deviation. The output signal of the modulation attenuator is available at the same time at the level attenuator and deviation switch whose input switches are driven according to the type of modulation (B80/IV for AM and B14/II for FM).

External level control (ALC) or dual modulation is possible through the modulation input 3 (ST10.15b) which is connected to the modulation socket on the rear panel. The two outputs of the ALC amplifier drive the level attenuator and the deviation switch. In FM operation the switch B80/III is switched on connecting the ALC amplifier to the level attenuator. The RF level can be varied by about 40 dB by means of the DC voltage of between 0 and 2.8 V applied to the rear-panel modulation socket. In AM operation switch B14/III is in the on position and the ALC amplifier connected to the deviation switch. In this mode of operation φ M (phase modulation) or FM is possible via the modulation socket on the rear panel in addition to AM depending on the position of ER1 at the input of the deviation switch.

4.10.2 Circuit Description of the Individual Subassemblies

The modulation generator is a Wien-Robinson-bridge oscillator (C1-C2-R1 to R6). The oscillator frequency is switched over between 400 and 1000 Hz by means of the switching transistors T2-T3, which are driven from T1. Frequency adjustment is accomplished by means of R1 (1000 Hz) and R2 (400 Hz). Gain control takes place in the feedback path of the oscillator amplifier B1/I by means of a FET (T4) which acts as a variable resistor

whose resistance depends on the DC control voltage present at the gate. The DC control voltage is produced in a control amplifier (B1/II) by rectification (GL3-GL4) of the oscillator signal. The control voltage is adjusted such that the rectified oscillator signal is equal to the control quantity present at the positive input of the level amplifier. The temperature effect of the rectifier diode GL3 is compensated for by means of the diode GL2. The oscillator level is adjustable by means of R15. To minimize the distortions of the oscillator signal the FET T4 is fed back via C3 and R21. Moreover, the oscillator balance is set with R11 so that safe oscillation is just ensured.

The modulation attenuator contains an electronic switch (B3/I-B3/II) at its input which permits switching between internal and external modulation sources. With internal modulation the modulation signal is also available at the socket <u>31</u> on the front panel via the switch B3/III and the amplifier B4/II. The binary-stepping attenuator consists of two parallel branches which are brought together in a summing amplifier (B7/II). In the one branch the modulation values 0.5, 1, 2, 4 and 8% and kHz deviation, respectively, can be set and in the other branch the values 16, 32 and 64. The summing amplifier is followed by a level attenuator (B8/I to II-B7/II)with switch-selected division ratios of 1:1 and 10:1. If the modulation values are < 10 the division ratio 10:1 is selected and the 10-fold modulation value is set on the binary-stepping attenuator. This permits setting of modulation values from 0 to 9.95 in steps of 0.05.

The level attenuator produces the control quantity for the RF level control and the amplitude modulation. The DC voltage corresponding to the RF level is applied to the positive input of the amplifier B9/I. Level switching between CW and AM is accomplished by means of the switches E80/I and E80/II. The AC voltage corresponding to the modulation is applied to the negative input of B9/I via the switch E80/IV. ALC is possible via the switch E80/III. As a result a DC voltage develops at the output of the amplifier E9/I on which an AC voltage is superimposed in AM operation. This signal passes through a network of seven binarystepping attenuator pads. Electronic switching permits settings between 0 dB and 0.1, 0.2, 0.4, 0.8, 1.6, 3.2 or 6.4 dB. By appropriate combination the RF output level can be attenuated by between 0 and 12.7 dB in steps of 0.1 dB. The actual transfer constant of each attenuator pad is 0.5%/dB less than its nominal value. This partly compensates the slight bend of the detector diode characteristic curve occurring at low levels. The RF output level is set by means of R76 (in CW operation) and R71 (in FM operation). For setting the modulation depth R79 is used.

The deviation switch also contains an electronic switch at the input (B14/I and B14/II) which permits selection of the internal or external modulation source. When the set is driven via the modulation socket on the rear panel, PM or FM can be selected by connecting the link ER1 (plug-in type) accordingly. For PM , the value of the coupling capacitor is reduced to give a modulation frequency response rising by 6 dB/octave up to 20 kHz.

In certain frequency ranges the frequency-modulated RF signal passes through a 2:1 frequency divider and in other ranges (if fitted with the Option Frequency Range Extension) through a doubler. In this way, the frequency deviation is either halved or doubled. To avoid, however, that the deviation of the output signal of the set is affected by these internal operational states the gain is switched over correspondingly in the deviation switch. The gain of the stage Bl6/I is unity when the signal frequency is halved, 0.25 when it is doubled, and is otherwise 0.5. Furthermore, the RF signal passes through a mixer, the upper or the lower sideband being utilized depending on the frequency range. To make sure that the instantaneous frequency rises with the rising modulation voltage the phase of the modulation voltage is shifted by 0° or 180° depending on the sideband. This takes place in the amplifier Bl6/II which operates in the non-inverting mode (Bl5/II closed) or in the inverting mode (Bl5/III and Bl5/IV closed).

The control signals supplied by the microprocessor are combined by means of a logic circuit so that the electronic switches are switched on or off depending on the selected operating modes. In CW operation, the switches at the input of the modulation attenuator (B3/I and B3/II) and of the level attenuator (B80/III and B80/IV) and at the output of the deviation switch (B15/II and B15/III) are open to provide for the maximum possible crosstalk attenuation between any modulation signal present and the AM and FM output. The switches of the attenuator pads of the level and modulation attenuators are driven from a port expander (B25). The control signals from the microprocessor are successively applied to the inputs of this device and distributed to its outputs 1 to 5 and 13 to 23 where they are stored until new data arrives from the microprocessor.

4.11 Microprocessor Y11

(See circuit diagram 302.7111 S)

The circuit board Y11 comprises the microprocessor and the IEC-bus Interface. The microprocessor consists of modules of the MCS-48 family. B1 is a single chip microprocessor 8049 with 2K ROM program storage. B2 (8355) is a 2K ROM with two 8 bit I/O ports. More modules of the MCS-48 family are on other boards. One I/O expander 8243 is on each of the boards Y7, Y8 and Y10. The keyboard/display interface 8279 is on the keyboard/display unit.

The clock frequency for the 8049 is generated by an internal oscillator. The crystal Q1 is used as reference. Addressing of and data transfer to/from the ROM B2, the keyboard/display interface 8279 and the IEC bus is effected via the 8-bit bus DBO to DB7.

To address the 2K program storage locations in B2 the bits 8 to 11 of the address are output by means of the port outputs P20 to P24.

The data transfer to/from the keyboard/display interface 8279 is controlled by the control lines \overline{WR} , \overline{RD} , \overline{CS} , CD (BU27, 9, 10, 11, 12). Access to the program stores in B2 and the data output at the ports of B2 are controlled by the control lines \overline{RD} , \overline{WR} , ALE, \overline{PSEN} (8049).

For data output at the expander ports 8243 of the boards 8, 7, 10, addresses and data are output via the four port outputs P20 to P23 of the microprocessor 8049. The data transfer to/from the I/O expanders 8243 is controlled by the signals \overline{CS} 8243 8, 7, 10 (ST11 19a, 18a, 17a) and by the PROG output of the μP 8049.

The signals I, J, K, M, L, N at the port outputs P10 to P15 of the μ P 8049 have the following meaning:

		High	Low	
I	AM	On	Off	
J	FM	On	Off	
к	Modulation	Internal	External	
L	Modulation	1000 Hz	400 Hz	ŀ
М	Deviation, mod. depth	< 10 kHz, (%)	\geq 10 kHz, (%)	
N		RF off	RF on	

Table 14

The binary coded setting of the M divider is output at the port PA of B2. At port PB the BCD-coded setting of the attenuator is output.

By an INTERRUPT command the μ P 8049 is instructed to read-in data via the bus. The INTERRUPT command is issued from the keyboard/display interface 8279 on the board Y14 when data is entered from the keyboard, the data being called up from the FIFO RAM of the 8279. With remote control via the IEC bus the INTERRUPT command is issued by the IEC-bus interface B17 on the microprocessor board. The two INTERRUPT commands are combined at the input INT of the 8049 by the NAND gates B10 and B9/III-IV.

4.12 Keyboard/Display Unit Y14

(See circuit diagram 302.7911 S)

The module 8279 (B35) acts as interface between the microprocessor and the display or keyboard. The display information is written into the display RAM of the 8279. The content of the RAM is periodically output to the multiplexed displays under the automatic control of the 8279.

SLO to SL3 are the four binary coded control outputs which periodically switch over the display digits. In synchronism with the control outputs SLO to SL4 the data for the display digits are output via the data outputs AO to A3 and BO to B3. By decoding the four control outputs SLO to SL3 by means of B36 a periodic pulse - the scanning pulse for scanning the keys - is obtained. At the push of a key the scanning pulse is connected through to one of the return lines RLO to 7. The information on which of the eight return lines the key has made contact is stored in the 8279 as well as the information about the state of the SL outputs. The two pieces of information make up the position code of the key. At the push of a key the microprocessor is instructed by an INTERRUPT command to call up the position code of the key stored in the 8279.

For each push of a key the 8279 issues only one INTERRUPT command. On continuous depression of the variation keys a periodic control pulse is produced by the counter modules B21, 22, which periodically interrupts the through-connection of the outputs B36 to the return line inputs RLO to RL7 and simulates repeated operation of the key concerned.

The positive control pulse at output 1 of the gate B23/I is produced whenever the counter modules B21-22 have reached their final count. Prior to pushing one of the step keys C1 is charged. B20, B21, B22 are preset via the SET, PRESET and RESET inputs as follows. The outputs Q and \overline{Q} of B20/I and B20/II are at high and low levels, respectively. B21 is preset to the information at the data inputs, hence to 8. B22 is reset to 0. B22 operates as a fixed 10:1 divider. The control output B23/1 is at low level.

Upon pushing one of the variation keys the negative scanning pulse with a pulse repetition rate of 5 ms is connected through to Cl and the count input CK UP of B22. Cl is discharged. The SET, PRESET and RESET inputs change their logic levels. The two counters connected in cascade are preset to 80. After 80 scanning pulses, corresponding to a time of 400 ms, the counters have reached their final count. The flipflop B20/I is flipped over by the positive slope of the borrow pulses of B21. This pulse is produced whenever the counters have reached their final count. The second borrow pulse flips the Q and \overline{Q} outputs of the flipflop B20/II over to low and high, respectively. The counter B21 is, therefore, no longer preset to 8 but to 2 after the third borrow pulse. The count cycle lasts then only 20 scanning pulses corresponding to a time of 100 ms; i.e., the circuit is designed such that the first four steps occur at intervals of 400 ms while all further steps occur at intervals of 100 ms with continuous variation.

4.13 Attenuator Y16

(See circuit diagram 302.7311 S)

The attenuator is connected between the output stage and the output of the set. It permits the signal of the output stage to be attenuated by 138 dB in steps of 2 dB. Smaller level steps - as small as 0.1 dB - can be electronically achieved using the AM circuit.

The attenuator comprises nine attenuator pads of 1 dB, 2 dB, 2 x 4 dB, 10 dB, 2 x 20 dB and 2 x 40 dB (R1 to R9) as well as ten through-line sections. The attenuator pads and through-line sections are of thin-film design. They are mounted on a total of 19 ceramic chips which in turn are soldered onto a base plate. Nine contact groups each with three switching contacts protrude between them through the base plate permitting switchover from attenuation to through-connection and vice versa.

Each of these nine contact groups is operated by an actuator driven by a magnetic coil and kept in its final position by means of a permanent magnet. Force transmission between actuator and contact is ensured by means of springs, making for a uniform contact force of 20 gf. All contact surfaces are gold-plated.

The magnetic coils for attenuation switchover are driven from power gates (B1 to B5) which are partly so linked via OR gates (B6) or via the dashed lines on the motherboard 2 that BCD control is possible. The 1-dB attenuator pad is used for switching off the output of the SMS (after pressing the key RF-OFF or upon response of the Overload Protection Option). For this purpose, the last contact is omitted in the 1-dB attenuator pad. In this way, the signal path is interrupted when switching on this attenuator pad.

A survey of the control is given in table 15.

Attenuation	Attenuator pads switched on	Control lines, free plug ST21 at high level
RF OFF	_{R9} +)	1
2	R7	2
4	R6	3
8	R1, R6	4, 11
10	R3	5
20	R4	6
40	R5	7
80	R2, R5	8, 15, 16
100	R2, R5, R8	8, 9, 13, 15, 16
.)	n due to missing contact	

Table 15

The +15-V power supply of the attenuator is effected via a control circuit which is provided on the motherboard 2 and has the following functions:

The power supply of the attenuator is switched on via the relay RL1 only about 45 ms after switching on the SMS. During this time the reset process takes place in the microprocessor. The computer outputs controlling the attenuator have not yet assumed a defined state. This switching delay prevents undefined switching states at the attenuator.

About 50 ms after switching off the SMS, the +15-V terminal of the attenuator is connected to chassis. This causes all attenuator pads to assume the "attenuation" switching state independent of their previous switching states. The 1-dB attenuator pad cuts off the RF output of the SMS from the output stage which protects the SMS when it is switched off against RF power inadvertently applied to the output.

The relay control is accomplished by means of a comparator (B1), the inputs of which are connected to the operating voltages of +5 V and +20 V via delay lines.

When switching the SMS on C101 is quickly charged via GL1 while C100 is slowly charged via R4. Thus the voltage at the inverting input is for about 45 ms

higher than that at the non-inverting, the comparator output remains at "low" and the relay unexcited.

In a stationary state, the voltage at the non-inverting position is higher. The relay is excited.

When switching the SMS off, the voltage at the non-inverting input decreases at about the same pace as the operating voltages while the voltage at C101 decreases much more slowly. As soon as the operation voltages have dropped by about 200 mV - this will be the case about 50 ms after switching off the voltage at the non-inverting comparator is less than that at the inverting input. The relay is switched off.

4.14 Power Supply Y5

(See circuit diagram 302.7411 S)

The power supply produces the five regulated DC voltages of +5 V, +15 V, -15 V, +20 V and +28 V.

The -15-V voltage is produced in an integrated fixed-voltage regulator (B3). The circuits for the four other voltages are each made up of an integrated precision voltage regulator (B1-B4-B6-B8) and one or two (+5 V) power transistors (T1 to T5) as control elements for current gain. The integrated voltage regulator (type μA 723) contains an internal reference source which produces about 7.15 V at contact 6, a control amplifier (the non-inverting input at 5 and the inverting input at 4, output at 10) and a current-limiting circuit which is controlled via contact 2.

All regulated voltages are protected against sustained shortcircuit. For this purpose, the -15-V fixed voltage regulator contains a constantcurrent-limiting circuit and a protective circuit against thermal overloading. The circuits of the other four voltages contain a currentlimiting circuit which reduces the output current to a small fraction of the maximum value (foldback current limiting).

As an example, regulation and current limiting of the +15-V regulator is here described. The internal reference voltage is applied to the non-inverting input of the control amplifier of B4 via R16. Part of the output voltage is present at the inverting input of the control amplifier via the adjustable resistive divider R24 to R26. By the control process the output voltage is so adjusted that the part of the voltage tapped off the resistive divider - independent of the input voltage and the load is equal to the reference voltage.

Current limiting is controlled by a comparator (B5) the two inputs of which are connected in the diagonal arm of a bridge circuit. The two bridge arms consist of R21-R22-R27 and the load resistance. Within the range of the permissible output current load, the load exhibits such a high impedance that the voltage at the inverting comparator input is higher than at the non-inverting input. The output of the comparator has a negative potential. Current limiting does not respond. In the case of an overload the impedance of the load becomes too low, which causes the voltage at the inverting comparator input to be higher than the voltage at the non-inverting input. As a result, the comparator initiates current limiting.

The unregulated input voltage is produced in a full-wave rectifier (GL6-GL8-C8). It feeds only the series transistor T2 through which the load current flows. The voltage regulator B4, which requires a higher minimum input/output voltage difference than T2 but only very little current, is supplied with the input voltage of the +20-V regulator. Thus the power dissipation occurring in the series transistor can be minimized. In the same way, the 5-V regulator B1 is supplied with the input voltage of the 15-V regulator B6 with the input voltage of the 28-V regulator.

To prevent hum pickup the earth terminals of the four resistive dividers for setting the output voltages and of the integrated voltage regulators are connected to the PC board cassette via a separate line (BU2509.10) of the 24-pole flat cable. If the power supply is operated in open circuit, i.e. with the cable not plugged in, connection to earth is made via R12. The voltage is set by means of the potentiometers R2 (+5 V), R25 (+15 V), R36 (+20 V) and R47 (+28 V).

4.15 Overload Protection

The overload protection circuit protects the RF attenuator Y16 and the output stage Y1 against RF or DC voltages inadvertently applied to the RF output. To this end, the voltage present at the RF output is measured. If a threshold value is exceeded a contact at the output of the RF attenuator opens and interrupts the connection to the attenuator pads and to the RF output of the set.

The RF voltage is coupled out in the thin-film circuit B3 by means of a capacitive divider and after rectification by means of a diode is taken from contact 2 via R2 to the comparator B1/II. An externally applied DC voltage is brought out at contact 1/B3. Negative voltages pass via

GLI directly to the comparator. Positive voltages are inverted in BI/I. The response level is set by means of R7. The switching signal for switching off the RF output passes from the comparator output via R11, GL5 and contact 4 to the RF attenuator. This signal is at high level if the overload protection responds. The integrated circuit B2 provides delayed resetting.

4.16 <u>IEC Bus</u>

The IEC bus provides the SMS with the following interface functions in accordance with DIN IEC-625:

AH1 Acceptor Handshake

L2 Listener

RL1 Remote/Local

DC1 Device Clear

The IEC-bus interface module B17 HEF 4738 handles the IEC-bus functions directly by means of hardware while the handshake between HEF 4738 and the microprocessor is executed by means of a program via the lines rdy and dvd.

The output $0_{1 \text{ oc}}$ of the HEF 4738 is at high level if the interface is in the local state and at low level if the interface is in the remote state. The through-connection of the keyboard interrupt commands via the gate B10 is inhibited if the output $0_{1 \text{ oc}}$ is in the remote state disabling the operating controls on the front panel.

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The output 0_{dvd} of the HEF 4738 is connected through to the interrupt input of the microprocessor via the gates B9, 10. The handshake between HEF 4738 and the microprocessor for transfer of the data present at the IEC-bus data lines DIO1 to DIO8 to the microprocessor is controlled by the two lines rdy and dvd.

If the rdy line is at low level, the microprocessor signals that it is ready to accept a data byte. If the dvd line is at high level the HEF 4738 signals that a valid data byte is present at the IEC-bus data lines.

If the rdy line is at high level the microprocessor has accepted the data byte but is not yet ready to accept a new byte since the execution of the program for processing the old byte has still not been completed. The buffer B13 is connected through by the microprocessor if a data byte is to be read into the microprocessor from the IEC bus. The time sequence of the handshake is shown in Fig. 11.



Fig.11 Time sequence of the handshake.

The information of the listener addresses A1 to A5 and the standard IEC signals t_{on} , l_{on} , rsv, rtl, is read into the input J_{SR} of the HEF 3748 by means of the two shift registers B11 and B12. The shift registers read the data in parallel when 0_{red} is at high level and read them out serially when 0_{red} is at low level.

The signal rtl (return to local) goes high when the local key is pressed. The signal rsv (request for service) goes high when the overload protection responds. The SRQ line on the IEC bus is activated. The IEC-bus interface does not, however, have a polling function.

The output $0_{\rm clr}$ of the HEF 4738 is at high level if the interface is in the deas (device clear active state). The monostable B18 produces a reset pulse which restarts the microprocessor from the beginning of the program.

4.17 Reference Oscillator SMS-BI

The SMS-B1 is a plug-in unit. It is electrically connected to the reference board Y1 via four plug-in contacts A, B, C, D. If retrofitting the SMS-B1 remove the links ER1 and ER2 on the reference board Y6. =

The crystal oscillator of the SMS-B1 consists of the crystal Q1, the oscillator transistor T16 and the resonant circuit TR1, C56, C57 and C58. C57 provides for temperature compensation of the transformer TR1 in the resonant circuit. C53 permits exact frequency setting. The transistor T13 acts as impedance transformer.

To keep the oscillator transistor and the crystal at a constant temperature they are housed in an oven. The thermistor R60 is used as a temperature sensor and the transistor T15 as a heating element. The nominal temperature is determined by the factory-adjusted value of R52. The differential amplifier T10, T11 in conjunction with T12 forms the control amplifier. Current limiting of the heating transistor is accomplished via R58, GL10 and T12.

4.18 1.04-GHz Frequency-range Extension SMS-B2

Use of the 1.04-GHz Frequency-range Extension Option SMS-B2 in conjunction with the SMS extends the frequency range to 1040 MHz. The SMS-B2 contains a doubler which produces the range 520 to 1040 MHz by doubling the frequency range 260 to 520 MHz.





The overall function is shown in the block diagram (Fig. 14). The signal derived from the output stage Y1 of the basic unit passes from the input of the SMS-B2 either via the RF relay directly to the output (range up to 520 MHz) or via a diode switch to the doubler. This PC board contains an attenuator pad at the input for level adjustment. It is followed by the input filter which matches the impedance to the input resistance of the frequency doubler (approximately 25 Ω).

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The microprocessor switches the RF signal path over via pin BU1.1 as a function of the frequency entered. This is accomplished by means of the relay RS1 and the diode switches B4 and B5, which are driven by the final stage T1-T2 and the switching amplifier B1. At frequencies above 520 MHz the switching signal at BU1.1 is at high level.

The frequency doubler proper consists of the four diodes GL11I/II and GL1/II which operate as a full-wave rectifier. The two diode arms are driven in push-pull from the transformer TR1 made up of coaxial lines. For increased doubler efficiency the diodes are supplied with a quiescent DC voltage via the resistors R11 to R15 which can be distributed by means of R13 so that a high suppression of non-harmonic spurious frequencies is obtained.

The level attenuation introduced by impedance matching and the doubler is compensated by the following three-stage amplifier (T15, T25, T35) and the thin-film output amplifier. The doubled signal passes from the output amplifier via the detector and the diode switch at the output end to the RF output. The detector forms part of the level control circuit. The rectified voltage obtained with the aid of the RF rectifier diode GL5 is applied to the non-inverting input of the control amplifier B2 where it is compared with the reference value supplied by the PC board Y10 and applied to the inverting input of the control amplifier via B3. The resulting control voltage adjusts the attenuation of the amplitude modulator on the PC board Y1 and consequently the RF level present at the input of the doubler such that the rectified voltage and the reference value are equal. Hence, this control circuit is designed such that a frequency- and load-independent EMF is obtained through the resistor R1 which acts as output impedance.

The RF output level is adjusted to the same value as produced by the reference value in the basic unit by means of the potentiometer R14.

The line from ST22.4 on the motherboard connects to the microprocessor on the circuit board Y11 where its logic state is interrogated for frequency entries > 520 MHz. It is high if the SMS-B2 is fitted. The computer will then accept frequency entries up to 1040 MHz. If SMS-B2 is not fitted the line level is low and only frequency entries up to 520 MHz are accepted.

4.19 Mechanical Construction

The mechanical frame of the set is a metal cassette into which the circuit boards Y1 to Y11 are plugged. On the front of this cassette the front panel with circuit board Y14 is screwed in place, at the rear the rear panel and two panels on the sides.

The left section of the cassette is subdivided into nine compartments in which the RF circuit boards Y1 to Y8 are accommodated. It is covered up by two RF-leakage-proof covers. The circuit boards are electrically interlinked via the RF motherboard 1. They are connected to the other parts of the set via the RF filter board Y9.

In the right section of the cassette the circuit boards Y10 (modulation control) and Y11 (microprocessor) are contained. The electrical connection to the filter board and to the cables is established via the AF mother-board 2.

The rear panel carries the circuit board Y15 (regulator), the power transformer and the blower. The Frequency Range Extension Option SMS-B2 can be inserted above the blower. The RF attenuator (Y16) is screwed onto the rear wall of the cassette.

For ventilation of the set a low-voltage AC blower is used which is operated from a voltage transformer of its own. The air is sucked in through the filter on the rear panel and is let out through the perforation in the upper panelling.

Most of the circuit boards in the RF-leakage-proof section of the cassette are screened off completely to prevent crosstalk between the individual subassemblies on a circuit board. These screens can be opened after loosening the upper retaining screws by pressing the two spring covers together and pushing back the retaining clip.

5. Repair Instructions

Tolerances in this section (if not stated otherwise)

Voltages: <u>+5</u>%

Frequencies: see data sheet

5.1 Measuring Instruments Required

			· · · · · · · · · · · · · · · · · · ·	
Ref. No.	Measuring instrument required	Performance specifications	R&S instrument recommended	See section
1	RF counter	Range 0.4 to 520 MHz Resolution 10 Hz	part of FAM	5 .2. 7 5 .3.1 6
2	Power meter	Range 0.4 to 520 MHz 3 to 20 mW Z = 50 Ω, error < 0.1 dB	NRS 100.2433.92	5.2.7 5.3.12 5.3.14 5.3.17 5.3.18
3	Precision attenuator	Range 0.4 to 520 MHz O to 120 dB, Z = 50 Ω	DPVF 214,8017,52	5.2.7 5.3.19
4	Test receiver Frequency controller	Range 25 to 520 MHz Inherent noise < -10 dB/µV	ESU 2 100.1143.02 EZK 255.0010	5.2.7 5.3.19
5	RF spectrum analyzer	Range 0.4 to 1100 MHz Dynamic range > 70 dB	-	5.2.7 5.3.13
6	AF generator	Range 50 Hz to 20 kHz Output voltage > 1 V $Z_{out} = 600 \Omega$ Distortion < 0.2%	SRB 100.4094	5.3.14 5.3.15

			· · · · · · · · · · · · · · · · · · ·	
Ref. No.	Measuring instrument required	Performance specifications	R&S instrument recommended	See section
7	Test demodulator	RF range 0.4 to 520 MHz	FAM 334.2015	5.2.7 5.3.14
		AF range 50 Hz to 20 kHz		5.3.15
		AM 9 to 90%		
		FM O to 250 kHz deviation		
		Distortion $< 0.2\%$	· · ·	
8	Distortion meter	Range 50 Hz to 20 kHz	part of FAM	5.2.7
		Measurement range		5.3.14
۰.		0.1 to 10%		5.3.15
9	AF counter	Range 0.1 to 1000 kHz	part of FAM	5.2.7
-		Resolution 0.1 Hz		5.3.11
10	DC power supply	V > 5 V	NGM	5.2.7
		I > 100 mA	117.7110	5.3.17
·			or	
			NGR	
			100.5084	
11	Deviation meter	Range 0.4 to 520 MHz	FAM	5.2.7
		Inherent spurious	334.2015	5.3.20
		deviation < 1.5 Hz (CCITT)		х.
12	Psophometer	Min. input voltage	part of FAM	5.3.1
·		≈ 0.1 V with CCITT weighting filter and rms-responding rectifier		5.3.20
13	Precision extension	$Z = 50 \Omega$	SWOB-Z	5.3.21
	cable		100.3598.50	

			· · · · · · · · · · · · · · · · · · ·	
Ref. No.	Measuring instrument required	Performance specifications	R&S instrument recommended	See section
14	RF millivoltmeter	Range 1 to 520 MHz Sensitivity ≈ 100 mV	URV 216.3612	5.3.21
15	RF sweep signal generator with display	Frequency range 30 to 500 MHz Sweep width 2 to 100 MHz Dynamic range > 30 dB EMF 1 V Z = 50 Ω	Polyskop IV SWOB 289.0013	5.3.5
16	Oscilloscope	Bandwidth 100 MHz		5.2.7 5.3.11
17	Digital tester	Indication of high and low TIL level states		5.2.7
18	Service RF adapter for SMS (2)		302.8376	5.2.7
19	DC voltmeter	Measurement range 0.1 to 50 V $R_i > 10 M\Omega$ Error < 0.2%	-	5.2.7 5.3.1 5.3.10 5.3.12 5.3.15
20	AF voltmeter	Measurement range 0.1 to 10 V 50 Hz to 20 kHz Error < 0.5%	-	5.3.10 5.3.14
21	Signature analyzer			5.2.8

Table 16

Trouble Shooting

The following flow charts are intended to help determining the faulty circuit board. The figures given in the left-hand section of the boxes refer to the performance checks described in section 5.2.7, e.g. in chart 5.2.1 the "2" in the top left-hand box relating to "mixer switchover" means that this stage is covered in section 5.2.7.2. In addition the numbers of the circuit boards are given on which the error is traced with the aid of the relevant circuit diagram and the levels and switching states specified thereon. Prior to trouble shooting make sure that all operating voltages are o.k. (see section 5.3.1).

!!! Replace circuit boards only with the SMS switched off !!!

List of circuit boards

1		1	
No.	Designation	Id. No.	Colour coding
Yl	Output stage	302,5219	White - white
Y2	Divider	.5419	Green - green ,
¥3	Oscillator	.5619	Black - black
¥4	Phase control	.5819	Blue - blue
¥5	Converter	.6015	Yellow - yellow
үб	Reference	.6215	Yellow - red
¥7	50-kHz interpolation oscillator	.6415	Yellow - blue
¥8	100-Hz interpolation oscillator	.6615	Red - red
¥9	Filter	.6815	
¥10	Modulation control	.7011	Red - blue
Y11	Microprocessor	.7111	Blue - white
Y12	Motherboard 1	.5119	=
Y13	Motherboard 2	.5160	-
Y14	Keyboard/display	.7911	-
Y15	Regulator	.7711	
¥16	Attenuator	.7311	-
	Adapter	.8676	-

Table 17

5.2





5.2.3 RF Frequency Response not to Specification



5.2.4 Harmonics not Within Specification



5.2.5 Excessive AM Error or Envelope Distortion



5.2.6 Excessive FM Error or FM Distortion



5.2.7 Performance Checks

For certain checks use of a special service adapter is required in order to measure the RF output level and the frequencies of the subassemblies. This service adapter can be obtained from your nearest R&S distributor.

It is also possible to make some of these measurements up to 100 MHz without the use of this service adapter by high-impedance coupling of the frequency counter or using an oscilloscope.

5.2.7.1 Conversion Frequency

Pull out circuit board Y3. Connect frequency counter either via the oscilloscope probe to BU3/St.19ab of motherboard 1 or insert service adapter in compartment for circuit board Y3 and connect the frequency counter to the RF connector 19. Select frequencies on the SMS and check conversion frequencies according to the following table:

Frequency on the SMS	270	300	330	360	390	420	450	480	MHz
Conversion frequency	300	340	300	340	420	460	420	460	MHz

The level of the conversion frequencies is approximately 25 mV. If the counter sensitivity is inadequate use an RF preamplifier, such as OM423 Valvo.

5.2.7.2 Mixer Switchover - Circuit Board Y1

Take off cover from circuit board Y1 and plug the latter onto the adapter. Check voltage at checkpoint MP7:

Frequency on the SMS	120	130 MHz
Voltage at MP7	+14.5	-14.5 V

5.2.7.3 Mixer Switchover - Motherboard 1

Check voltage at BU1, conta	ct 17b:	
Frequency on the SMS	120	130 MHz
Nominal voltage BU1. 17b	Н	L

5.2.7.4 Reference Signals 40/80 MHz - Circuit Board Y6

Pull out circuit board Y5. Connect the frequency counter either via the oscilloscope probe to BU5/St.11ab of motherboard 1 or insert service adapter in compartment for circuit board Y5 and connect frequency counter to RF connector 11. Select frequencies on the SMS and check reference frequencies according to the following table:

Frequency on the SMS	270	300	330	360	390	420	450	480	MHz
Reference frequency	80	40	80	40	40	80	40	80	MHz

5,2.7.5 M Control Signals and M Divider - Circuit Board Y6

For this check use frequency counter with high-impedance input $(Z_{in} > 100 \text{ k}\Omega)$ which is suitable for processing TTL signals. First connect frequency counter using a high-impedance probe (e.g. oscilloscope probe) to MP7 and then to MP8 on motherboard 1. Select frequencies on the SMS and check output frequencies according to Table 18. If the output frequencies are faulty check the M control signals at BU6 of motherboard 1 by means of a TTL digital tester acc. to Table 18:

Frequency on the SMS (MHz)	Output freq Y8 (MP7)	М со 13а		sign ntact 15a		W6 17a	
280	16.6	16.6	Н	H	Н	Н	\mathbf{L}
281	17.2414	17.2414	Н	H	H	\mathbf{L}	Н
285	18.5185	18.5185	Н	Н	L	Н	H
293	21.7391	21.7391	Н	L	Н	H	Н
309	33.333	16.6667	Ŀ	Н	Н	Н	Н

Table	18
	the second s

5.2.7.6 N Divider and N Control Signals

For this check use frequency counter with high-impedance input $(Z_{in} > 100 \text{ k}\Omega)$ which is suitable for processing TTL signals. Connect via oscilloscope probe to MP9 on motherboard 1.

Select frequencies on the SMS and check output frequencies according to Table 19. If the output frequencies are faulty plug the circuit board Y7 onto the adapter and check the N control signals at the integrated circuit B13 by means of a TTL digital tester according to Table 19:

Frequency on the SMS (MHz)	Output frequency (MHz)		con 15 P7	с 14	ont	act	18			21		23 5	1
445.75	2.145833	L	H	L	L	L	L	L	L	L	L	L	L
445.775	2.147917	L	Η	L	L	L	\mathbf{L}	L	L	L	\mathbf{L}	L	H
445.8	2.15	Ĺ	Н	L	L	L	L	\mathbf{L}	Ľ	L	L	H	L
445.85	2.154167	L	Η	L	\mathbf{L}	L	L	L	\mathbf{L}	L	Η	\mathbf{L}	L
445.95	2.1625	L	Η	L	\mathbf{L}	$\mathbb{L}_{\mathbf{r}}$	L	L	L	Н	L	L	L
446.15	2.011538	L	Η	\mathbf{L}	L	L	L	Ĺ	Н	L	L	L	Ľ
446.55	2.042308	L	Н	\mathbf{L}	L	L	L	Н	L	L	L	L	\mathbb{L}
447.35	2.103846	L	Н	\mathbf{L}	Ĺ	L	H	L	${\tt L}$	L	L	L	L
448.95	2.067857	L	Η·	L	\mathbf{L}	Н	L	Ŀ	L	\mathbf{L}	L	L	L
452.165	2.010313	L	Н	L	Η	L	\mathbf{L}	\mathbf{L}	L	L	L	L	L
458.6	2.031579	L	Η	Ĥ	L	L	L	L	Ļ	L	L	\mathbf{r}	L
445.725	2.143750	L	L	Η	Η	Η	H	Н	H	H	H	Н	H

Table 19

5.2.7.7 P Divider and P Control Signals

For this check connect a frequency counter with a high-impedance input $(Z_{in} > 100 \text{ k}\Omega)$ which is suitable for processing TTL signals via the oscilloscope probe to MP10 on motherboard 1.

Select frequencies on the SMS and check output frequencies according to Table 20. If the output frequencies are faulty plug the circuit board Y8 onto the adapter and check the P control signals at the integrated circuit B11 by means of a TTL digital tester according to Table 20:

Frequency on the SMS (MHz)	Output frequency (kHz)		5 15	cc	nta	ign ct 5 17	18			21		23 '5	1
340.0048	102.4	н	Ľ	L	L	L	L	L	Ľ	L	L	L	L
340.0049	102.45	Н	\mathbf{L}	Ŀ	L	L	L	\mathbf{L}	L	L	Ŀ	\mathbf{L}	H
340.0050	102.5	H	L	L	Ŀ	Ľ	L	\mathbf{L}	L	L	$\mathbf{\Gamma}$	H	\mathbf{L}
340.0052	102.6	H	L	L	\mathbf{L}	\mathbf{r}	Ĺ	L	\mathbf{L}	L	H	L	\mathbb{L}
340.0056	102.8	Н	\mathbf{L}	L	L	L	L	L	Ľ	H	\mathbf{L}	L	L
340.0064	103.2	Н	L	\mathbb{L}	L	\mathbf{L}	\mathbf{L}	L	Η	L	L	\mathbf{L}	L
340.0080	104	H	\mathtt{L}	\mathbf{L}	$\mathbf{\Gamma}$	L	L	Η	L	\mathbf{L}	L	L	Ĺ
340.0112	105.6	H	L	\mathbf{L}	L	L	Η	\mathbb{L}	L	L	L	L	\mathbf{L}
340.0176	108.8	H	\mathbf{L}	Γ	\mathbf{L}	Η	L	L	L	L	L	L	L
340.0304	115.2	н	L	L	H	L	L	L	L	L	\mathbf{L}	L	L
320.0024	102.4	L	Ĥ	L	L.	\mathbf{L}	L	L	L	L	L	L	L
399.8727	102.3	L	L	Η	H	Ħ	Н	H	H	H	Η	Η	н

Table 20

5.2.7.8 Control Signals A to D - Circuit Board Y7

Select frequencies on the SMS and check the control signals at BU7 on motherboard 1 by means of a TTL digital tester according to Table 21:

Function	Range ide	ntification	Oscillator switchover	Divider
Control signal	А	- B	C A	D
Contact	15b	13b	бъ	8b
Frequency on the SMS (MHz)				
240 MHz				Н
270 MHz	L	L	L ·	L
300 MHz	H	L	\mathbf{L} .	
330 MHz	·L	Н	L .	
360 MHz	Н	Н	\mathbf{L} :	
390 MHz	L	L	Н	
420 MHz	Н	L	H ·	
450 MHz	L	н	H .	
480 MHz	H	· H · ·	H	
Circuit beards	¥4	<u> </u>	Y3 Y4	Y2
controlled	¥5	Y4	¥5 ¥6	¥10
	¥6		¥10	

Table 21

If the control signals are faulty successively pull out the circuit boards controlled to check whether the trouble source is one of these circuit boards or circuit board Y7.

5.2.7.9 Control Signals E to H - Circuit Board Y8

Select frequencies on the SMS and check the control signals at BU8 on motherboard 1 by means of a TTL digital tester according to Table 22:

Function Control signal Contact	Filter 2 E 15b	Mixer 3 F 13b	Doubler 4 G 6b	AM slow 5 H 8b
Frequency on the SMS (MHz) 1 81 161 241 321 521 +)	L H	H H L L L L	L L L L L H	H L L L L
Circuit boards controlled	Υ2	Y1	Y1 Y10 Range Extension Option	У1

Table 22

If the control signals are faulty successively pull out the circuit boards controlled and check whether the trouble source is one of these circuit boards or circuit board Y8.

+) Only if fitted with the Frequency Range Extension Option.

5.2.7.10 Control Quantity for Gain Control

Connect the oscilloscope to MP1 on motherboard 1 and check the voltage levels:



If the AM is distorted also check the distortion (nominal value < 0.3%) of the control quantity at MP1.

Setting on the SMS: UNMOD., level 13 dBm, frequency 130 MHz.

Set levels according to Table 23 by means of 0.1-dB key. Check the voltages at MP1 of the motherboard 1. If the voltages are faulty plug the circuit board Y10 onto the adapter and check the control signals at B25 by means of a TTL digital tester:

Level on the SMS (dBm)	Level control value Voltage at MP1 (V)	Level attenuator o	control ntact	nal B25	
(umi)	VOLCAGE AC MIL (V)	2345 1		23	22
		P4		P5	•
13.0	-2.6	LLLL	L	L	L.
12.9	-2.57	HLLL	\mathbf{L}	L	L
12.8	-2.54	LHLL .	${ m L}$	L	L
12.6	-2.48	LLHL	L	Ļ	L
12.2	-2.36	LLLH	L	L	L
11.4	-2.14	LLLL	Н	\mathbf{L}	L ·
9.8	-1.77	LLLL	L	Н	L
6.6	-1.20	LLLL	L	L	H

Table 23

5.2.7.11 Control Voltage

Connect the oscilloscope to MP3 on motherboard 1.

Setting on the SMS: UNMOD., level 3 dBm, frequency 0.4 to 520 MHz.

The control voltage should be between -2 V and -4.6 V over the entire frequency range and should be free from oscillation.

5.2.7.12 380-MHz Signal

Insert RF adapter in compartment for circuit board 1. Connect the power meter to the RF connector A. The nominal level is between 0.5 and 2 dBm.

5.2.7.13 Output Power and Harmonic Suppression - Circuit Board Y2

Insert RF adapter in compartment for circuit board 1. Connect power meter or RF spectrum analyzer to the RF connector 19. Apply DC voltage of -2 V to MP3 on motherboard 1. Check the level and the harmonic suppression in the frequency range 130 to 520 MHz:

Nominal level: -4 to +2 dBm Nominal harmonic suppression: > 34 dB.

5.2.7.14 Output Power and Harmonic Suppression - Circuit Board Y3

Insert RF adapter in compartment for circuit board Y2. Connect power meter or RF spectrum analyzer to the RF connector 4. Check the level and the harmonic suppression in the frequency range 260 to 520 MHz.

Nominal level: -2 to +4 dBm Nominal harmonic suppression: > 36 dB.

5.2.7.15 Modulator Characteristic - Circuit Board Y2

Determine modulator attenuation by measuring the modulator input level (5.2.7.14) and the output level (5.2.7.13) at the frequencies 260 to 520 MHz and with a DC voltage of 0 V, -2 V and -4 V applied to MP3 on motherboard 1.

Nominal transmission factor (dB)

DC voltage		o v	-2 V	-4 V
Frequency	260 MHz 520 MHz	+6.6 +5.7	+2.4 +1.5	-10.3 $+2$ dB -11.5
		Table 24		

Setting on the SMS: FM INT. 1 kHz, level 3 dBm, frequency 250 MHz. Set deviations according to Table 25. Check the modulation voltages at MP5 of the motherboard 1. If the voltages are faulty, plug the circuit board Y10 onto the adapter and check the control signals at B25 by means of a TTL digital tester.

Deviation on the SMS (kHz)	Modulation voltage V MP5 (V) pp	Modulation attenuator control signals at B25 contact						
		20 19 18 17	13 14 15 16					
		P6	P7					
0.5	0.025	HLLL	LLLL					
1	0.05	LHLL	LLLL					
2	0.10	LLHL	LLLL					
4	0.20	LLLH	LLLL					
8	0.40	LLLL	HLLL					
16	0.80	LLLL	LHLL					
32	1.60	LLLL	LLHL					
64	3.20	LLLL	LLLH					

Table 25

5.2.7.16 Deviation Voltage

Connect the oscilloscope to MP5 on motherboard 1.

Setting on the SMS: FM INT. 1 kHz, deviation 125 kHz, level 3 dBm, frequency 250/260 MHz.

The voltage at MP5 should be 6.2 V at 250 MHz and 3.1 V at 260 MHz. If the FM is distorted also measure distortion (nominal value < 0.3%) of deviation voltage at MP5.

5.2.7.17 Frequency Deviation - Circuit Board Y6

Insert RF adapter in compartment for circuit board Y5. Connect deviation meter to RF connector 11.

Setting on the SMS: FM INT. 1 kHz, deviation 125 kHz, level 3 dBm, frequency 135/145 MHz.

The output frequencies of the circuit board Y6 are 40/80 MHz. The deviation should be 250 kHz at both settings.

5.2.7.18 Spurious FM of 380-MHz Signal

Insert RF adapter in compartment for circuit board Y5. Connect deviation meter to RF connector A.

Setting on the SMS: UNMOD., level 3 dBm, frequency 260 MHz Nominal spurious deviation: < 2 Hz with CCITT weighting filter.

In the FM INT. 1 kHz, deviation 3 kHz mode the spurious deviation must not rise.

5.2.7.19 RF Attenuator

Performance check as described in section 3.2.2.5.

5.2.8 Signature Analysis

Measuring instrument: Signature Analyzer 5004 A from hp.

5.2.8.1 Checking the Microprocessor Y11

Setting on the 5004 A: START edge L , STOP edge J , CLOCK edge L

Make the following preparations on the circuit board Y11:

- Disconnect the shorting plug from link I and connect to link V.

- Shift the shorting plugs II and III by 90° and engage.

- Plug the START, STOP, CLOCK and GND lines of the 5004 A
- onto the four adjacent test pins ST, SP, CK and \perp

- Switch on power switch 17 (Fig. 13) of the SMS.

During the self-testing program - to distinguish it from the operating program - all digits on the displays show the figure 8 with the exception of the first digit on the output level display. Moreover, the lamp OVERFLOW <u>11</u> blinks.

The self-testing program can only be stopped by switching off the power switch.

All signatures that can be measured on the circuit board Y11 are entered in the circuit diagram, e.g. the signature 1FA4 at the output \overline{WR} of B1. The signature at the output port P10 of B1 can be measured if the shorting plug of the link V is removed whilst the self-testing program is running. When checking the signatures all ICs are left on the Y11 and all circuit boards remain inserted. If a faulty signature is discovered proceed stepwise for locating the error. First remove all peripheral modules and then insert them one after the other:

- Pull out B2, B5, B13 from the Y11.

- Pull out Y4, Y6, Y7, Y8, Y10.

- Interrupt cable connections to the keyboard/display and to the attenuator. - Check the signatures at the port, bus and control line outputs of B1.

Successively replace B2, B5, B13 and check respective signatures. After mounting B2 also check the signatures at the port outputs of B2.

Successively insert the circuit boards and connect the keyboard/display Y14 and the attenuator. Check the respective signatures. The signatures are identical irrespective of which circuit boards are inserted.

5.2.8.2 Checking the 50-kHz Interpolation Oscillator Y7, the 100-Hz Interpolation Oscillator Y8 and the Modulation Control Y10

Setting on the 5004 A: same as under section 5.2.8.1.

Make same preparations on the circuit board Y11 as under section 5.2.8.1. Connect START, STOP, CLOCK and GND lines on Y11 as under section 5.2.8.1.

The measurable signatures are entered in the circuit diagrams: input and output ports and CS and PROG. inputs of the input/output expanders 8243.
5.2.8.3 Checking the Keyboard/Display Y14

Make same preparations on the circuit board Y11 as under 5.2.8.1.

The measurable signatures are entered in the circuit diagram.

- a) Signatures at B35 DBO to DB7, RD, WR, CS, C/D and at the control lines I, J, K, L, N.
 - Setting on the 5004 A: same as under section 5.2.8.1. Connect START, STOP, CLOCK, GND lines on Y11 as under section 5.2.8.1. The signature of the control line I can be measured if the shorting link V on Y11 is interrupted.
- b) All other signatures:
 - Setting on the 5004 A: same as under section 5.2.8.1. Connect the START, STOP and CLOCK lines of the 5004 A to the adjacent test pins ST, SP and CK on Y14.
- Connect the GND line of the 5004 A to the test pin on Y11.
- c) Signatures at the outputs 8 to 15 of B37: Merely the setting on the 5004 A is different from b): START edge _ , STOP edge _ , CLOCK edge _ .

5.3 Checking and Adjusting the Basic Unit

5.3.1 Setting the Operating Voltages (Y15)

Under operational load proceed as follows:

a) Measure the voltages at the particular checkpoints on the motherboard 1 and set according to Table 26:

Voltage	Potentiometer on Y11	Checkpoint
+5.15 V +0.05 V	R2	5 V
+15 V +0.05 V	R25	15 V
+20 V +0.05 V	R36	20 V
+28 V +0.1 V	R47	28 V

Table 26

Check voltage -15 V: checkpoint "-15 V" Permissible deviation: +0.5 V

b) Check levelling and noise voltage at all operating voltages and an AC supply voltage of 198 to 242 V. The permissible deviations are given in Table 27:

Voltage	Deviation	Noise voltage (mV _{rms})			
+5.1 V	+0.01 V	< 0.4			
-15 V	+0.05 V	< 1.5			
+15 V	+0.1 V	< 1			
+20 V	+0.1 V	< 0.6			
+28 V	+0.1 V	< 1.5			

Table 27

Measure the noise voltage by means of an oscilloscope with differential inputs or by means of a millivoltmeter with floating inputs, such as the UPGR. The correct noise voltage values are only obtained if all phase control loops are locked.

c) For first-time operation of the regulator, preset the voltages in open-circuit operation (pull plug ST.25) or across a dummy load. The required current flow into the dummy load is given in Table 28:

Voltage (V)	Current (A)
-15 V	0.6
+5.15 V	3.25
+15 V	1.1
+20 V	0.7
+28 V	0.027

Table 28

5.3.2 Microprocessor Y11

For a complete check of Y11 perform steps a) and b) of the test procedures or step c).

- a) Checking by entering frequency, modulation, level. The display must be conform to the entry.
- b) Checking of control signals for the M dividers at port A of B2 on Y11 according to Table 29:

Control signal Pin B2	MO 24	M1 25	M2 26	M3 27	м4 28	
Frequency on the SMS (MHz)		-				
280	\mathbf{L}	Н	Н	Н	H	
281	H	L	Н	Н	H	
285	Н	Н	L	H	Н	
293	Н	Н	H	L	Н	
309	Н	Н	H	H.	L	

Table 29

Checking of control signals for the attenuator at port B of B2 on Y11 according to Table 30:

(UNMOD.) Control signal Pin B2	2 32	4 33	8 34	10 35	20 36	40 37	80 38	100 dB 39
Level on the SMS (dBm)								
10 dBm	H	L	Г	L	L	L	Ĺ	L
8 dBm	L	Н	L	$\cdot \mathbf{L}$	L	\mathbf{L}	L	L
4 dBm	L	L	Н	L	L	L	L	L
2 dBm	L	L	L	Н	L	L	L	L
-8 dBm	L	L	L	L	H	L	L	L
-28 dBm	L	L	L	\mathbf{L}	L	Н	L	L
-68 dBm	L	L	L	L	L	L	Н	L
-88 dBm	L.	L	L	L	L	Ľ.	Ľ	H

<u>Table 30</u>

Checking of control signals I, J, K, M, L, N at port 1 of Bl on Y11 according to Table 31:

I 27	J 28	к 29	м 30	L 31	N 32
	••• • •				
H	L	L	L	Х	L
L	Н	L	L	х	L
L	H	Н	L	L	L
L	н	H	Н	L	L
L	H	H	L	Н	L
L	L	X	Н	х	H
	27 H L L L L	27 28 H L L H L H L H L H	27 28 29 H L L L H L L H H L H H L H H	27 28 29 30 H L L L L H L L L H H L L H H H L H H H	27 28 29 30 31 H L L X L H L L X L H H L L L H H L L L H H H L L H H H

Table 31

For the following measurement Y7 must be inserted. Checking of control signals A, B, C, D at BU7 of motherboard 1 according to Table 32:

Control signal Contact	A 15b	B 13b	С 6ъ	D 8ъ
Frequency on the SMS (MHz)				
300 MHz	н	L.	\mathbf{L}	L
340 MHz	L	H	\mathbf{L}	L
390 MHz	L	\mathbf{L}	Н	L
135 MHz	L	\mathbf{L}	L	H

Ta	bl	е	32

c) Checking by signature analysis (see section 5.2.8).

5.3.3 Keyboard/Display Y14

For a functional check of Y14 proceed acc. to step a). Test procedure step b) permits error location. The microprocessor board Y11 must be inserted.

- a) Check Y14 by test entries. The test entries must cover all keys and indications. Exception: LOCAL key and REMOTE indication. Check continued switching of the variation keys if kept permanently depressed.
- b) Check by signature analysis (see section 5.2.8).

5.3.4 Reference Y6

a) 10-MHz crystal oscillator

- BR1, BR2, BR4, BR5 linked.

- Measure frequency at output REF. FREQ. 10 MHz 29 on the rear panel.

- Adjust 10 MHz +30 Hz by means of C62.

With the SMS fully equipped repeat frequency adjustment with enhanced accuracy:

- Measure frequency at RF output 20.
- Enter frequency of 520.000 MHz.
- Adjust 520.0000 MHz +250 Hz by means of C62.
- b) Optional 10-MHz reference oscillator (SMS-B1)
 - Connect high-impedance DC voltmeter to checkpoint MP1. Set minimum DC voltage by means of TR1.

Setting the zero-coefficient temperature of the crystal:

- Connect a frequency meter with a resolution of $> 10^{-8}$ to the socket REF. FREQ. 10 MHz 29 (Fig. 13).
- Insert an ammeter into the +15-V voltage supply line (the oven draws approximately 280 mA when cold). After 5 to 10 minutes, the current remains constant.
- Change the resistor R52 in steps according to the E24 progression between 100 Ω and 500 Ω . Allow for about 5 minutes after each change of resistor and read off frequency.
- Solder in resistor which gives the lowest frequency.

Frequency adjustment:

- Measure frequency at output REF.FREQ. 10 MHz 29 on the rear panel.
- Adjust 10 MHz +2 Hz by means of C53 (on reference oscillator SMS-B1).

With the SMS fully equipped repeat frequency adjustment with enhanced accuracy:

- Measure frequency at RF output 20.
- Enter frequency of 520.000 MHz.

- Adjust 520.000 MHz +5 Hz by means of C53 (on reference oscillator SMS-B1).

c) 80-MHz oscillator

- In addition to board Y6, the boards Y7 and Y11 must be inserted.
- Enter frequency of 340 MHz.
- Measure frequency at input ST5.11a,b (nominal value 80 MHz).
- By turning coil core of L2 find minimum tuning voltage at MP1. Then advance the coil core further clockwise until the tuning voltage is -8 V.

d) Harmonic distortion

In addition to board Y6, the boards Y10 and Y11 must be inserted.

- Enter frequency of 150 MHz.
- Enter 75 kHz FM INT. 1 kHz.
- Measure harmonic distortion at input ST5.11a,b.
- Adjust minimum harmonic distortion by means of R7.
 Nominal harmonic distortion < 1%.
 Repeat adjustment under b).

e) Frequency deviation

In addition to board Y6, the boards Y10 and Y11 must be inserted.

- Enter frequency of 340 MHz.
- Enter 75 kHz FM INT. 1 kHz.
- Measure frequency deviation at input ST5.11a,b.
- Adjust 75 kHz deviation by means of R9.

Checking of control signals MO to M4 (setting data for M divider) and A, C according to Table 33.

2011年1月1日日日日日日日日日日日日日日日日日日日日日日日日日日日日日日日日日	. :			•1.5	
Control signal ST6 contact	MO 17a	M1	M2 15a	M3 15b	M4 13a
Frequency on the SMS (MHz)				· · · ·	
280	L	H	Н	Ħ	H
281	H	L	Н	Н	Н
285	н	H	L	H	Η
293	н	H	H	\mathbf{L}	H
309	H	Η	H	Н	L
Control signal	A	С			
ST6 contact	бъ	8ъ			
Frequency on the SMS (MHz)					
390	L	Η			
300	H	` L			

Table 33

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5.3.5 100-Hz Interpolation Oscillator Y8

In addition to board Y8, the boards Y6 and Y11 must be inserted.

Coil adjustment:

- Enter frequency of 280.0000 MHz.
- By turning the core of I2 find minimum tuning voltage at MP1. Then turn the core further clockwise until the tuning voltage is
 - 4 V +0.1 V.
- The frequency at input ST7.11a,b is 100 kHz.

Enter frequency of 318.0152 MHz. The voltage at MP1 must be < +25 V. The frequency at input ST7.11a,b is 134.8 kHz. If the voltage at MP1 is > +25 V, there is a fault which must be investigated.

Check the output ports P5, P6, P7 of the expander port B11 according to Table 20 in section 5.2.7.7.

Check the control signals E, F, G, H of port P4 according to Table 22 in section 5.2.7.9.

Check the input port P2 and the output ports P5 to P7 as well as the inputs CS and PROG. of B11 by signature analysis (see section 5.2.8).

5.3.6 50-kHz Interpolation Oscillator Y7

a) 50-kHz interpolation oscillator

In addition to Y7, the boards Y6 and Y11 must be inserted. Adjustment of coil L6:

- Enter frequency of 297.9901 MHz.
- By turning the core of L6 find minimum tuning voltage at MP1. Then turn the core further clockwise until the tuning voltage is 5 +0.1 V.
- Enter frequency of 318.0001 MHz. The voltage at MP1 should be < +25 V.

b) Mixer oscillator

In addition to Y7, the boards Y6, Y8, Y11 must be inserted. Adjustment of coil L7:

- Enter frequency of 280.0000 MHz.
- By turning the core of L7 find minimum tuning voltage at MP2. Then turn the core further clockwise until the tuning voltage is 5 +0.1 V.

- Enter frequency of 318.0001 MHz.

- The voltage at MP2 should be +21 V < V < +25 V.

If the voltage is not within this range correct by readjusting the core.

Check the output ports P5, P6, P7 of the expander port B13 according to Table 19 in section 5.2.7.6.

Check the control signals A, B, C, D of the output port P4 according to Table 21 in section 5.2.7.8.

Check the input port P2 and the output ports P5 to P7 as well as the inputs CS and PROG. of B13 by signature analysis (see section 5.2.8).

5.3.7 Converter Y5

The circuit boards Y6, Y7, Y8 and Y11 must be inserted.

5.3.7.1 Oscillator

Insert RF adapter in the compartment for the circuit board Y1. Connect RF adapter and power meter to the RF connector A.

- a) Adjust RI such that the DC voltage at MPI is about 6.3 V. Connect spectrum analyzer and correct R1, if necessary, to obtain maximum amplitude and spectral purity.
- b) Adjust trimmer C9 so as to activate the phase control (output frequency 380 MHz). The tuning voltage at MP6 should be 16 V.
- c) By bending the coupling coil L4 set the output level to 2 to 4 dBm. Subsequently, check the tuning voltage and readjust by means of trimmer C9, if necessary.

The setting data given are valid with U-link connected on the oscillator and the circuit board screening closed.

5.3.7.2 Bandpass Filter

- a) Check the bandpass filter switching.
- Select frequencies on the SMS and check the switching voltages at MP7 and MP8 according to Table 34:

2,3

Frequency on the SMS (MHz)	Voltage (V)
	MP7	MP8
330	+14.5	-14.5
360	-14.5	-14.5
390	+14.5	+14.5
420	-14.5	+14.5
420		· · · · · · · · · · · · · · · · · · ·

Table 34

b) Insert the RF adapter in the compartment for the circuit board Y6. Connect sweep generator (SWOB IV) output to RF connector 11. Remove the mixer B11 from the circuit board Y5 and connect the socket contacts 1 and 2 with a wire link (0.4 mm dia.).

Insert the second RF adapter in the compartment for the circuit board Y4. Connect the input of the sweep generator display to RF connector 19.

c) Tune the bandpass filter (with circuit board screening closed) to resonant frequency according to Table 35:

Sweep range	Frequency setting on the SMS	Resonant frequency	Tuning elements
260 to 340	330	300	126/127
300 to 380	360	340	120/121
380 to 460	390	420	124/125
420 to 500	420	460	122/123

Table 35

d) Adjustment of bandwidth and coupling:

To adjust the bandwidth bend the coupling links between the two coils of a filter. For coupling adjustment bend the two coupling pins at the ends of the filter coils so as to obtain the attenuation between input and output given in Table 34 (RF adapters inserted in the compartments for the circuit boards Y6 and Y3). The attenuation can be directly read off the display screen of the Polyskop SWOB IV if a level reference line is superimposed prior to the adjustment. Since bandwidth and coupling adjustment are mutually interdependent, repeat the adjustment several times. Sweep width is 4 MHz.

Frequency	setting on the SMS MHz	Resonant frequency MHz	Bandwidth MHz	Attenuation dB
	330	300	1.5 +0,2 -0.15	9 <u>+</u> 0.5
1	360	340	1.6 +0.2	9 +0.5
	390	420	1.9 +0.2 -0.1	9.5 <u>+</u> 0.5
	420	460	2.1 +0.3 -0.2	10 +1 -0.5

Table 36

• · · · · · ·

The above values hold true if the circuit board screening is closed.

5.3.7.3 Checking the Output Signals

Insert circuit boards Y5 to Y8 as well as Y11 with screening closed.

a) Insert the RF adapter in the compartment for the circuit board Y1. Connect the deviation meter (frequency 380 MHz) to RF connector A.

The spurious deviation of the 380-MHz signal should be < 1.9 Hz (according to CCITT, rms weighting) < 8 Hz (50 to 15 kHz, quasi-peak weighting).

 b) Insert the RF adapter in the compartment for the circuit board Y3.
 Connect the power meter to RF connector 19. Select frequencies on the SMS and check the level according to the table below.

Frequency on the SMS	Level
330 360 390 420	}-18 to -22 dBm

5.3.8 Oscillator Y3

- a) Setting of operating point and coil adjustment:
 - In addition to circuit board Y3, the circuit boards Y7 and Y11 must be inserted.
 - Interrupt link BRI in the tuning voltage line and apply +18.5 V to the oscillator end.
 - Enter 300 MHz (oscillator switchover C = low ST3, 2b).
 - Adjust DC voltage between MP2 and MP3 to 8.5 to 8.6 V by means of potentiometer R2.
 - Measure frequency at the RF input ST2.4a,b or at RF output 20 (Fig. 12) if the SMS is fully equipped.
 - Adjust 380 +1 MHz by correcting the spacing of the windings of L6.
 - Enter 400 MHz (oscillator switchover C = high ST3, 2b).
 - Adjust DC voltage between MP1 and MP2 to 8.5 to 8.6 V by means of potentiometer R1.
 - Adjust 520 +1 MHz by correcting the spacing of the windings of L5.
 - Reestablish link connection (BRI) in the tuning voltage line.

For the following checks the circuit boards Y4, Y5, Y6, Y7, Y8, Y11 must be inserted in addition to Y3.

b) Level adjustment:

Measure level at the RF input ST2.4a,b.

Vary frequency by means of keys 3.

The RF level should be between -2 dBm and +4 dBm over the frequency range 260 to 520 MHz.

The RF level can be adjusted by correcting the spacing of the coils L6, L8 and L5, L7. This is accomplished by bending L8 and L7, respectively.

- c) Enter 260 MHz and 380 MHz and check timing voltage. The tuning voltage should be > +2.4 V.
- d) Check the buildup and spectral purity of the RF signal at the RF input ST2.4a,b in the frequency range 260 to 520 MHz by means of a spectrum analyzer. Check harmonic distortion (suppression > 36 dB). Adjust R1/R2, if necessary.
- e) Measure level at the RF input 20 to 60 MHz (ST4.19a,b). Vary frequency by means of keys 3 over the frequency range 260 to 520 MHz. The signal level should be > -14 dBm.

5.3.9 Phase Control Y4

In addition to Y4 the circuit boards Y3, Y5, Y6, Y7, Y8, Y11 must be inserted.

a) Adjustment of fixed tuning voltages:

Enter the following eight frequencies and adjust voltage at MP5 to 0 ± 0.2 V by means of the respective potentiometers. For example, enter 270 MHz, vary potentiometer R118 until the voltage at MP5 is 0 V, etc.

Frequency on the SMS	Potentiometer
270 MHz	R 118
300 MHz	R 119
340 MHz	R 120
370 MHz	R 121
390 MHz	R 122
420 MHz	R 123
460 MHz	R 124
500 MHz	R 125
<u> </u>	

Table 37

b) Adjustment of spurious FM of reference signal:
 Enter frequency of 260 MHz. Connect a spectrum analyzer to the RF input ST2.4a,b or, if all circuit boards are inserted, to the RF output <u>20</u>.

Adjust suppression of the modulation lines 2 MHz and 4 MHz to ≥ 75 dB below the signal by means of potentiometer R83. If no modulation lines are discernible, set R83 to mid-position.

c) Gain switching:

Interrupt the link BR1. Feed in signal (10 kHz, 1 V $_{pp}$) at test pin of link connected to R92.

Enter frequencies and check voltages at MP6 according to Table 38.

Frequencies on the SMS	v (v _{pp})
279 MHz	0.14
319	0.14
321	0.20
361	0.27
399	0.13
439	0.13
441	0.16
481	0.22
500	0.42
520	0.62

Table 38

d) Lock-in aid:

- Pull out circuit board Y3.
- Enter frequencies 270 MHz and 340 MHz.
- Measure signal at MP5 according to diagrams below.



e) Enter frequencies and check logic levels at contacts ST4 according to Tables 39 and 40.

Control signal ST4 contact	А ба	B 8a	С 8ъ	
Frequency on the SMS				
270 MHz	L	\mathbf{L}	L	н
300 MHz	н	L	\mathbf{L}	
340 MHz	L	Н	L	
390 MHz	L	L	H	:
	ł			

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Table 39

Control signal ST4 contact	MO 17a	M1 17b	M2 15a	M3 15b	M4 13a
Frequency on the SMS					
280 MHz	L	Н	Н	Н	Н
281 MHz	Н	L	H	H	Н
285 MHz	Н	H	${ m L}$	Н	Н
293 MHz	H	Н	Η	${ m L}$	Н
309 MHz	H	Н	Η	Н	L

Table 40

5.3.10 Checking the Modulation Control Y10

The circuit board Y11 must be inserted.

- a) Setting on the SMS: FM EXT. deviation 100 kHz, frequency 250 MHz.
 Connect an AF voltmeter to MP15/Y10.
 Apply a voltage of approximately 0.57 V/1 kHz to the modulation socket on the front panel so that the AF voltmeter reads 1.00 V.
- b) Adjust deviation and check voltage on the AF voltmeter according to the following table:

Deviation (kHz)	9	16	16.5	17	18	20	24	32	64	· · ·
Voltage (V)	0.09	0.16	0.165	0.17	018	0.20	0.24	0.33	0.64	<u>+</u> 1%

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- c) Setting on the SMS: FM EXT. deviation 100 kHz, frequency 260/520 MHz. The AC voltage at MP15 should be 0.50 V.
- d) Setting on the SMS: UNMOD. level 13 dBm, frequency 130 MHz. Adjust voltage at MP14 to 2.50 V by means of R76.
- e) Reduce the level by means of 0.1-dB key and check voltage at MP14 according to the following table:

Level (dBm)	12.9	12.8	12.6	12.2	11.4	.9.8.	6.6	
Voltage (V)	2.47	2.44	2.38	2.27	2.06	1.70	1.15	+1%

5.3.11 Adjusting the Modulation Generator Y10

- a) Setting on the SMS: AM INT. 1 kHz.
- b) Connect the frequency counter to the modulation socket 23 (Fig. 13). Check generator oscillation at MP1 by means of oscilloscope.
- c) Vary resistor R12 until the generator oscillates.
- d) Adjust frequency to 1000 Hz +3 Hz by means of resistor R1.
- e) Setting on the SMS: AM INT. 400 Hz. Adjust the frequency to 400 Hz +2 Hz by means of R2.
- f) Connect the oscilloscope to MP2. Vary R12 until the lower of the two voltages is 80 mV when switching between 400 and 1000 Hz.

5.3.12 Adjusting the RF Level - Y10 and Y2

The circuit boards Y2 to Y8, Y10 and Y11 must be inserted.

a) Setting the operating points of the transistors T3 and T4 on the circuit board Y1:

Plug the circuit board Y1 onto the adapter.

Adjust the voltage at MP9 to 0.7 V by means of R36 and the voltage at MP10 to 1.3 V by means of R51. (Switch on SMS only for a short time since there is no heat dissipation for T3 and T4 when the cover is open.) After adjustment replace screening cover, screw shut and insert circuit board again into the cassette without the adapter.

- b) Setting on the SMS: UNMOD. level 13 dBm, frequency 130 MHz. Set the level to 13.00 dBm by means of R76/Y10.
- c) Setting on the SMS: FM EXT. level 13 dBm, frequency 130 MHz. Set the level to 13.00 dBm by means of R71/Y10.
- d) Adjust frequency to 520 MHz. Set the level to 13.05 dBm by means of C64/Y1.
- e) Checking the control voltage: Setting on the SMS: UNMOD. level 3 dBm.
 Connect the oscilloscope to MP3 on the motherboard 1. The control voltage should be between -2 V and -4.6 V in the frequency range 0.4 to 520 MHz. If the control voltage falls below -2 V check the output power on the circuit board Y3 (section 5.2.7.14) and the modulation characteristic on the circuit board Y2 (section 5.2.7.15).

5.3.13 Adjusting the Suppression of Harmonics and Non-harmonic Spurious Signals

All circuit boards must be inserted.

a) Connect the RF spectrum analyzer to the RF output of the SMS.

b) Setting on the SMS: UNMOD., level 3 dBm.

Adjust maximum harmonic suppression in the frequency range 250 to 270 MHz by means of R51/Y1. Check harmonic suppression over the entire frequency range from 0.4 to 520 MHz. The typical value is > 36 dB.

c) Adjust maximum suppression of non-harmonic spurious signals 380 MHz - 2 f_{SMS} and

380~MHz - 3 $f_{\mbox{SMS}}$ in the frequency range 100 to 129 MHz by means of C91/Y1.

Check suppression of non-harmonic spurious signals over the entire frequency range from 0.4 to 520 MHz,

Typical	values:	0.4	to	105	MHz	>	70	dB
	·	110	to	129	MHz	>	65	dB
		130	to	520	MHz	>	75	dB.

To adjust C91 unscrew the left-hand guide and push down until a hole is visible in the cassette through which C91 is accessible.

5.3.14 Adjusting the Amplitude Modulation

All circuit boards must be inserted.

- a) Setting on the SMS: AM EXT., m = 85%, level 7 dBm, frequency 130 MHz.
 Apply external modulation voltage of 1.00 V/1 kHz to the modulation socket 23 (Fig. 12). Connect test demodulator to RF output of the SMS (test setup see section 3.2.2.11).
- b) Set m = 85% by means of R79/Y10.
- c) Set level on SMS to 5.1 dBm and reduce to -4.9 dBm by means of 0.1-dB key. Set m = 85% by means of R101/Y1.
- d) Repeat steps a) to c).
- e) Setting on the SMS: AM INT. 1000 Hz, m = 85%, level 7 dBm, 130 MHz. Set m = 85% by means of R15/Y10.
- f) Set SMS to AM INT. 400 Hz. Check modulation depth (85%). In the case of a deviation between 1000 Hz INT. and 400 Hz INT. correct by means of R15/Y10.
- g) Setting on the SMS: UNMOD., level 13 dBm, frequency 130 MHz. Connect power meter to the RF output of the SMS and check the level. Correct by means of R76/Y10, if necessary.
- h) Setting on the SMS: AM EXT., m = 0, level 5.1 dBm, frequency 130 MHz.
 Reduce level to -4.9 dBm by means of the 0.1-dB key. Check the level.
 Permissible deviation +0.3 dB. If the level accidentally drops below
 -4.9 dBm readjust level to 5.1 dBm and reduce again to -4.9 dBm.

In the case of greater deviations, adjust R102/Y1 and repeat steps a), b) and g).

- Setting on the SMS: AM INT. 400 and 1000 Hz.
 Connect the AF voltmeter to the modulation socket 23 and measure the modulation voltage.
 Nominal value: 0.95 to 1.05 V.
- j) Check AM error and envelope distortion according to sections 3.2.2.12 and 3.2.2.13, respectively.

5.3.15 Adjusting the Frequency Modulation

- a) Setting on the SMS: FM EXT., deviation 75 kHz, level 3 dBm, frequency 390 MHz. Test setup according to section 3.2.2.11. Set modulation voltage (1 kHz) so as to obtain 75 kHz deviation.
- b) Adjust R7/Y6 for minimum distortion.
 Correct tuning voltage of circuit board Y6 according to section 5.3.4 b).
- c) Setting on the SMS: FM EXT., deviation 100 kHz, level 3 dBm, frequency 500 MHz. Apply external modulation voltage of 1.00 V/1 kHz. Adjust R9/Y5 for 100 kHz deviation.
- d) Set the frequency of the SMS to 460 MHz. Adjust R12/Y6 for 100 kHz deviation.
- e) Check the FM error and FM distortion according to sections 3.2.2.10 and 3.2.2.11.

5.3.16 Adjusting the Doubler of the 1.04-GHz Frequency-range Extension Option SMS-B2

a) Setting on the SMS: UNMOD., level 7 dBm, frequency 750 MHz.
Set RF level to 7 dBm by means of the potentiometer R14 on the circuit board 335.0368 (switch). Measure level at RF output of the signal generator using a power meter. The frequency response of the output level between 520 and 1040 MHz should be flat within +1 dB. For frequencies
> 900 MHz, the frequency response can be adjusted by means of trimmer C20 on 335.0368. The level voltage at checkpoint MP3 of the motherboard 1 should be between -2 V and -4.8 V and must not have any oscillations.

b) Setting on the SMS: a.) UNMOD., level 13 dBm

b.) AM INT. 1 kHz, level 7 dBm, m = 0%.

For the frequencies 520.0001 to 1040 MHz adjust the suppression of nonharmonic spurious signals to > 20 dB by means of potentiometer R13 on the circuit board 335.0316 (doubler).

c) Setting on the SMS: AM EXT. 60%, level 7 dBm.

Feed modulation signal of 1.0 V to modulation socket. Adjust potentiometer R23 on the circuit board 335.0368 (switch) such that the modulation depth for the modulation frequency range 20 Hz to 20 kHz fluctuates less than +5%.

5.3.17 Adjusting the Response Threshold of the Overload Protection

Settings on the SMS: UNMOD., level -27 dBm, frequency 130 MHz.

Test setup 1:

Feed a frequency between 25 and 1000 MHz to the RF output from a power signal generator, such as the SMLU, with an RF power output between 0 and 2 W.

Adjustment:

Vary the potentiometer R7 until the overload protection responds with a power input P = 0.5 W. R7 is on board 335.0716 overload protection, which is connected at the output of the RF attenuator.

Test setup 2:

Apply a DC voltage of 0 to 6 V to the RF output of the SMS (any polarity).

Test:

Increase the DC voltage starting from 0 V. At > 1 V, the overload protection should respond. If so, the LED of the RF OFF key <u>19</u> lights.

5.3.18 Checking the Frequency Setting

Make check as described in section 3.2.2.2.

5.3.19 Checking the Modulation Input on the Rear Panel

Make check as described in sections 3.2.2.16 (ALC) and 3.2.2.14 (phase modulation), respectively.

5.3.20 Checking the RF Response

The frequency response is preset under section 5.3.4. After level and modulation adjustments check the frequency response (section 3.2.2.3) and correct, if necessary, by means of C64/Y1.

5.3.21 Checking the RF Attenuator Error

Make check as described in section 3.2.2.5.

5.3.2² Checking the Spurious Deviation

Make check as described in section 3.2.2.15.

5.3.23 Checking the VSWR

Make check as described in section 3.2.2.17.

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5.4 Retrofitting of Options

Prior to mounting an option pull out power plug. Exchange circuit boards only with the SMS switched off.

5.4.1 Mounting the Reference Oscillator Option SMS-B1

Remove lower cabinet panelling and open lower cover of cassette. Pull out the reference circuit board Y6 (302.6215; yellow/red colour coding). Unplug the two links ER1 and ER2 on the circuit board. Insert the SMS-B1 and screw in place by means of the three screws supplied with it. Replace circuit board Y6 in the cassette and close cassette and cabinet again.

5.4.2 Mounting the 1.04-GHz Frequency-range Extension Option SMS-B2

Space for accommodation of this option is reserved in the lower left-hand corner of the signal generator.

Preparations:

- Remove upper and lower cabinet panelling.
- Remove left side strip.
- Remove cover plate on the rear wall above the air filter.
- Remove RF cable K2 between ST15 of the cassette and the attenuator.

Mounting the option:

Insert the option in the lower left-hand corner of the signal generator with the RF connectors pointing upwards and loosely fix to the side wall with 4 screws M2,5x8. Screw the heat sink to the rear wall with 2 screws M2,5x8 and to the option with 2 screws M2,5x6. Then tighten down the four screws on the side wall.

Running of the cables supplied with the option:

- Run the RF cable K2 between ST15 of the cassette and ST2 of the option.
- Run the RF cable K3 between ST3 of the option and RF terminal of the attenuator.
- Connect the narrow flat cable K22 to ST6 of the option and ST22 of the motherboard 2 (for location of connector and pin numbers see Components Location Plan 302.5160, Bl. 2).
- Connect the broad flat cable K30 to BU7 of the option (same direction as K22) and BU30 of the power supply (for location of socket and laying of the cable see Components Location Plan 302.7711).

The subsequent incorporation of the option causes a reduction of the RF output level by approx. 0.8 dB.

It is therefore necessary to make an adjustment on the "Modulation Control" board (302.7011, ¥10).

- a) Level correction
 Set SMS to UNMOD., and 3 dBm
 Adjust the output power to 3 dBm using potentiometer R76/Y10.
 Average-out frequency response variations between 400 kHz and 520 MHz.
- b) Modulation-depth correction
 Set SMS to 130 MHz, 80% AM INT. 1 kHz, 3 dBm
 Adjust the modulation depth of the RF output signal to 80% using potentiometer R79/Y10.
 Average-out frequency response variations between 400 kHz and 520 MHz.
- Shift the cover plate on the front panel such that the extended frequency range, i.e. "0.4 1040 MHz" appears in the window.
- Cover up signal generator again.

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Bilder Figures



Fig. 13 Frontansicht Fig. 13 Frontansicht



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Schaltteillisten numerisch geordnet Parts lists in numerical order

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S M S Ertsatzteile (Ergänzung)

 IEC-Bus Schraube M 2,5 (SMS, SMFP)
 336.7272.00

 Luftfilter SMS
 302.7534.00

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c96	DRALORIC EDPU3X4/560/10282 CC 560PF+-1023X4R2000	2000 cc 087.7002	302.5119	C15		D 1,50UH3UX022CHN0,560A	' LD 067.2886	302-6815
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6L 1	AD 1N4448 SI 75V 150MIA Valvo 1N4448	AD 012.0700	302_5160	Vortali Societi Mitio	u ~ 1 ⊔ _ 1 □ _ 1	T 500H10%0,220HM0,560A	LD 067.2886	302 . 6815
6L2	AD 1N4448 SI 75V 150MIA Valvo 18448	AP 012-0700	302.5160	M N J J J J J J J J J J J J J J J J J J		500H10X0,220HK0,5	Ł. 0 067 _ 2886	302-6815
6L3	AD 1N4448 SI 75V 150NIA Valvo 1N4448	A0 012-0700	302.5160	L L L L L L L L L L L L L L L L L L L	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	ацекам риозаец тидэ-са р 1,500410%0,220400,560A егеман выссст али за	1.0 067.2886	302.6815
5 X X	HF+KABEL HF-KABEL	302-8618	(-	a School of C		LD 10004+~20% 1A 0,65004M SIEMENS B82111~E-C25	LD 155.9446	302-5160
∾ 4 × ¥	HF-KABEL Kabel	302-8630 302-8630		intecha guitechau de	니 다 산 산	Rt 0,125W2370HA++1%TK50	RL 086.2065	302 . 7357
K5 X1C	.KABEL HF-KABEL	302 4593	•	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2		RF 069.4728	302.5160
к 1 2	1	302.7392		2 2 2	2 4 4 C	ИКАЦИКТИ - БОХИСИГ/ТНОХ4,/Ж RF 0,25И120 ОНМ +-5% DD&LODIC - 1//0007/-5%377	RF 069.1212	
5	LD 1,50UH1020,220H00,560A DELEVAN DROSSEL 1025-24	LD U67.2886	302.6815) .		CHX ++5% 0#% ++5% cv0707×+-6%	RF 069.1812	302-5160
r.2	LD 1_500M10X0,220MM0,560A DELEVAN DR05SEL 1025-24	LD .067.2886	302.6815	R2 T2	د <u>ا</u>	90484-12753 90484-127K50 #26737 64 000044	د ــــــــــــــــــــــــــــــــــــ	3 02 73 57
5	L0 1,50UM1020,220HM0,560A DELEVAN DROSSEL 1025-24	LD 067.2886	302.6815	r r	ມ (ສ) ຊີ ແມ່ ຊີ ແມ່	~ 7K0HM *=5%	RF 069_4728	302_5160
47	LP 1,50UH1020,220HM0,560A DELEVAN DR055EL 1025-24	LD 057.2886	3026815		4 4 2 4 4 0 22 4	NALORIC LCAUZU//+-5%4,2% NF 0,25W 47%0HM +-5% 	RF 069.4734	302 . 5160
۲5	LD 1,500H10X0,220HM0,560A DELEVAN DR055EL 1025-24	to 067.2886	3 02 . 68 15	R\$1		НАЕЦИТС ССАОСИ//+→5%4/К 5N 12V 1XU RH #ONOSTABIL 500057481L	SN 063.7083	302_5160
٤ó	LD 1,50UH1020,220HM0,560A	LD 067.2886	302-6815		202 	N71SH		
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L8	LD 1UH BEI 3 A 0,017 0HM Siemens B87111-A-C1	LD 026_4510	302.4815		102 COS	R 220 V Imel. 72,50 DIN 41571		
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R ø Blatt Nr. - ENDE enthalten In 302.4012.01 SA Sachnummer Sachnummer SMS SIGNAL GENERATOR HIERZU 302.7311 S Fuer var 02.03,12,15,16, 22,24,25,32,33 Schattleiliste für Benennung / Beschreibung 22 0282 AZ , Datum **ROHDE & SCHWARZ** MÛNCHEN PF 098.0028 0679 ¢ Kennzeichen ະເສດ ແລະ ເວັ້ນ ແລະ ເຈັ້ນ ແລະ ເຈັ້ນ ແລະ ເຈັ້ນ ແລະ ເຈັ້ນ ເປັນ ແລະ ເຈັ້ນ ແລະ ເຈັ້ນ ແລະ ເຈັ້ນ ແລະ ເຈັ້ນ ແລະ ເຈັ້ນ ແ ແລະ ແລະ ເຈັ້ນ ແລະ ເຈັ ແລະ ເຈັ້ນ ແ oppoyogun Yoo ascrift O () 1× Blatt Nr 302-5160 302-5160 302-5160 302.5119 3 02 - 5119 302.5119 302.7357 enthaiten In 302.5119 302.4012.01 SA Sachnummer 302_6615 302_7011_22 302.7111.22 1147-205 302.7311 302-6215 302 . 6415 302.7911 302 . 5190 302 . 5183 302_5619 302_5819 302-6015 302-5419 302.5219 010-51.63 FJ 063.5168 FJ 249.9684 FJ 249-9684 FJ 249.9684 FJ 063-5168 Sachnummer ¥K SMS SIGNAL GENERATOR FUER VAR 02,03,04,12,25,32 Tastatur/anzeige-einheit Hierzu 302,7911 S FUER VAR 15,16,22,24,33 Mikroprozessor 302,7111.52 ED AUSGANGSSTUFE FUER VAR 02,03,04,15,16, 22,32,33 Alerzu 302,5219 S Ausgangsstufe 302,6550 Fuer Var 12,24,25 Ever Var 12,24,25 Ever Var 12,24,25 HIERZU 302.6215 S ED INTERPOLATIONSOS.50KHZ HIERZU 302.6415 S ED INTERPOL-052. 100 HZ ED MODULATIONSSTEUERUNG HIERZU 302.7011 S BAUGR EICHLEITUNG F SMS RADIALL FJ EINBAUSTECKER SYST_SAB •UER VAR 02,12,15,16,22, 24,25,32,33 HIERZU 302.7411 S Fuer var 02.12.15,16,22, EINBAUSTECKER SYST_SMB AK BCY591X NPN 45V 200MA Siemens bcy591X FJ EINBAUWINKELST. SMC Radiall R 112 669 FJ Einbauwinkelst. SMC Radiall R 112 669 FJ Einbauwinkelst. SMC Radiall R 112 669 Schelttellfiste für Beneinung / Beschreibung NETTETL 302.7428 FUER VAR 03.04 NETZTETL 302.7434 FUER VAR 32,33 HIERZU 302_5819 S ED UMSETZER HIERZU 302_6015 S ED REFERENZ HIERZU 302.7111 S HIERZU 302_7111 S ED TEILER Hierzu 302_5419 S Ed 0szillator HIERZU 302 "5619 S ED PHASENREGELUNG MIKROPROZ ESSOR **STECKEREINHEIT** STE CKERE INHE IT 22 0282 AZ, Datum 4ETZTEIL RADIALL ROHDE & SCHWARZ MÜNCHEN 2 Ŷ Kennzeichen PF 005 0028-0079 TI. ¥14 Y16 S115 ST17 ST21 ST25 Υ8 Υ10 ST13 ST16 Y15 ST 14 111 77 74 5 ۲6 23 ΥЗ ۲, រាល់ទីលេខា ដែលសីលាខា unadra Urbedra I

 \sim Blatt Nr. ontheiten in š Sachnumme 302.5219 CE 022.8104 cc 082.1712 022.8156 CE 022_8156 CC 082 .7712 CC 083.6747 CC 082.1712 CC 087.7102 CC 060.1149 CC 083.6718 CE 022.8156 022 .7572 CE 022.8156 CC 082_1712 022.8104 CC 060.1149 CC 087.6964 CC 087_6241 CC 082.1712 CC 022.0255 CC 022.0255 022.0255 CC 087_6964 CC 087.6964 CC 082.1712 087.6506 CC 087_6335 CC 087_6270 Sachnummer ш С ы С CE с С 2 CC 4 & 7NF +-1025X9R2000 C DRALORIC EDPU6X9/4700/10R2000 TEFK7, 1500/2080E9000 TEFK7,1500/2080E9000 STETTNER TEFK7,1500/2080E9000 CE 2,2UF+-20220V 5X 4X 7 (ER0-TANTAL TA-ELK0ETR~2,2/20 TEFK7,1500/2080E9000 ALORIC EDPU3X4/5,9/0,25P100 T_5NF-20+802810000TRAP C ETTNER TEFK7_1500/2080E9000 1_5NF-20+802810000TRAP C YEFK7,1500/2080E9000 20 CE 100NF+-20235V 5X 4X 7 ERO-TANTAL TA-ELKOETR1-0,1/35 CE 2,2UF+-20220V 5X 4X 7 ER0-TANTAL TA-ELKOETR1-0,1/35 EC 1,5NF-20+802810060TRAP ER0-TANTAL TA-ELKOETR1-0,1/35 CE 22UF -10+100%40V 9X13 ERO-YANTAL TA-ELKOETR-2,2/20 7 EFK7/33/10%N1500 ERO-TANTAL YA-ELKOETR1-0,1/35 EDPU6X9/330/2%N750 EDPU6X9/330/2XN75 EDPU4X5/6,8/0,25P1 EBPU6X9/330/2XN750 837292-85821-2002 837292-85821-2002 837292-85821-2002 EDPU6X9/22/2XP100 ED AUSGANGSSTUFE TEFK7/1810XN750 CC 1,5NF-20+80XR10000TRAP EDPUSX614712X1NP CC 1,5NF-20+80%R10000TRAP CC 100NF+-10X100V K1200VI CC 1,5NF-20+802R10000TRAP CC 820PF+ 50-20XHDK4000TR B4136-B7226-2 CC 820PF+ 50-20%HDK4000TR CE 100NF+-20X35V 5X 4X 7 CE 100NF+-20%35V 5X 4X 7 CC 820PF+ 50-20%HD%4000TR CE 100NF +- 20%35V 5% 4% 7 CC 33PF+-10X N1500 TRAP CKR068X104KL CC 3,99F+-0,25PF4X5P100 CKR06BX 104KL CC 6,8PF+-0,25PF4X5P100 CC 18PF+-10X N750 TRAP Schalttoliliste für Benennung / Beschreibung CC 330PF+-2%6X9N750 CC &7PF+-ZX5X6NP0 DRALORIC EDPU5X6/4 CC 22PF+-2X6X9P100 DRALORIC EDPU6X9/2 CC 330PF+-2%6X9N750 CC 330PF+-2%6%9N750 STETINER STETTHER STETTNER STETTNER 13 0282 STETINER Datum DRALORIC STETTNER DRALORIC DRALORIC DRALORIC A EROVOX SIEMENS DRALORIC STETTNER DRALORIC AEROVOX SIEMENS SIEMENS SIEMENS Ř 3 υ Ω ROHDE & SCHWERZ MONCAEN Keenzelchen T c59 c56 C5.7 C58 5. C 5 5 C 4 7 C & 8 c50 C51 0.53 C 4 5 040 542 C.13 C 4 4 C37 643 c.35 636 033 C 3 4 c29 č30 C31 C 3 2 C 2 8 C27 ນັກຈຽເປັນສະຫາ ໄປ ອຽຣໂານເຕັນ ຈວດເຜີ ອີດແຫ່ລາກທີ່ຢູ່ ເວັດຫັດຈາດຈັກ ລາວູນເອດດາ ໄດ້ນັກຂ້າຍກ່ອງຄະນາຍ

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(EDPU4X5/6,8/0,25P300 TEFK7_4_7PF/0_5PF/NP TRE7LOE, 150/1028700 E DPU6X9/22/2XP100 EDPU6X9/330/2XN75D EDPU6X9/18/2%P100 EDPU4X5/27/2X/NP0 CE 1,00F+-20235V 5X 4X 7 Ero-tantal ta-elkoetr1-1/35 DRALORIC EDPU6X9/22/2XP100 ERO-TANTAL TA-ELKOETR1-1/35 B38280~A5020-D702 CE 1,00F++ 20235V 5X 4X 7 ER0-TANTAL TA-ELKOETR1-1/35 CE 1,00F+-20235V 5X 4X 7 ERO-TANTAL TA-ELKOETR1-1/35 ED AUSGANGSSTUFE TEFK7/12102N470 CC 150PF+-10X R700 TRAP CE 1,0UF+-20%35V 5% 4% 7 TEXAS UA741MJG Fj Einbaubuchse syst. Smb Radiall 115 556 2,7PF+-0,5PF P100TRAP BL CD4066BE 4/ANALOGSWITC 4.7PF+-0.5PF7NPO TRAP CC: 6_8PF+-0_25PF4X5P100 5082-9200 MIXER-1GHZ LF256H BIFET-OP.AMP. CC 12PF+-10% N470 TRAP 4 ~7NF+-10X6X9R2000 CC. 4 _7NF+-10%6X9R2000 6M 5082-9200 MIXER-16H2 HEULETT-P. 5082-9200 B0 MC15586 DUAL-0P-AMP HOTOROLA MC15586 BO TLD44MJ 4X0P-AMPL-Schallteilistu für Benennung / Beschreibung 22PF+-2X6X9P100 BO UA741MJG OP-AMP. 330PF+-2X6X9N750 18PF+-226X9P100 CC 22PF+-2X6X9P100 C 040668E 47PF+-2X5X6NP0 CC 27PF+-2%4X5NP0 1104491 LFZ56H DRALORIC DRALORIC STETINER DRALORIC 13 0282 DRALORIC DRALORIC DRALORIC Datum STETTNER DRALORIC DRALORIC DRALORIC SIEMENS TEXAS ¥ 2 2 3 RCA NSC ပ္ပ 2 3 g 80 20 ROHDE & SCHWARZ **MÛRCHEN** PF 095 0026 0674 Kennzelchen È C2 5 C18 C 3 9 C2:0 C2 1 C23 523 8012 C16 C17 513 C1 5 c † 0 C 1 1 ŝ 23 80 S S с 6 86 5 2 95 28 м 60 84 â

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Blaft Nr. enthalten in SA Sachnummer 302-5219 Rf 069.8217 RF 069.1029 RF 069.2725 069.4311 069.4328 069 .2225 069.2719 RF 069.2219 RF 069_1529 RF 028_0157 028_0157 069.2725 RF 069_8217 069.1829 069.3915 RF 069.1006 RF 069.1006 RF 069.4711 069.2019 RF 069.1006 RL 082-9994 069.1006 Lb 067.2886 LD 067_2911 LD 067.2886 067.2928 082-9636 RL 082-9636 Sachnummer 5 22 يد 12 ц. Ж 3-14 ΥF RF RF ۶F R F RĽ 2 î RL 0.25% d8.1 0HR+-1XTK50 PRALORIC SMA0207/68,10HM-F-D RL 0.25% 68,1 0HM-+1XTK50 BRALORIC SMA0207/68,10HM-F-D RL 0.25% 169 0HM+-1XTK50 RL 0.25% 169 0HM+-1XTK50 RALORIC SMA0207/1690HM-FD UNALORIC LEADZO7/+-5%2 //K RF: 0,25%2 // 240HM +-5% ALORIC LCA0207/+-5%4,3% 0,254820 04M +-5% IC LCA0207/+-521,5K W 50 04M+-52 A 54/5004M52 L CA0207/+-5Z2 2K DRALDRIC LCA0207/+--522_7K RF 0_2554430 088 ---52 LD 1,50041020,220440,5604 DELEVAN DR0SSEL 1025-24 LD 2,70411020,550440,5555 DELEVAN DR0SSEL 1025-30 LD 1,50041020,220440,5550 LD 1,50041020,220440,55505 LD 3,30041020,850440,255-35 DELEVAN DR0SSEL1025-35 LCAU207/+-521.0K ED AUSGANGSSTUFE RF 0.2541,8%0HM +-5% DRALORIC LCAD207/+-5%1,8K DRALORIC LCAD207/+-5X390 RF 9,254220 6H+ +-5X DRALORIC LCAD207/+-5X220 RF 0,254 10 0HM +-5X DRALORIC LCA0207/+-52820 RF 0,254 1K0HM +-52 LCA0207/+-52430 LCA0207/+-52820 рияцойіс цса0207/3-5%470 RF 0,2541,5% анм 3-5% Вылісто RF 0_254200 0HM +-5% DRALORIC LCA0207/+-5%200 RF 0_2554 10 0KH +-5% DRALORIC LCA0207/+-5%10 L CA0207/+-5X270 DRALORIC LCA0207/+-5210 RF 0,254270 04M +-52 DRALORIC LCA0207/+-52270 DRALORIC LCA0207/+-5X10 RF 0,254470 онм +-5X DRALORIC LCA0207/+-5210 RF 0,254 10 04M +-52 DRALORIC LEA0207/+-5210 RF 0,254 10 0HM +-52 SK4/500HM5% 0,2542,7K0HM 4-5X RF 0,25V4,3K0HM +-5X RF 0,254820 0HM +--5% RF 0,25W390 0NM 4-5% RF 0,25W2,7K0HM +--5% Schaltfeillista für Benesnung / Beschreibung W 50 08M4-5% RF 0,2548 DRALORIC 13 0282 DRALORIC DRALORIC DRALORIC AZ , Datum OR ALOR I C RESISTA RESISTA RF 0,5 RF 0,5 5 RONDE & SCHWARZ WUNCHEN Ì Kennzeichen PF 005.0024.0679 823 R 2 4 825 R26 8.1S 816 R 3 7 818 R1 9 R20 82.3 R 2 2 R13 ጸግሩ R10 Rî 812 ₹ S 136 .1.3 4 €33 R S 86 87 83 2 83

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Sachnummer 302 "5219	Sachnummer	RF 069_4757	RF 069 4757	RF 069-6820	RL 083-1580				RL 092.5991	RF 069 .3021	RF 069.1529	RL 083-0084	RL 083.1122						40n		RF 069.2231	RF 069_2231	RF 069 . 1829	RF 069-5630	RF 069.4705	RF 069.1058			RF 069_4311	RF-069-4728	RF 069-3921	P4 169 6843		
Schafttelikiste für ED AUSGANGSSTUFE	Benennung / Beschreibung	£CA0207/+-5%4,7M	LCAD207/+-5%4,7%	LORIC L.CA0207/+-534,7M 0,2546,8K0HM +-5X	LORIC LCA0207/+-526,84 0.25425,5400000000000000000000000000000000	SMA0207/25_5K-F-C	MK1 560HM 2X UNGEN.	LCAD207/+-5%680	20 OHMZX UNGEH MK1 2200HM 2X UNGEW	3X0HM +-5X 1 240207/+-523 0K	5×0+ WH9X5	LCA0207/+-5X1,5K 221 04M+-1XTK50	5MAU2U7/2210HM-F-D 5,23K0HM+-121K50	SHA0207/5,23K-F-D 5,23K0HM++1%TK50	SMAD207/5,23K-F-D	SMA0207/1620HM-F-D	SMA0207/15K-F-D	DRALORIC LCAO 207/+-5%6,8%	LCA0207/+-5X3,9K	LCA0207/+-5%2%	22K0HM +-5% Lca0207/+-5%22K	22K0HM +-5% LCA0207/+-5%22K	. 8KOHM +-5%	56K0HM +=5%		LCA0.207/+-5247 1 MOHM +-52	LORIC LCA0207/+-5X1_0M 0_2542 2804M +-52	LCA0207/+-5%2,2K	30 0HM +-5% ECAD207/+-5%430		0.2543,9%0HM +-5%	LCA0207/*-5X3_9K	DRALORIC L.CA0207/+52680K	
ARZ Az Datum	Genenn	RALORIC F 0,25W	RALORIC F 0,25W	 ∠ ⊥ 	DRALORIC RL 0,25425	DRALORIC RI D 13U	RESISTA	DRALORIC	RL 0,134 220 0HM Resista - MK1 23	RF 0,254 3 DRALORIC	RF 0,2541,	RALORIC RL 0,254 2		່ບສ	ALORIC D.254	DRALORIC BI D 250	DRALORIC DEALORIC	DRALORIC	DRALORIC LCA020	DRALORIC	RF 0.25W DRALORIC	RF 0,25W DRALORIC	RF 0,25W1	RF 0,254	RF 0,254	DRALORIC RF 0,254	DRALORIC RF 0 2542	-	RF 0,254430	0,2	RF 0,2543	BRALORIC	DRALORIC	
ROHDE & SCHWARZ MUNCHEN	Kennzeichen	R67	R68	R 69	R70	R71			R 7 3	875	876	R77	R78	R79	80						. 886	R87	R88	889	R90	R91	892		R93	R9 6	R97	898		
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SA	enthalten in			,																							•							-
Sachnummar 302 - 5219	Sachnummer				RF 069_3909	RF 069.1512	RF 069.3315	RS 247.7949	BF 069.1229	007.1725				RF 069.2202	RF 069-2202	RF 069.3909	RF 069.3309	RF 069.1812	RF 069-1012	RS 247_7949	RF-069.1229	RF 074_0010	049.470	07.070	AC* 400	RF 069.1806	RL 092-5910	RL 092.6030	EW	0 7 0	00-400	RF 069.4757	RF 069.4757	
Schalttelillste für ED AUSGANGSSTUFE	Benennung / Baschreibung		. 0HH +-5X LCA0207/+-5X18	22 0HM +-5% LCA0207/+-5%22	LCA0207/+-5239	0HM +-5% ca0207/+-5%150	0HA +-52	JENZOD OHM+-Z0X10X10X	3586X#1-201 240HM +-52	LCA0207/+-5%1,2%		27 UNM 1-32 LCA0207/+-5239	LCA0207/+-5233	2 0HM +-5% LCA0207/+-5%22	2 0HM +-5% LCA0207/+-5%22	9 0HM +-5%	0HM +-5%	HH +-52	0.04M +-5%	0,5W200 0HM+-20210X10X	3386×-1-201 2kohm +-52	LCA0207/+-5X1,2K 3.9 0HM +-5X	LCA0207/+-523.9 47 0HM +-52	LCA0207/+-5%47	LCA0207/1-5X39	8 0HM +-5% LCA0207/+-5%18	47. OHM2X UNGEN. Mrt 2704m 27 Ungen	470 0HM22 UNGEN.	ISTA MK1 4700HM 22 UNGEW. 0,254680K0HM +-52	LCA0207/+-52680K	0	L CAO207 / +-5 24 .7M	4,7M0HM+-52	
n S		N.	*	2 7 7 7	2	54150 LC	F 0,254330	54200	5 41 , 2	56	2	DRALORIC	0	22	RF 0,254 22 Draloric	ņ.,	8	0 254180 0HM +	RF 0,254100 0	RS 0,5W200	5	LORIC 0,254 3,	DRALORIC RF 0.254 47		•	-	RL 0,134 / Resista		RESISTA RF 0,25468(DRALORIC	RALORIC	F 0,254 4 Raloric	S	
ROHDE & SCHWARZ X3 0282 E MONCHEN 33 0282 E	Benennung	F 0,2541 RALORIC	- 2	RF U_Z54 DRALORIC	RF U, ZSW DRALORIC	RF 0_254150 Draloric	RF 0, 254	RS D	BOURNS RF 0,2	DRALORIC RF 0.5 W	RESI		DRAL	RF () DRAL	BRAC O	RF DRA	RF	R F		ŝ	BOU RF	DRA RF	DR.A	DRA	a a	R F DRA	а 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2		RES F	4 A D R A		R F	<u>.</u>	

Blatt Nr. enthalten in SA Secheummen 302.5419 CC 083.6718 083-6660 CC 083_6699 083.6718 CC 083.6699 083-6676 CC 083.6724 CC 083.6676 CC 083.6724 JELINER TEFK7,1500/208069000 CC 1,5NF-20+80281000018AP STETTNER TEFK7 CC 083-6660 ESPUGATON CC 087-7102 026X974700/1082000 026X982000 CC 087.7102 083.6682 ТЕКК7,1500/2080Е9000 1026х982000 CC 022.0784 CC 087.7525 CC 4 // TMF+-10% CASP2000 CC 087.7102 DRALORIC EDPU6X9/470D/10R2000 CC 1/5NF-20+80% 7100001RAP CC 082.1712 CC 087.7102 CC 022.0784 CC C22.0784 475.0830 BM 302 5483 BL 302_5490 BM 476.1082 Sachnummer с С 20 с С 2 80 C0X767/10000/PF-20+5 DRALORIC EDPU6X9/4700/10R2000 CC 6,8PF+-1PF NP0 TRAP (E0PU6X9/4700/10R2000 ETTNER TEFK7,1500/2080E9000 1,5NF-20+802R10000TRAP 0 EDPU6X9/4700/10R2000 STETTNER TEFK7,1500/2080E9000 CC 1,5NF-20+80%R10000TRAP C 084L081C EDPU4X5/63V1000/102 CC 10NF-20+50X7X886000 E0PU4X5/63V1000/10z EDPU4X5/63V1000/102 TEFK7/6,8/1KP0400 CC 10PF+~10% N150 TRAP 08ALORIC TEFK7/10/10%N150 CC 18PF+~10% N750 TRAP STETTNER TEFK7/1810XN750 CC 12PF+-10X N470 TRAP STETTNER TEFK7/1210XN470 CC 6,3PF+-1PF NP0 TRAP TEFK7/8,2/1N150 TEFK7/1210ZN47C TEFK7/2210%N750 TEFK7/1810ZN750 TEFK7/8,2/1N150 TEFK7/2210ZN750 BL SP8607AC 2:1TEILER UHF PLESSEY SP8607ACM BM 0M321 ANTENNEN-VERST. CC 8 "2PF+--1PF N150 TRAP CC 8,2PF+-1PF N150 TRAP CC 18PF+-10% N750 TRAP BM OM323 ANTENNEN-VERST. 22PF+-10% N750 TRAP CC 22PF+-10% N750 TRAP CC 12PF +-- 10% N470 TRAP VALVO 0M321 BO MC1558G BUAL-OP-AMP CC 4 7NF+-10%6X982000 CC 4,7NF+-10X6X9R2000 CC 4 7NF+-1026X9R2000 DRALORIC EDPU6X9/470 CC 4,7NF+-10%6X9R2000 CC 1NF+-10X63V K2000 CC 1NF+-10%63V K2000 CC 1NF+-10%63V K2000 Schalttelliste für ED TEILER Benennung / Beschreibung MC1558G 0 M 3 2 3 DRALORIC DRALORIC STETTNER STETINER STETINER STETTNER STETTNER DRALORIC DRALÖRIC 13 0282 URALORIC DRALORIC AZ Datum STETTNER THOMSON MOTOROLA VALVO ខ 20 ROHDE & SCRWARZ RUNCHER -----Kennzeichen ¢ c3 0 CZ 6 C27 C28 C 2 9 c31 C 2 4 C 25 623 C1 6 C20 cг¹ C 2 2 c12 613 C 14 **C**10 c11 3 3 S S 22 82 53 Β6 5 8 ມີເວລາ (ການເຊິ່ງຊີນ ແລະ ເຊິ່ງ ເປັນ ແລະ ເຊິ່ງ ເຊິ່ Ċ Ż ۲ ¢ Blatt Nr. - ENDE enthalten In Ş Sachnummer 302-5219 AK 117_8398 AK 010.5163 AK 010_3777 AK 451.4320 AK 249.8236 AK 010_3777 070.5163 AK 451.4320 AK 117.8398 RL 082.9820 RF 069_8217 092-4520 RF 069.4311 RF 069_6843 RS 087.7683 RF 069_1235 RL 092-5891 RL 092-5879 Sachnummer AK RL RESISTA MKT 3304M 22 UNGEN RL 0.13W 22 OHM22 UNGEN RESISTA MKT 2204M 22 UNGEN RL 0.25W 113 OHM+-12TK50 SMA0207/1130HN-F-267 1X TK50 ED AUSGANGSSTUFE 4LORIC LCAD207/+-52820 0_13W 267 0HM+-127K50 DRALORIC LCAD207/+-5%680K RS 0,5W100K0HM+-20%10%10% DRALORIC LCA0207/+-5212K RL 0_134 33 OHM22 UNGEW. AK BERISA SI-NPN HE-TRANS LCA0207 / +-52430 AK BFR15A SI-NPN HF-TRANS STEMENS BCY79IX AK BCY59IX NPN 45V 200MA STEMENS BCY59IX AK DCY591X NPN 45V 200MA Siemens bcy591X AK BCY791X PNP 45V200MIA Siemens BCY791X AK BFTT2 SINPN 25V2GHZ SIEMENS BFTT2 AK BFR94 SI NPN 25V. 0,2A AK BCY791X PNP 45V200MLA AK BFR94 SI NPN 25V 0,2A 3386X-1-104 RF 0,254820 0HM +-5% Schattteilliste für Benannung / Beschrelbung RF 0,25W 12K0HM +-5% RF 0,254680K0HM +-5% DRALORIC LCA0207/+-0,254430 OHM +-5% BFR15A BFR15A 8 FR 9 4 8 F R 9 4 MK 1 ENTHALTEN IN 302.5219 13 0282 Datum DRALORIC DRALORIC DRALORIC SIEMENS DRALORIC RESISTA SIEMENS VALVO BOURNS VALVO Ϋ́ Ľ ROHDE & SCHWARZ MONCHEN Kennzelchen F085.0026.0679 Ŷ R106 R108 R 1 0 0 R 101 R 102 R105 R107 R 1 09 110 899 511 \$ 77 18 ŝ F 2 ñ ž

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Dises Unterlage ist unser Egentum. Vernielitähgung, nösugta Verwentung, Mitteilung an andere ist straffe und achtedenersatzpflichtig

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380	L 08 10	00/1028	200 CC 087	202-202		eora() Suitodriu	L.53	LD 15,00H	СССТАМИ ИКОЗЗЕЕНОСЭТАЗ В 15,0041022,7004M0,210A ЕГЕМАН ВООССЕГАОЗСТАВ	LD 067-3001	302 5619
	LORIC	2700/1082	. 000	TO2 5410		_	L54	LD 15,00b	LL 15,00H10%2,700H00,210A	LD 067.3001	302-5619
	DRALORIC F 2211 - 40	EDPU6X7/100/2XN150		×10.5×10	<u> </u>	(L55	LD 15,00H1	11022,700NM0,210A	Lb 067.3001	302 ~5619
c 87	SIEMENS	MENS 84136-87226-Z		302-561)	L56	LD 0,15U	11020,100HM1,100A	LD 067-2763	302-5619
C88	1	00/10k2 3x13	000 300	302-5619	¢		L57	LD 0, 1801	LD 0,18UH10Z0,120HH1,010A	LD 067_2770	302 - 5619
067	OEDERSY	ELKOEK100/25		202 541	<u> </u>		L58	LD 0,15UH	1020,100HM1,100A	LD 067.2763	302-5619
101		8,2PF++0,25PF3X4NP0	CC 087 6412	202-5619 202-5619	000		53	LD 1,50UH	LD 1,50UH10%0,220HM0,745A	LD 067_2886	302 .561
į	, Z	E0PU3X4/12/2%/NP0	;				L60	LD 0,10U	H10%0,080HM1,100A	LD 067.2740	302 ~ 5619
611	AE BB141A 2	28V 2/12PF KAP.	AE 012.6138	302-5619	19		161	LD 1,50US	41020,220HM0,745A	LD 067.2886	302 . 5619
61.2	38141A	28V 2/12PF KAP.	AE 012.6138	302-5619	19		L62	DELEVAN LD 1,50UF	EVAN DROSSEL 1025-24 1,500H1020,220HM0,745A	LD 067 2886	302 - 5619
6L3	1 N 4448	SI 75V 150MIA	Ab 012.0700	302-5619	19		1.63		EVAN 080555L 1425-24 1,500H1020,220HM0,745A	LP 067-2886	302-561
61.4	8777	21 75V 150MIA DIODE1N4448	AD 012.0700	302-5619	19		L64	DELEVAN LD 1,50UH1 DF1 FVAN	DR0SSEL 1U23-24 H1020,220HM0,745A DR0SSFI 1025-24	LD 067.2886	302-5619
6L5	346	BER_SCH_DIOD_UHF DIODEBA244	AE 4396310	302-5619	61		R1	RS 0,5W21	0,5W2K0HM+-20%10%10%5	RS 247 -7961	302-5619

Image Set (Marked In 1 (10)) Intel (10)	International J 02.5400 A D01.1010 D11.1010 D11.1010 <thd11.1010< th=""> <</thd11.1010<>	L	¢	Az Datum Schalttelitiste für		Sachnummer	Biatt Nr.		¢	Az Datuce Schaltteilikte für	Sach	Blatt
Optimize Definition Beams	Optimization Definition Definit Definition Definit	ROI	HDE & SCHW Mûnchen	11 1180	7	302-561	SA		ROHDE & SCHV MÜNCHEN	11 1180 052 11 ATO	2 302-5	SA
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R1 R2	R12 R10 R			10 0HM +5% LCA0207/+		718	4195-205	negen Bart	R50	DEALORIC LCAD207/+-52100 RF 0,2544,7K0HM +-52	069	302-5619
(1) ((1) (RF 0,254180 0HM +-5%		1812	302-5619			DRALORIC LCA0207/+-5X4,7K		011 F440
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R1 R2 R24 R24 R25 R25 <thr27< th=""> <thr27< th=""> <thr27< t<="" td=""><td>NI Number of a constraint Sectors Sectors Number of a constraint Sectors Number of a constraint Sectors Number of a constraint Sectors Sectors Number of a constraint Sectors Number of a constraint Sectors Number of a constraint Sectors Sectors Sectors Sectors Sectors Sectors Sectors Sector Sectors Sector Sectors Sector Sectors Sector Sector</td><th></th><td></td><td>DRALORIC LCA0207/+-5X220</td><td></td><td>102</td><td>103 E410</td><td></td><td>.R.53</td><td>RF 0,254390 0HH +-5%</td><td></td><td>302-5619</td></thr27<></thr27<></thr27<>	NI Number of a constraint Sectors Sectors Number of a constraint Sectors Number of a constraint Sectors Number of a constraint Sectors Sectors Number of a constraint Sectors Number of a constraint Sectors Number of a constraint Sectors Sectors Sectors Sectors Sectors Sectors Sectors Sector Sectors Sector Sectors Sector Sectors Sector			DRALORIC LCA0207/+-5X220		102	103 E410		.R.53	RF 0,254390 0HH +-5%		302-5619
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R17 R10 District LANDOTY-=5230 R1 R10 District LANDOTY-=5230 R1 District SANDOTY-=5230 R1 District R1 R2 R1 R1 R2 R1 R2 R4 R2 R4 R2 R4 R2 R4 R2 R4 R4 <td>R17 FF 0.55490 MH +5X R5 0.55490 MA 0HC 1.000077+2X30 R1 082.2594 302.5619 R3 0.5541 R3 0.5541 R1 0.52428 R1 0.82.2594 R1 0.82.25294 R1 0.82.2594 R1 0.82.2594 R1 0.82.2594 R1 0.82.25294 R1 0.82.25294 R1 0.82.25294 R1 0.82.25294 R1 0.82.25294</td> <th></th> <td></td> <td>RF 0,254220 0HM + 52 DRALORIC LCA0207/+-52220</td> <td></td> <td>2219</td> <td>302-5619</td> <td>ng ag</td> <td>855</td> <td>WFS22/14,01/1/0,</td> <td></td> <td>302-5619</td>	R17 FF 0.55490 MH +5X R5 0.55490 MA 0HC 1.000077+2X30 R1 082.2594 302.5619 R3 0.5541 R3 0.5541 R1 0.52428 R1 0.82.2594 R1 0.82.25294 R1 0.82.2594 R1 0.82.2594 R1 0.82.2594 R1 0.82.25294 R1 0.82.25294 R1 0.82.25294 R1 0.82.25294 R1 0.82.25294			RF 0,254220 0HM + 52 DRALORIC LCA0207/+-52220		2219	302-5619	ng ag	855	WFS22/14,01/1/0,		302-5619
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RF 0.2544 X02.5619 R57 R1 0.25428 70 0000000000000000000000000000000000	PRS7 Re77 Re10 State Re10 Re10 <t< td=""><th></th><td></td><td>RFLURIC CLAUSUST 24370</td><td></td><td>3915</td><td>302-5619</td><td></td><td>800 8</td><td></td><td>ž</td><td>4100-200</td></t<>			RFLURIC CLAUSUST 24370		3915	302-5619		800 8		ž	4100-200
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BRALORIC BRALORIC BRALORIC SUC20722*7044-FP BF 069-2719 302 BRALORIC CLO2541540 MH<-52	RF 0.25943 0 MM +5X RA0207/25 470 0 MM +5X RA0207/4 551/6 RF 0.69.2719 302 RF 0.25943 0 MM +5X 302.5619 R65 RF 0.25949 0 MM +5X RF 0.69.1829 302 RF 0.25943 0 MM +5X 302.5619 R65 R6 0.25949 8 ML 0 M +5X RF 0.69.3315 302.5619 302 RF 0.2594 10110.05 RF 0.69.3315 302.5619 R65 R6 0.5294 8 ML 0 M +5X RF 0.69.3315 302.5619 864 R5 0.2594 8 ML 0 M +5X RF 0.69.3315 302.5619 865 864.0000 876.0000 876.0000 876.0000 876.0000 876.0000 876.0000 876.0000 876.0000 876.0000 876.0000 876.0000 870.0000 870.0000 870.0000 870.0000 870.0000 870.0000 870.0000 870.0000 870.00000 870.00000 870.0000 870.00000 870.00000 870.00000 870.00000 870.00000 870.00000 870.00000 870.00000 870.00000 870.00000 870.00000 870.000000 870.00000 870.00000 870.000000 870.00000 870.00000 870.00000 870.000000 870.000000 870.000000 870.00000000000 870.000000000000000000		<u> </u>	RESISTA WFS22/14,01/1/0,0	; 			<u>ر</u> .	R58		RL	302.5619
Re 0.05W $+5X$ D30.0538302.5619Re3Re 0.52W 800 Curr 2077+-5X70Re 0.00.1829302Re 0.25W $100M +-5X$ Ne 069.1315302.5619Ne 0.52W $100M +-5X$ Re 0.09.1829302Re 0.25W $100M +-5X$ Re 0.05-1029302.5619Re 4Re 0.25W $200M +-5X$ Re 0.069.2219302Re 0.25W $100M +-5X$ Re 0.05-1029302.5619Re 4Re 0.25W $200M +-5X$ Re 0.069.2315302Re 0.25W $100M +-5X$ Re 0.05-1029302.5619Re 4Re 0.25W $200M +-5X$ 8080Re 0.25W $100M +-5X$ Re 0.09-1029302.5619Re 5Re 0.25W $200M +-5X$ 8080Re 0.25W $100M +-5X$ Re 0.09-1029302.5619Re 5Re 0.25W $200M +-5X$ 8180Re 0.077+-5X1, KRe 0.09-1029302.5619Re 6Reulostic LCA02077+-5X19818686.0, 25W $200M +-5X$ 81909.1706Re 0.25W $200M +-5X$ Re 0.09-1029302.5619Re5Re10.25W $25W 200M +-5X$ 81069-1506302Re 0.25W $200M +-5X$ Re 0.09-3221Re10.25W $25W 200M +-5X$ Re 0.069-1506302Re 0.25W $200M +-5X$ Re 0.05-3219302.5619Re5Re10.25W $25W 200M +-5X$ 81<069-1506	RF 0.05W 14,01 0HW-1X 030.0538 302.5619 R63 RF 0.25W1 8KUR LCA0207/+-5X10 RF 069.1829 302 RF 0.25W1 30 0HW +-5X WFS22/14,01/1/0,05 RF 069.3315 302.5619 R64 RF 0.25W1 8KUR KF 069.2219 302 RF 0.25W1 400R1C LCA0207/+-5X10K RF 069.3315 302.5619 R64 RF 0.25W130 0HM +-5X RF 069.3915 302 RF 0.25W1 400R1C LCA0207/+-5X10K RF 069.30129 302.5619 R64 RF 0.25W190 0HM +-5X RF 069.3915 302 RF 0.25W1 400R1 LCA0207/+-5X10K RF 069.3029 302.5619 R65 R60.0507/+-5X190 RF 069.302 302 RF 0.25W1 400R1 LCA0207/+-5X10K RF 0.25W15 0HM +-5X RF 0.69.41029 302 302 RF 0.25W5 6KHM +-5X RF 0.69.4029 302.5619 R65 R60.058415 LCA0207/+-5X15 RF 0.69.4706 302 RF 0.25W5 6KHM +-5X RF 0.25W4 160HM +-5X RF 0.25W4 160HM +-5X RF 0.69.4706 302 RF 0.25W4 5KHM +-5X RF 0.55W4 160HM +-5X R66 R60.025H4 160HM +-5X RF 0.69.4706 302 RF 0.25W4 6KHM +-5X RF 0.55W4 5KHM +-5X RF 0.55W4 5KHM +-5X <th>-</th> <td>R 20</td> <td>RF 0,254350 0HH +-57 DRALORIC LCA0207/+-52330</td> <td>*</td> <td></td> <td>6100-205</td> <td>)</td> <td>RéZ</td> <td>3,70HA-F</td> <td>RF</td> <td>302-5619</td>	-	R 20	RF 0,254350 0HH +-57 DRALORIC LCA0207/+-52330	*		6100-205)	RéZ	3,70HA-F	RF	302-5619
RF 0.25W.370 0HM +-5X RF 0.25W.370 0HM +-5X RF 0.25W.2077+-5X1,0K RF 0.69.3315 302.5619 302	#F 0.25W1300 0HM +-5X #F 069-1315 302-5619 864 #F 0.25W220 0HM +-5X #F 069-2219 302 # F 0.25W12007+-533.0 #F 0.52W12007+-531.0K #F 0.69-1029 302-5619 864 #F 0.25W1290 0HM +-5X 864 #F 0.25W12910 0HM +-5X 302 # F 0.25W KOHM +-5X #F 0.69-1029 302-5619 302 865 #F 0.25W1390 0HM +-5X 87 860-3915 302 # F 0.25W KOHM +-5X #F 0.69-1029 302-5619 302 865 #F 0.25W1390 0HM +-5X 87 865 87 87 87 87 302 # F 0.25W KOHM +-5X #F 0.69-1029 302-5619 302 865 860 87 87 0.59150 302 # F 0.25W KOHM +-5X #F 0.69-1029 302-5619 865 860 0.72W150 87 069-1506 302 # 0.25W KOHM +-5X #F 0.69-1029 302-5619 867 861 0.72W150 87 069-1506 302 # 0.25W KOHM +-5X #F 0.25W KOHM +-5X #F 0.25W KOHM +-5X 869 861 87 0.25W KOHM +-5X 87 86 860 87 86 860		R21	RF 0,05w 14,01 0HM+−1% RESISTA HESP2/14.01/170.0		0538	302.5619		2,70	LCA0207/+-5%27		\$N2_5690
Rf 0_2594 K0HM +-5X Rf 0,2544 K0HM +-5X 302.5619 302 864 RF 0,2544 S0 MM +-5X 87 0,2544 S0 MM +-5X 87 0,2544 S0 MM +-5X 302.5619 302 RF 0_2594 K0HM +-5X Rf 0,2544 S0 MM +-5X 302.5619 865 RF 0,2544 S0 MM +-5X 87 0,69.1305 302 RF 0_2594 K0HM +-5X Rf 0,2544 S0 MM +-5X 302.5619 865 RF 0,2544 S0 MM +-5X 87 0,69.1506 302 RF 0_2594 K0HM +-5X RF 0,2594 K0HM +-5X RF 0,2594 S0 MM +-5X 87 0,2594 S0 MM +-5X 87 0,69.1506 302 RF 0_2594 K0HM +-5X RF 0,2594 S0 MM +-5X RF 0,2594 S0 MM +-5X 87 0,2594 S0 MM +-5X 87 0,69.1506 302 RF 0_25945 K0HM +-5X RF 0,2594 S0 MM +-5X RF 0,2594 S0 MM +-5X 87 0,69-1506 302 RF 0_25945 S0 MM +-5X RF 0,25945 S0 MM +-5X RF 0,2594 S0 MM +-5X 87 0,2594 S0 MM +-5X 87 0,69-1506 302 RF 0_25945 S0 MM +-5X RF 0,25945 S0 MM +-5X RF 0,2594 S0 MM +-5X 87 0,69-1506 302 RF 0_25945 S0 MM +-5X RF 0,2594 S0 MM +-5X RF 0,2594270 MM +-5X 87 0,69-1820<	RF 0_2545 RF 0_254520 OHM +-5X RF 0_254390 OHM +-5X RF 0_254390 OHM +-5X RF 0_69.13915 302 RF 0_25545 RF 0_255419 302.5619 R65 RF 0_2534390 OHM +-5X RF 069.13915 302 RF 0_25545 K60 RF 0_255419 R65 RF 0_25419 302 302 RF 0_2554 R60 RF 0_25419 302.5619 R65 RF 0_25415 RF 069.1305 302 RF 0_2554 RF 0_25541 RF 0_255415 RF 0_255415 RF 0.69.1305 302 RF 0_2554 RF 0_255415 RF 0_255415 RF 0_255415 RF 0_69.1506 302 RF 0_2554 RF 0_255415 RF 0_255415 RF 0_255415 RF 0_69.1506 302 RF 0_2554 RF 0_255415 RF 0_255415 RF 0_69.1506 302 302 RF 0_25545 RF 0_255415 RF 0_69.1506 302 302 861 RF 0_254175 87 069.1506 302 RF 0_255417 RF 0_255417 R60 R60 RF 0_625477 302 87 069 302 RF 0_255417 RF 0_255417 R60 R60 RF 0_6254		R 22	RF 0,254330 0HM +-52	RF 069	3315	302-5619					
R65 RF 0,25W390 0HM +-5X RF 069.3915 302 R7 0,25W3660HM +-5X RF 069.3915 302.5619 R66 RF 0,25W 15 0HM +-5X RF 069.1506 302 R7 0,25W1 K0HM +-5X RF 069.1029 302.5619 R66 RF 0,25W 15 0HM +-5X RF 069.1506 302 R7 0,25W1 K0HM +-5X RF 069.1029 302.5619 R66 RF 0,25W 15 0HM +-5X RF 069.1506 302 R7 0,25W1 K0HM +-5X RF 069.5619 R67 RF 0,25W 15 0HM +-5X RF 069.1506 302 R7 0,25W1 K0HM +-5X RF 069.5619 R67 RF 0,25W 15 0HM +-5X RF 069.1506 302 R7 0,25W4 K0HM +-5X RF 069.5619 R68 RF 0,25W1 SK0HM +-5X RF 069.1329 302 R7 0,25W4 KN0HM +-5X RF 069.321 302.5619 R68 RF 0,25W1 SK0HM +-5X RF 069.1829 302 R7 0,25W4 KN0HM +-5X RF 069.3321 302.5619 R68 RF 0,25W1 SK0HM +-5X RF 069.1829 302 R8 0,25W4 KN0HM +-5X RF 069.3321 302.5619 R69 R70 RF 0,25W1 SK0HM +-5X RF 069.1829 302 R8 0,25W4 KN0HM +-5X RF 0,25W4 SK0HM +-5X RF 069.3321	DRALORIC LGA0207/+-5X1,0K RF D69.3915 302 DRALORIC LGA0207/+-5X1,0K RF 069.5624 302.5619 R66 R7 LGA0207/+-5X30 RF 069.1506 302 DRALORIC LCA0207/+-5X5,6K RF 030,1029 302.5619 R66 RF 0,25415 RF 069.1506 302 RF 0,2545 KOHM +-5X RF 0.60,1029 302.5619 R66 RF 0,25415 RF 0.69,1506 302 RF 0,2545 KOHM +-5X RF 0.69,1702 302.5619 R66 RF 0,25415 RF 0.69,1706 302 RF 0,2545 KOHM +-5X RF 0.69,4728 302.5619 R67 RF 0,254270 RF 0.69,1706 302 RF 0,2544 XOHM +-5X RF 0.69,4728 302.5619 R69 R60 RF 0.6207/+-5X15 RF 0.69,2719 302 RF <t< td=""><th></th><td>R23</td><td>. BRALORIC LCAD207/+-5X350 RF 0,254 1K0HM +-5X</td><td></td><td>1029</td><td>302-5619</td><td></td><td>864</td><td></td><td></td><td>302-5619</td></t<>		R23	. BRALORIC LCAD207/+-5X350 RF 0,254 1K0HM +-5X		1029	302-5619		864			302-5619
0RMLORIC LCA0207/+-5X5,6K RF 069-1506 302 0RMLORIC LCA0207/+-5X1,0K RF 069-1506 302 0RALORIC LCA0207/+-5X1,0K RF 069-5624 302.5619 R67 RF 0,25W 15 0HM +-5X 0RALORIC LCA0207/+-5X1,0K RF 069-5624 302.5619 R65 R6027/+-5X15 RF 069-1506 302 0RALORIC LCA0207/+-5X1,0K RF 069-4728 302.5619 R68 RF 0,25W1,8K0HM +-5X RF 069-1829 302 0RALORIC LCA0207/+-5X1,7K RF 069-3321 302.5619 R69 R7 0,25W1,8K0HM +-5X RF 069-1829 302 0RALORIC LCA0207/+-5X1,7K RF 069-3321 302.5619 R69 R60 RF 0,25W1,8K0HM +-5X RF 069-1829 302 0RALORIC LCA0207/+-5X1,7K RF 069-3321 302.5619 R69 R60 R7 0,25W1,8K0HM +-5X RF 069-2719 302 0RALORIC LCA0207/+-5X1,7K RF 069-4	R66 RF 0.25W 15 0HM +-5X RF 069-1506 302 RF 0.25W 150HM +-5X RF 0025H 15 0HM +-5X RF 069-1506 302 RF 0.25W 150HM +-5X RF 0025H 15 0HM +-5X RF 069-1506 302 RF 0.25W 150HM +-5X RF 0025H 15 0HM +-5X RF 069-1506 302 RF 0.25W 150HM +-5X RF 069-5624 302.5619 R67 RF 0.25W 15 0HM +-5X RF 069-1506 302 RF 0.25W 560HM +-5X RF 069-5624 302.5619 R68 RF 0.25W 15 0HM +-5X RF 069-2719 302 RF 0.25W4,7K0HM +-5X RF 069-3321 302.5619 R69 RF 0.25W1,8K0HM +-5X RF 069-1829 302 RF 0.25W4,7K0HM +-5X RF 069-3321 302.5619 R70 RF 0.25W1,8K0HM +-5X RF 069-1829 302 RF 0.25W4,7K0HM +-5X RF 069-3321 302.5619 R70 R70 RF 0.25W1,8K0HM +-5X RF 069-219 302 RF 0.25W4,7K0HM +-5X RF 0.65%3321 302.5619 R70 R70 R70 R70 RF 0.25W1,8K0HM +-5X RF 069-2219 302 RF 0.25W4,7K0HM +-5X RF 0.25W1,7H0HM +-5X RF 0.25W1,8K0HM +-5X RF 0.69,.2319 302		77	084L0RIC LCA0207/+-521,0K		5624	302-5619		R65	0,2543		302-5619
RF U_CSW TKUMM +1-5X NF U_CSW TKUMM +1-5X NF U_CSW TKUMM +1-5X RF U_CSW TKUM +1-5X	RF 0.25W TKOHM +-5X RF 0.25W TKOHM +-5X RF 0.25W TKOHM +-5X RF 0.69-1506 302 RF 0.25W TKOHM +-5X RF 0.69-1506 302 302 302 5619 RF 0.55W TKOHM +-5X RF 0.69-1506 302 RF 0.25W TKOHM +-5X RF 0.69-4728 302.5619 R68 RF 0.25W270 0HM +-5X RF 0.69-1719 302 RF 0.25W4,7KOHM +-5X RF 0.69-4728 302.5619 R66 RF 0.25W270 0HM +-5X RF 0.69-1820 RF 0.25W4,7KOHM +-5X RF 0.69-3321 302.5619 R69 RF 0.25W270 0HM +-5X RF 0.69-1820 RF 0.25W4,7KOHM +-5X RF 0.69-3321 302.5619 R69 RF 0.25W18K 0HM +-5X RF 0.69-1820 RF 0.25W4,7KOHM +-5X RF 0.059,3321 302.5619 R70 R70 RF 0.25W18K 0HM +-5X RF 0.69-1820 RF 0.25W4,7KOHM +-5X RF 0.059,3321 302.5619 R70 R70 RF 0.25W270 RF 0.69-1820 RF 0.25W4,7KOHM +-5X RF 0.69-3719 R70 R70 RF 0.25W270 RF 0.69-3719 302 RF 0.25W4,7KOHM +-5X RF 0.69-3728 302.5619 R71 R70 R70 R70 R70 R70 R70 <t< td=""><th>,</th><td></td><td>DRALORIC LCA0207/+-525,6K</td><td></td><td></td><td></td><td></td><td>Róó</td><td>0,254</td><td></td><td>302-5619</td></t<>	,		DRALORIC LCA0207/+-525,6K					Róó	0,254		302-5619
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DRALORIC LCA0207/+-5%,7% RF 069_4728 302_5619 R70 RF 0,25%20 0HM +-5% RF 069_2219 302 RF 0,25%4,7%0HM +-5% RF 069_4728 302_5619 R71 RF 0,25%390 0HM +-5% RF 069_3915 302 DRALORIC LCA0207/+-5%4,7% RF 069_4728 302_5619 R71 RF 0,25%390 0HM +-5% RF 069_3915 302	RT O RE 0.254307/+-5X3/3K RF 069-4728 302_5619 R70 RF 0.254220 OHM +-5X RF 069-2219 302 RF 0.2544,7K RF 069-4728 302_5619 R71 RF 0.254390 OHM +-5X RF 069-3915 302 R71 RF 0.254390 OHM +-5X RF 069-3915 RF 069-4915 RF 069-4915 RF 069-4915 RF 069-4915 RF 069-3915 RF 069-4915 R			RALORIC		1012	203 5410		R 69	1,8		302-5619
RF 0,2544,7K0HM +-5% RF 069-4728 302-5619 0 RALORIC LCA0207/+-5%220 RF 069-3915 302 0 RALORIC LCA0207/+-5%2%20 RF 069-3915 302	RF 0,2544,7K0HM +-5X DRALORIC LCA0207/+-5X4,7K RF 069-3915 302 DRALORIC LCA0207/+-5X4,7K RF 069-3915 302 - PRALORIC LCA0207/+-5X4,7K RF 069-3915 302 - PRALORIC LCA0207/+-5X4,7K RF 069-3915 302	<u> </u>	2 X	RALORIC	10 0				R70	RALURIC F 0,254220		302,5619
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Sachnummer 12 - 5 8 1 9 SA	enthalten in				<u></u>											<u>.</u>		·													
Sachnumm 302+5819	Sachnummer	302.5831	302.5848	302.5848	475.1166	302.5854	302-5860	302-5877	302 -5883	301.3335	302_5248	302.5248	302.5248	302+5248	086.9999	531_0259	086.7150	290-3906	290-3906	087.7048	087-7048	-704	08.7 70.8			087.6506	087.6506	087.7525	087.7525	087.7525	
NG	Sach	BL 3(BL 3(BL 31	81 4	BL 31	B F 31	9F 3	80 3	80 3	80 3	80 3	80.3	80.3	8L ()	BL	BL 0	BL 2	BL 2	55	3	50				5	20	3	2	с С	
Schaltenliste für ED PHASENREGELUNG	Benennung / Beschreibung	4XLINE-RECEIV Metaolisi		BL MC10136L BIN_V/R-COUNT MOTOROIA METO1364	RE MC10131L 2/0-MS-FLIPFL MOTOPOLA MC10131	BL MC10109L 2/4INP.0R/NOR Motodol A Mc10100L	MC10109L . 2/21NP.0R/AND MC10117L	BL MC12040L PLL-PHASE-DET MOTOROLA MC12040L	BIFET-OP_AMP_ LF256H	с.с.О. FE ОР-АМР. Se5534абр	4 X 0 P - 4 M P L	4 X 0 P - A M P L .	, LU3463 4 X OP - AM PL - Y 1 O 4 M 4	4 XOP-AMPL.	AS FLO44MJ CD4069UBE HEX-INVERTER CN202055	CUSUOSUBE 4/2INP.ANDG. FD&DRTHE	SCL402886 8C0/DEC_DEC_ SCL402886 8C0/DEC_DEC_	JULTUROBE 5 4/ANALOGSWITC FD&DAAAF	CUTCODE 4/ANALOGSUITC 60404688	024X5R2000	0RIC EDPU4X51500102R2000 	E0PU4X5150010XR2000 +-10X4X5R2000	LORIC EDPU4X5150010XR2000 1.5NF +-1022 Y502000	DRALORIC EDPU4X51500102R2000	0/PF	25X6NP0 FDD15X6 <i>1</i> 47 <i>1241</i> NP	25 X6NPO 25 X6NPO	CC 10NF-20+50%7X8R6000	c0X767/10000/Pf-20+5 +50X7X8R6000	THOMSON COX767/10000/PF-20+5 CC 10NF-20+5027X8R6000	-
15 Datum 15 0282	Benennur	BL MC10115L	361	BL MC101361 MOTOPOLA	BL MC10131L	BL AC10109L	HUIUKULA P BL MC10117L MOTOROLA P	BL MC12040L	BO LF256H B NSC	GO SESS34AFE O StGNETICS SFS	B0 TL044MJ TFXAS	RMAPAC	BO TLO44MJ TFY25	80 TL044MJ	1 EXAS BL CD4069UE Pra	ALA BL CD40818F RCA	ی در د	333 BL CD4066BE 8CA	BL C040668E BL C040668E		AL.	<	DRALORIC CC 1.5NF+-	DRALORIC	THOMSON	CC 47PE+-2%5X6NPO DRALORIC FDDI5Y	CC 47PF +-2X5X6NP0	CC 10NF-20	THOMSON CC 10NF-20	THOMSON CC 10NF-20	
ROHDE & SCHWARZ MUNCHEN	Kennzeichen	6	82	B3	84	8 5	96	18	B10	811	B12	613	B14	815	816	B17	818	819	820	5	ري . د ک	c3	74	ž	}	66	с 7	c8	69	C1 0	
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Sachnummer 2.5619 SA	enthalten In	302-5619	302-5619	302-5619	302-5	302-5619	302-5619	¥07.5649		302-5619	302-5619	302 . 5619	302	302-5619	302-5619	302.5619	302-5619	30 2.5619	302 .5619	302~5619	302.5619	יש ו									
Sachnumn 2 302-5619	Sachnummar	069 - 1506	069-1506	069-1812	092-5833	074_0056	069.5618			247.6536	247_6536	451.4320	451-4320	010_3777	010.5163	010_3777	010.5163	010_5163	451_4320	451-4320	451-4320									·	
	Sach	RF 06	RF 06	RF	يد يد	к. К.	RF 06			AM 24	AM 24	AK 45	0			<u> </u>	AK	, AK	AK 4	AK 4	AK 6.							÷			
0 0521LLATOR	Benennung / Beschreibung	DRALORIC LCAO207/+-5X390 RF 0,25W 15 0HM +-5X Dralobir 15 0AM +-5X	15 0HM +-5%	30 0HM ++5%	. 10 0HM2% UNGEW.	7.6 0HM +-52	LCAUZU//+-525,0 60 0HM +-52 LCA0207/+-52560	-		N-KANAL-FET 25V Teamersteeping	TPANAL-FET 25V TPANSTSTRF2474				SI-TRANSISTORBCY791X X NPN 45V 200MA	X PNP 45V200M1A			SI-NPN H	SI-KPN HF-TRANS SI-KPN HF-TRANS Bed 45 A	SI-NPN H	2									
NARZ 11 Datum	มิตกลุก	DRALORIC RF 0,25W	RF 0,254 1	RF 0,25W18	RL 0,134.		PRALURIC L RF 0,254560 DRALORIC L	CATHALTEN	302-5619	AM BF247A	AM 8F247A	AK BFR15A	AK BFR15A	AK BCY791X	SIEMENS AK BCY59IX	SIEMENS AK BCY791X	AK BCY59IX	AK BCY591X	SIEMENS AK BFR15A	AK BER15A	AK BFR15A										
ROHDE & SCHWARZ HONCHEN	Kennzeichen	R72	R73	R 75	R 76	R 7 7	R 73	513	7	1	12	13	51	15	T6	17	13	61.	150	151	152										
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onthalten in' SA Sachnummer 302-5819 CC 087-7048 cč 087.7019 CC 087_7019 CC 087-6512 CC 087-7525 AE 012-9066 AE 012-9066 AD 012-0700 AP 012.0700 AD 012.0700 CC 087_6906 CC 087_6512 CC 087.7048 CC 087-7048 CC 087.7048 CC 087.6906 CC 087.7048 CC 087-7048 CC 087.7019 CK 006.5056 CC 087.7525 CC 087-7525 CC 087.7525 CK 006-5062 CK 087-0972 CC 087-7525 CC 087.7525 CK 006.5062 CC 087-6906 Sachnummer

 CK
 22 DNF+-20X100V
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 MKT1822-422/0
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 T0NF-20+50X78R6000
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 THOMSON
 C0X767/10000/FF-20+5
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 THOMSON COX767/10000/PF-20+5 C0X767/10000/PF-20+5 DRALORIC EDPU4X51500102R2000 CC 1_5NF+-1024X5R2000 DRALORIC EDPU4X51500102R2000 E DPU4 X51500107R2000 EDPU4 X5150010XR2000 LORIC EDPU4X51500102R2000 1,5NF+-1024X5R2000 EDPU4x51500102R2000 ALORIC EDP114X5680102R200 680PF+-1024X5R2000 F DPU4X5680102R2000 ED PHASENREGELUNG EDPU4X5/100/22N750 E 0PU4X5/100/2XN750 E0PU4 X5680102R2000 E DPU4X5/100/2XN750 0RALORIC EDPUSX6/56/22NP0 CC 10NF~20+5027X8R6000 E DPUSX6/56/22NPO AE 5082-2800 SCH0TTKY-DI. HEWLETT-P. 5082-2800 AE 5082-2800 SCH0TTKY-DI. HEWLETT-P.5082-2800 DA AD 144448 SI 75V 150MIA VALVO 144448 AD 144448 SI 75V 150MIA VALVO 144448 VALVO 714448 CK 330NF+-202100V QUADER ROEDERST MKT1822-433/0 AD'1N4448 SI 75V 150MIA CC 10NF-20+50%7X8R6000 1_5NF+-10%4X5R2000 680PE+-10X4X5R2000 1.5NF+-10%4X5R2000 Schaltteilliste für 680PF+-10X4X5R2000 CC 1_5NF+-1024X5R2000 Bencnnung / Beschreibung CC 100PF+-2X4X5N750 CC 100PF+-2%4%5N750 100PF+-2%4X5N750 C 56PF+-2X5X6NP0 56PF+-2X5X6NP0 DRALORIC DRALORIC 15 0282 AZ Datum DRALORIC DRALORIC DRALORIC DRALORIC RALORIC RALORIC DRALORIC DRALORIC THOMSON ROHDE & SCHWARZ <u>ບ</u> ပ ပ ų MÛNCHEN Kennzeichen <u>6</u>29 615 C49 C50 C51 C52 C 6 5 6L1 GL2 GL3 614 C 4 3 C S 3 C 5 4 C 5 7 C58 C 6 0 C 6 1 C 6 2 C 6 3 C 6 4 C 6 6 C 6 7 C45 C48 C 5 6 C.4.6 C 4 7 C55

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Blatt Nr. 2																									,				
Sachnummer 12 "5819 _{5 A}	enthatten in																					3	i					· .	
Sachi 302_5	Sachnummer	7 - 75 25	7.7048	7-7048	7.7048	7-7048	7.7048	2.8085 7 7525		.752	7-6512	7-6512	7.7525	2.8085	2.8185	2.8185	7.7525	7.7525	7.6906	7.6906	7.6464	2.8185	2.8185	2.8185.	2.8185	16.5056	17-7525	· ``	
e	Sachn	0+5 CC 08	00 CC 08	C CC 087	cc 087	CC 087	CC 087	CE 022	0+5 CC	+2 CC	20+5 cc 087	CC 087		U+3 CE 02	CE 02	CE 022		÷;	cc 08	cc 087	CC 087	CE 022	CE 022	CE 03	CE 02	CK 00	CC 087	0 0	<u>.</u>
Datum Schatteiliste für 0282 ED PHASENREGELUNG	Bansnnung / Boschreibung	-20+502747/10000/PF-2 -20+50278846000	X4X5R2000 X4X5R2000 DPU4X51500102R2	X4 X5 R	24X5R2 0PU4X5		+-1024X5R20 EDPU4X51	X16V 7X 4X A-ELKOETR2 027X8R6000	COX767/10000/PF- 0+5027X8R6000	0X767/10000/PF- 0X7X8R6000	C0X767/10000/PF225X6NP0	EDPU5X6/56/ -2%5%6NP0	X6/56/22NPO 8R6000 7 10000 101	+-20216V 7X 4X 8	AL TATELKUE 186-10 +-20235V 5X 4X 7		+5027X8R6000	-20450%7%8%6000/PF -20450%7%8%6000	F+-224X51	+-2%4%5N750 EDPU4%5/300	-2 24 X 5 NPO	F+-20%35V 5% 4% 7	F+-20%35V 5% 4%	F+-20%35V 5% 4% 7	+-20235V 5X 4X 7	+-20%100V 9UA0ER	-20+5027X8R6	C0X767/10000/Pf- -20+50X7X886000	
XZ 15	Ben	THOMSON CC 10NF- THOMSON	CC 1, SNF	CC 1,5NF DRALORIC			` ₹	CE 10 UF ER0-TANT CC 10NF-	10NF	NOSCI	HOMSON C 56PF	KALUKI C 56PF	CC 10NF-		5-6	ເພື	CC 10NF-20	CC 10NF-	CC 100P	CC 100PF	22PF	1 DU		- 1	CE 1,0UF	CK 220NF	CC TONF	THOMSON CC TONE	
ROHDE & SCHWARZ MONCHEN	Kennzeichen	611	C12				C16	C17 C18	c19	620	C21	C22	c 23	C24	c25	C26	c27	C28	C29	30	. 531	C 35	c36	C 37	C38	C40	C41	242	

PF 005 001 0 0.670

Blatt Nr. enthaiten in Ś Sachnummer 302,5819 RF 069_6814 RF 069_6814 RF 069-6814 069-6814 RF 069.1529 RF 069_2225 RF 069.2225 RF 069-2225 RF 069-6814 RF C69.6814 RF 069.1029 069.1529 RF 069.1529 RF 069 1529 069.1529 RF 069_2225 RF 069.2225 RF 069-1029 RF 069.1029 RF 069-1029 LD 067.2740 LD 067_2886 LD 067.2886 067.2886 082.9536 RF 069_6814 0.69_6814 069-1029 Sachnummer RF RF RF RF ę, Ľ 4 ç ED PRASENREGELUNG RF 0,2541,5X0HK +5X 0.8AL0RIC LC40207/+-5X1,5K 0.8AL0RIC LC40207/+-5X1,5K 0.2541,5K0HH +-5X 0.2541,5K0HH +-5X 8F 0,2541,5K0HH +-5X1,5K DELEVAN DROSSEL1025-28 LB 0,100410X0,080HM1,400A DELEVAN DROSSEL1025-94 LD 1,500410X0,220HM0,560A DELEVAN DROSSEL1025-24 LD 1,500410X0,220HM0,560A LD 1,500410X0,220HM0,560A DELEVAN DROSSEL 1025-24 LD 1,500410X0,220HM0,560A DELEVAN DROSSEL 1025-24 0,25% 51,1 0HM+-1XTK50 ALORIC SMA0207/51,10HM-F 0,258680 0HM +-5% ALORIC LCA0207/+-5%680 DRALORIC LCA0207/+-5%2,2% RF 0,2542,2%0HM +-5%2,2% DRALORIC LCA0207/+-522,2K RF 0,2542,2K0HM +-5% BRALDRIC LCA0207/+-5%2,2K RF 0,254680 0HM +-5% 11081C LCA0207/+-5X1,0K 0,254 1K04M +-5X 1.0K1K04M +-5X BRALORIC LCA02077+-5x2,2x RF 0,2542,2x0HM +-5x DRALORIC LCAC2C77++5x2,2x PRALORIC LCAC2C77++5x2,2x DRALORIC LCA02077++5x2,2x DRALORIC LCA02077++5x2,2x LCA0207/+-521,0K ALORIC LCA0207/+-521,0K 0,254 1K0HM +-52 LCA0207/+-521,0K RF 0,2541,5K0HM +-52 DRALORIC LCA0207/+-521,5K DRALORIC LCA0207/+-5%1,5% Rf 0,2541,5K0HM +-5% 08ALORIC LCA0207/+-521,5K RF 0,25W2,2K0HM +-52 DRALORIC LCAD207/+-52680 RF 0,254680 0HM +-52 DRALORIC LCAD207/+-52680 RF 0,254680 0HM +-52 DRALORIC LCA0207/+-52680 RF 0,254680 0HM +-52 ALORIC LCA0207/+-52680 0,258680 0HM +-52 RF 0,25W680 0HM +-52 DFRALORIC LCA0207/+-52680 RF 0,254 1K0HM +-52 Schalttelliste für Benennung / Beschreibung 0,25W 1K0HM +-52 0,254 1K0HM +-5% 15 0.282 AZ Datum DRALORIC RL D, 25W DRALORIC RF 0, 25W6 DRALORIC DRALORIC DRALORIC DRALORIC DRALORI ЯF Ч. u ce КĽ ROHDE & SCHWARZ MÜNCHEN R Kennzelchen ¢ PF Out OUL 00.18 0679 R25 R 2 6 R 2 7 L25 R13 818 R19 R 24 R 2 8 L26 R10 R 1 1 R1 2 R 1 4 R 1 5 816 R 1 7 R 2 3 L27 L21 R 8 £ 6 R R 1 82 В3 R 4 R 5

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¢	AZ Datum Schaitteilikste für	Sach	Sachnummer Nr.
NUMUE & SCHWAHZ MÛNCHEN	VAHZ 15 0282 ED PHASENREGELUNG	30	SA
Kennzeichen	Benennung / Beschreibung	Sachnummer	enthalten in
GLÓ	ALVO 1N444 D 1N4448 SI 75V	AD 012.0700	
617	4 4 4 4 4 4 6 4 4 6 4 6	Ab 012-0700	
GL8	VALVU IN4448 Ad 1n4448 SI 75V 150mla Valvo 1n2228	AD 012.0700	
6 L9	127 12 8777	AD 012-0700	
6110	17777 IS 87550	AD 012.0700	
6L11	10 114448 SI 75V	AD 012-0700	
6L12	D 1N4448 SI 75	AD 012.0700	
61,13	AD 144448 SI 75V 150MIA VALVO 144468 31 75V 150MIA	AD 012.0700	:
6114	52 IS 8777	Ab 012.0700	
6L15	AD 114448 SI 75V 150MIA	AD C12.0700	
6L16	x55/C12 82X55	AE 012.2532	
L1	0,18 0HMO	LD 067.2870	
۲2	D 1,50041020,220440,560A	LD 067.2886	
2	ссемии вкизаец 1025-2 0 1,50041020,2204М0,560A блемии всего 1035 3	LD 067.2886	
L4	10%0,220HMC	LD 067.2886	
ιó	0,10041020,080HM	LD 067.2740	
L7	50UH10X0,22CHMC	LD C67+2886	
L8	D 1,50UH1020,220HM(D 1,50UH1020,220HM(D 1,50UH1020,220HM(LD 067.2886	
L9	, 50UH10	LD 067.2886	
L10	111 0 2 0, 22 0HM	LD 067.2886	
111	1020,220HM	LD 067-2886	
L12	0 100H1020 080HM	LD 067.2740	
115	021,0000HM000	L0 067_2934	
L16	900H10X1,000HM	LD 067.2934	
L17	3,900H10X1,000HM	LD 067-2934	
L18	2,200H1020,400HM	LD 067.2905	
L19	D 2,20041020,4004M	LD 067_2905	
۲20	2,200H1020,400HM	LD 067_2905	
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Sachnummer 302-6015 RL 083.1297 092.1421 092-1509 082.6614 N. 082.1764 RF 069.8217 RE 069-4728 092.1521 069 .6814 069_3915 069.1012 083_0461 RL 082_6614 IF 069-5124 RL 092.1367 RL 092-1444 083-0461 RL 083_0461 RL 083.0461 RL 092-0031 RF 069-3338 092.1621 RL 092.1280 RF 069-2419 RL 092.1521 RF 069_1029 RF 069_3338 RF 069-5601 kt 092.1309 Sachnummer زر RL 물 F К. R ۳ ۲ 노 ЯF RL RESISTA NKT 6810HM 1X TK50 RL 0,13U 3,32K0H4+-1XTK50 RESISTA NKT 3K32 TX TK50 TK50 ALORIC LCAD2D7/+-5%56 0,13W 56,2 DHH+-1%7K50 5157A RK1 56,20HH 1% TK50 VAALURIC SMA0207/5620HM-F-D RL 0_258'562 0HM++12TK50 DRALORIC SMANTANY SISTA MK1 2210HM 12 TKSD 0,13W 1,00К0HM+-12TK5D RL 0,25W 3,83K0HM++12TK50 DRALORIC SMA0207/3,83K-F-D RF 0,2545,1K0HM +-53 SMA0207/3,83K-F-D DRALORIC LCA0207/45525,1K RL 0,254 10,0K0HM+-1XTK50 DRALORIC SMA0207/10K+F+D DRALORIC LCA0207/+-5%4,7% RL 0,134 4,75K0HM+-1%7K50 MK1 4K75 1% TK50 DRALORIC LCA0207/+-5X1,0K RL 0,134 221 0HM+-1X7K50 SMA0207/100K-F-C MK1 4K75 1% 7K50 SMA0207 /5620HM-F-SMA0207/5620HM-F. MK1 33K2 1% TK50 MK1 1K00 1X 1K50 LCA0207/+-5X680 LCA0207/+-52820 DRALORIC LCA0207/+-5%24C RL 0,134 4,75K0H#+-1%7K50 1081C LCA0207/+-5X390 0,254100 0HM +-5X 11081C LCA0207/+-52100 0,25W 562 0HM+-127K50 0,25W 3,83K0HM+-12TK50 0HM+-1X1K50 LCA02077+--5X33K LCA0207/4-5233K DRALORIC SMA0207/10K+F-RL 0,25W 100K0HM++1XTK50 RL 0.13W 16.2 OHM+-1XTK50 RF 0.25W 33K0HM +-5X DRALORIC LCA0207/+-5X33W 0,134 68,1 0HM+-1XTK50 SISTA MK1 68,10HM 1X RL 0,13W 33,2K0HM+-1X1K50 0 254 562 0HM+-1 XTK50 ED UMSETZER RF 0,2544,7K0HM +-5X RF 0,254680 0HN +-52 0,254390 0HM 4-57 0,254820 OHM +-5% Schaltteiliste für Benennung / Beschreibung RF 0,254 33K0ĤR +**-**5X RF 0,25W 56 0HH +-5X 0,254240 0HM +-52 RF 0,25W 1K0HM +-5% DRALDRIC LCA020714 RL 0,13W 681 19 0282 DRALORIC DRALORIC Datum DRALORIC DRALORIC DRALORIC DRALORIC DRALORIC DRALORIC DRALORIC DRALORIC RESISTA RESISTA RESISTA RESISTA RESISTA RESISTA RESISTA Ϋ́ <u>ب</u> ۔ چ u a ц Н ۳Ľ <u>ب</u> <u>ب</u> ž **JOHDE & SCHWARZ** MUNCHEN PF 095.0028 0679 Ŷ Kennzeichen R 5 0 R 5 2 R5 3 **R**55 R48 R49 R51 R 5 4 R 4 5 847 R 4 O R 4 2 843 R44 R35 R36 R37 R 38 R 3 9 R 4 1 825 826 827 828 R29 R 3 1 832 R33 R 34

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75 AX BF27X ST PNP 55V15M1A AK 010.4680 76 AX 010.4681 AK 010.4680 76 AK 010.4681 AK 010.4680 77 AK 010.4680 AK 010.4680 78 AK 010.4680 AK 010.4680 79 AK 010.5163 AK 010.5163 710 AK 60791X NP 450200M1A AK 010.5163 711 STERENS BC7931X NPN 450200M1A 711 STERENS BC7931X 200M1A		13	SA SI-NPN	451	
16 XE 2N2369A NFN 40V0,2A AK 010.4680 17 XE 2N2369A XE 010.3777 AK 010.3777 19 XE BCY91X BCY791X AK 010.5163 110 AK BCY791X BCY791X AK 010.5163 111 SERENS BCY91X AK 010.5163 111 SERENS BCY951X ZODMA		T5	AVI IS AS	082	
T8 VALVO ZK25054 AK 010.3777 T9 AK 010.3777 AK 010.3777 AK 010.3777 T11 STERRIS BC7991X AK 010.5163 STERRIS BC7991X AK 010.5163 AK 010.5163 T11 STERRIS BC7991X AK 010.5163 STERRIS BC7991X AK 010.5163 AK 010.5163 STERRIS BC7991X AK 010.5163 AK 010.5163 STERRIS BC7991X AF 010.5163 AK 010.5163 STERRIS BC7991X AK 010.5163 AK 010.5163		.76	2369A SI NPN 40V0	010	
19. SIEMENS BETT91X AK 010.5163 110 AK BETS91X AK 010.5163 AK 010.5777 111 SIEMENS BET791X AK 010.5763		18	ZNZ IX PNP	010	
T10 SIERens BCY991X AK 010.3777 AK 010.3777 SIERENS BCY991X AK 010.5163 AK 010.5163 AK 010.5163 AK 010.5163 AK 010.5163		т9	12 NPN 45V	010	
T11 SIERENS BCY791X AK BCY591X ANN 45V 200MA AK 010-5163 SIERENS BCY591X AK 010-5163	,		BCY59	010	
SIE MENS	dîn le		BCY791X IX NPN 45V		
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Az Datum Schaftleiliste für Sachnummer ÅRZ 32 0282 ED REFERENZ SA	Benennung / Beschreibung Sachnummer enthalten	100NF+-20235V 5X 4X 7	R0-TANTAL TA-ELKOETR E 10 Uf+-20225V 7X 5 B0-TANTAL FYD3-10/25	220UF-10+100X16V	100UF-10+100X25V 13X	EDERST ELKOEKI 1000F-10+100X25	RST ELK0EK100/25 F+-10%63V K2000	ALORIC EPPU4X3/05V1UGU 56PF+-2X5X6NPO	ALORIC EDPUSX0/50/ 56PF+-2X5X6NP0	RALORIC EDPU5X6/56/ C 56PF↔-2%5X6NPO	C 1NF+-10263V K2000	C 100PF+ZX6X9NP0 C 100PF+ZX6X9NP0	LOKIC E DPUGAY/100 120PF+-2%6%9NP0	RALORIC EDPU6X9/120/22NP C 100Pf+-226X9NP0	RALORIC EDPU6X9/100/2XNP0 C 1NF+-10263V K2000	KALORIC E0PU&X5/0 E 100UF+10+100X25V	ROEDERST ELKOEKIDUZS CC 100K-20+50%7%886000 CC 087_7525 THAMS00 CC 087_7555		C 1001 - COX767/10000/PF+20+5	C TUNF-ZU+SUX7X8K6UUU CC UB7.752 Homson cox767/10000/PF-20+5	22 UF+-20%35V12% 7%11 CE 022 -tantal ta-elkoetr4/22/35	DX16V 7X TA-ELKO	1NF+-103	C 330PF+-10%3%482000		330PF+-1023X4R2000	DRALORIC EDPU3X4/330/10ZR2000 CC 1NF+-10Z63V K2000 CC 022_0784	RALORIC EDPU4X5/63V1000/1 <mark>02</mark> c 1nf+-10263V K2000 c 1nf10263V K2000	RALORIC	EDERST ELKOEK100/25 52046421024041025 CE 022	IEMENS 841316-44227-2	
ROHDE & SCHWARZ WÜNCHEN	Konnzelchen	c12	C13	C14	c15	C16	C17	C18	c19	c20	c21	c22	C23	C24	c.25	C26	C27	BIS	, I	C45	C 4 7	870	C50	c51	C52	c53	c54	c55	¥53	Ş		

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Sachaummer	302.6215	Sechnummer	RF 069-5630	RF 069-1041	RF 069-1029	RF 069.2231	RF 069.5124	RF 069.1035	RF 069.1041	RF 069.1212	RF 069 6808	RF 069.1512	RF 069-6808	RF 069.3321	RF 069-3321	R.F 069.1858	RF 069-1535	RF 069-2219	RF 069_2725	RF 069.1029	RF 069-2219	RF 069.2219	RF 069.2725	RF 069-1029	RF 069-2219	RF 069.2219	RF 069-2725			RF 069.2719	RL 067.4720	RF 069-3921	
AZ Datum Scheltteilliste für	32 0282	Benennung / Beschreibung	RF D_254 56K0HM +-5% R	F 0,25W100K0HR +-5%		22K0HM +-5%	5,1KOHM +-5%	LCA0207/+-525,1K 10K0HM +-52	FALORIC LEAUSU//+-5X10K F 0,25W100K0HM +-5X	LCAU20//+-5%10UK 120 OHM +-5% 120 CHM +-5%	68 QHM +-52	150 0HM +-5%	•		DHALORIC LCAU2U//**5X5,5K			F 0,254220 0HM +-5%	LCAUZU//+->X2ZU 2,7KOHM +-5X 2,2KOHM +-5X	×1, 2, 2, 2, 2	PRALUKIC LEAUZU(//+->K1_UK RF 0.25W220 OHM +-5X	0,25W220	2,7	1.KOHM +52	DRALORIC LEAD207/+-521.0K	DRALORIC LCA0207/+5x220 Rf 0_254220 0HM +-5x	DRALORIC LCA0207/+~5%220		DRALORIC LCA0207/+-5%	RF 0,25W270 0HM +-5% DRALORIC LEAD207/+-5%270	46,19K0HM+-1	25W3,9K0HM +-5%	
¢	ROHDE & SCHWARZ MONCHEN	Kennzelchen	R24	R25	RZG	R27	R 28	R 29	R30	R 31	R32 .	R33	R34	R35	836	R37	R38	R40	R41	R42	R43	844	R45	846	R47	848	0 7 8		K50	R51	R52	R53.	
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	Sachnummer - 26215 _{S.a}	enthalten In											•.											1 ·		۰ ·	:						 <u> </u>
	Sachnumm 302 6235	Sachnummer	LD 067.2792	067.2828	067.2828	067.2792				055.6746	. 086.3678			F 069-1041		S 087.7660				RL 082-2560		RF 069-1041	RL 099.3242		RF 069.1035	RF 069.2725	RF 069-5618	RF 069-1812	11.940		069 27	RF 069.2225	
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—	Datum Schektelijste für 0282 ED REFERENZ	Benennung / Bei	27UH10X0,160HH0,975A	DELEVAN DROSSEL1025-06 LD 0,47041020,3504M0,660A	AN DROSSELTU25-12 47UH1020,350HM0,660A	DELEVAN DROSSEL1025-12 LD D_27UH10X0_160HM0_975A	AN DROSSEL1025-06 _0041023.4004M0_130A	DELEVAN DROSSEL 1025-56 LD 1504 BEI 0.574 1.32088	74_11-15ROK	EQ 10,000MHZ CL30 HC-18/U QUAKE QUAR2QDD61000M10	0 , 125436,5K0HM+-1XTK50	DALE Mf1/10 36,5K 1XTK50 RL 0,125416,5K0HM++1XTK50	.E MF1/10 16,5K 1XTK50 0,2542,7K0HM +-5X	<pre>praloric lca0207/+=5x2,7x rf 0_254100k0Hm +=5x</pre>	LORIC LCA0207/+-52100K 0,2541,5K0HM +-52	RALORIC LCA0207/+-521,5K S 0,5420K0HH+-20210X10X5	NS 3386X+1-203 ,254 2,15K0HM++12TK50	SHA0207/2,15K-F- 0HM+-20210X10X5	BOURNS 3386X+1-102 RL 0,254 1,91K0HH+-1%TK50	0RIC SMA0207/1,91K-F- ,25W 6,81K0HM+-1ZTK50	DRALORIC SMA 0207/6,81K-F- RS 0,5W20K0HM+-20210X10X5	RNS 3386×-1-203 0,254100×04m +-5%	DRALORIC LCA0207/+-52100K RL 0,134 15,8K0HM+-127K50	RF 0,254 1K0HM +-5X DRALORIC 1 CAN2N7+-521_NK	10	~	200 0HM +-5%	180	DRALORIC LCA0207/+-52180 BF 0.2541 8K04M +-52	DRALORIC LCA0207/+-521,8K	ORIC	RF 0,2542,2K0HM +-5X Draloric LCA0207/+-5x2,2K	
	ROHDE & SCHWARZ		Lb 0,2	LO 0,4	LD 0,4	LP 0,2	D'ELEV	DELEV.	JAHRE	EG 10, QUAKE	RL 0,	DALE RL 0,	DALE RF 0,	PRALORIC RF 0,25W	DRALO	DRALORIC RS 0,542	BOURNS	DRALORIC RS 0,541K	BOURD R C 0	DRALC RL D	DRAL(BOURNS		8 F 0	18		RF 0	PRAL RF O		DRAL	RF U	DRAL	
E	E & SCHW	Kennzeichen	1																										U C A		228		

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Nr. enthalten in ŞA Sachnummer 302-6415 CC 087.7102 CC_087.6829 087-0950 CC_022 .. 1945 CC 087.7102 CC 087.6441 CC 087_7048 CC 087.7102 BM 207.3465 BC 092_8155 BL 468.5976 CC 4,7NF+-1026X9R2000 Draloric EdPu6X9/4700710R2000 CC C87.6829 087.4049 087 4432 022 1945 CC 022.2764 BL 302-6038 BL 468.5976 BL 303.8986 80 246-1760 B0 275-0822 BL 530.5805 B0 246.1760 BL 275.0697 BL 302-6038 BL 302-6038 BL 303.8986 Sachnun ED INTERPOLATIONSOS_SOKHZ ž ų Š 20 CC 4 7NF+-10X6X9R2000 DRALORIC EDPU6X9/4700/10R2000 EDPU6X9/4700/10R2000 E DP U 6 X 9 / 4 7 00 / 10R 2 000 N220/ 1A27/2RD3X10LC DRALORIC EDPU3X4/15/2X/NP0 CT_15N++-1024X5R2000 DRALORIC EDPU4X31500102R20D0 CC 4_7NF+-1026X9R2000 I EXAS SN54LS193J/R8S-LV BL SN54LS04J 6/1NVERTER TEXAS SN64LS04J 6/1NVERTER EDPU3X4/22/2XN750 E0PU3X4/22/2XN750 SN54LS1931/R&S-LV NP0/25/2RR3X12LC SN54LS193J/R6S-LV DERST MKC1862-468/06 25PF 2% NP0/18 3R0HR CC 27PF 2% N220/IA 3 R0HR TEXAS UARAINU BL SN54LS290J DE2.ZAEHLER PEVAS SN54LS290J MOTOROLA MC140468CP BL SN54LS74J 2XD-FLIPFL0P TEXAS SN54LS74J BL SN54LS193J 48IT-COUNT. Texas sn54LS193J/rgs-RL SN54LS193J 4BIT-COUNT. TEXAS SN54LS193J 4BE BL MC14046BCP MICROP. PLL MOTOROLA MC14046BCP B0 UA709JG OP-AMPLIFIER BL MC14046BCP MICROP. PLL ROEDERST MKCT862-422/0 CK 22NF+-20X400V6X11X13 Roederst MKC1862-322/4 CC 25PF 2X NP0/IB 3R0HR CK 220NF+-202100V 9UADER CK 680NF4~20X63V QUADER BC D8243 4 X48IT-I/0-EXP. INTEL 08243 BL SN54LSO4J 6/INVERTER TEXAS: SN54LS04J UA709JG OP-AMPLIFIER XAS SN52709JG CC 4 7NF+-1026X9R2000 BM SRA-1 RINGMISCHER Schalttelläste für Benennung / Beschreibung SN52709JG BO UA741MJG OP-AMP. CC 22PF+-2%3X4N750 JEALUKIC EUPU6X9. CC 22PF+-2X3X4N750 DRALORIC EDPU6X9 CC 15PF+-2X3X4NP0 5 RA-1 DRALORIC 19 0282 DRALORIC DRALORIC CC 4,7NF+ Datum DRALORIC ROEDERST DRALORIC TEXAS 4 TEXAS TEXAS MCL 80 ROHDE & SCHWARZ MIQ NC NEM È Kannzeichen C10 C11 C 1 2 C13 C14 **B12** 010 B11 813 B14 6 S 5 3 5 30 5 C B 5 69 86 97 88 8 82 ŝ B.4 83 r rəərii tərəgərinin əsəli milli grunnəməy ətguladır. ərəbərəə bru 0 1 3 ı Blact - ENDE onthalten in ŠÅ Sachnummer 302-6215 AK 010-4680 AK 010.4680 AK 010-4680 AK 010.4680 AK 010.4680 AK 010.4680 AN 214.7685 AK 010.4680 AK 010,4680 RF 069.1012 RF 069.2219 RF 069_3315 RF 069.2219 AM 010.8591 RF 069.2219 RF 069.1041 RF 069.2202 069-5101 RF 069 4705 069.3909 Sachnummei R.F. RF DRALGNIC LCA0207/--5%220 RF 0,25%100%04M +-5% DRALORIC LCA0207/+-5%100K RF 0,25%330 04M +-5% RF 0,254 22 0HH +-52 DRALORIC LCA02074+-522 RF 0,254 51 0HH +-52 DRALORIC LCA0207/4--525 RF 0,254220 0HH +-52 1 51 NPN 40V0,2A 2N23694 1 51 NPN 40V0,2A 1 51 NPN 40V0,2A 1 51 NPN 40V0,2A 2N23694 ALORIC LCA0207/+-52100 0,254 47 04M +-52 LORIC LCAD207/+-57220 0,254100 0HM +-57 LCA0207/+-5X330 SILICONIX J111A AK 2N2369A SI NPN 40V0,2A Valvo. 2N2369A AK 2N2369A SI NPN 40V0,2A AM ZN4416 N-KAN_JFET 30V TEXAS ZN4416 AK 2N2369A SI NPN 40V0,2A ZN2369A SI NPN 40V0,2A LORIC LCA0207/+-5247 0,254 39 OHM +-52 AK 2N2369A SI NPN 40V0,2A Valvo 2n2369A LCA0207/+-5239 AM J111A N-KANAL-FET 35V ED REFERENZ Schatteiliste für Benennug / Beschreibun ZN2369A 2N2369A ENTHALTEN IN VALV0 2 AK 2N23694 5 VALVO. 2 Ak 2N2369A 9 Valvo 3 VALVO AK 2N2369A 9 2N2369A 32 0282 Datum DRALORIC DRALORIC DRALORIC 302 . 6215 DRALORIC DRALORIC VAL VO VALVO Ŋ ROHDE & SCHWARZ ¥¥ Ē ž ч С 110 ST6 R 6 5 870 R71 R72 R 5 & R 6 0 8 Ó 1 8 6 Z R63 R64 16 77 18 19 2 Τ2 17 75

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	19 0282 ED INTERPOLATIONSOS-SOKHZ	50KHZ 302_6415	15 SA
Kennzelchen	Banennung / Baschraibung	Sachnummer	enthattein in
C44	CK 22NF+-202250V QUADER	CK 006_5156	
C 4 5	DNF+-20%100V QUADER DST 4KY1822-410.00	CK 006.5033	
C46	Ŧ	CE 022.7589	
C47	SIEMENS 141310-8/4/0-2 CE 47UF -10+100216V 9X13	CE 022.7543	
C & B		CC 087-6906	
C 2 0	LORIC 15 UF+	087	
c51	1,00154~2	CE 022.8185	
c52	4 2NF +-1	CC 087.7102	
c53	C ED	cc 087_7102	
C56	DRALORIC EDPU6X9/4700/10R2000 CE 1.006+-20235V 5X 4X 7	0 CE 022.8185	
	TAL TA-ELK		
C 55	CK 47NF+~20X250V 9UADER 1TT MKC42544-14747	180	·
C5 6	70Pf+~1	CC 087-6993	
C57	DWALGALC - EUPUSX4 /4/0/104K2000 CC 2,2NF+-10%5X6R2000	cc 087.7060	
C 5 8	EDPU5X6/2200/10)+50%7X8R6000	JU CC 087.7525	
	HOMSON C TONE=20+	+5 CC 087.7525	
	OMSON COX767/		
C 6 U	ZZUPF+-ZZ Aloric E	100 11	
C 6 1	Ĩ	CC 087_6912	
GL1	1 IS 87478	AD 012-0700	
612	VALVO IN4448 AD IN4448 SI 75V 150M LA	AD 012-0700	-
	LVO 1N4448	55.	
613	SIEMENS BOII3	AE 6764711	
615	AD 114448 SI 75V 150MIA	AD 012.0700	
GLÓ	4448 SI 7	AD 012-0700	
617	VALVO 1N4448 AD 1N4448 SI 75V 150MIA	Ab 012.0700	
a 19	VALVO 1N4448 AD 1N4468 ST 75V 150MIA	AD 012-0700	
, , , , , , , , , , , , , , , , , , ,	VO 1N4448		
619	AD 1N4448 SI 75V 15UMIA Valvo 1N4448	00/0-710 QV	
6110	N4448 SI 7	Ab 012.0700	
6411	ALVO 7N4 E 88113 3FACH	AE 252-5711	
61.12	SIEMENS BOTIS AD IN4448 SI 75V 150MIA	A0 012.0700	
	LVO 114448	AN 012 0700	
9 1 2	8555NL	5	
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Sachnummer	302-6415		-2764	-7102	7.7102	1-6441	7-7048	7-7525	2.8091	2.0784	7.7054	2.0784	7.7525	7.7525	7_7525	.758	18-4007	12-7543	17.7525	37.7525	37.7525	87.7525	87.7525	087.7525	087.7525	087-7525	087.7525	022.7589	087.7525	022 .8091	-
	NSOS SOKHZ	Sachnummer	cc 022	CC 087	cc 087	cč 087	CC 087	8	-	CC 022		2	<u></u>	-20+5 cc 087	-20+5 cc 087		CE 208	CE 022			E	<u> </u>		ງ ງ ງ	-20+5	22		30	2) 	r-2045 2715 2715	
Schalttelillste für	ED INTERPOLATIONSOS_SOKHZ	Benennung / Beschreibung	NPO/25/2883X12LC X N22D/IA 3 ROHR	98200	X982	0 4 N 5 X	-10%4%5R2000	15001078 6000 6000	-20216V 7X 5X11	TA-ELKO E X63V K2000	202 X	2002	EDPU4X5/63VT000 +5027X8R6000	C0X767/10000/PF +5027X8R6000	C 0 X 7 6 7 / 10000 / P F 20 + 50 X 7 X 88 6000	COX767/100 10+100240V 1	7476 13X	2 2	7 X 8 R	0X767/ 027X8R	88	20+5027X8R6000 C0X767/10000/PF	20+5027X8R 6000 60X767/10000/	X886000 677100007P	886000 7/10000/P	20+50%7X8R6	28X	100240			
	19 0282	Benenr	RALORIC C 27PF 2	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	×~~~	! - :	CC 1 SNF+	25	22 UF	10-TAN 116+	1,8N	* +	DRALORIC CC 10NF-20	THOMSON CC 10NF-20	THOMSON CC 10NF-2		DOUF	E K S T 7 U F	S N C	N 0 0 0	2 N N N N N N N N N N N N N N N N N N N	CC 10NF-	10NF-	00	CC TONF- THOMSON	CC 10NF-	CC 10NF-	CE 47UF	z z S	CE 22 UF+-	
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Nesse frankrige ist transe Ergentum Vervie Diese Univertung, Mitteining an andere und schedensrationing

SIL.

Blatt Nr. enthalten in Ş Sachnummer 302-6615 CC 087.7525 CC 087.6912 CE 022.7589 CE 022_7589 CE C22.7589 CE 022.7589 C87 -7525 cc 087.7102 CC 087-6441 CC 087-7048 CC 006.1009 CE 022.8091 cc 087.7102 CC 022.0784 BL 468.5976 CK 087.4049 CK 006.4672 CC 022_2764 BL 302.6038 BL 302_6638 BL 302-6050 BC 092.8155 BL 530.5805 BL 302-6438 BL 303-8986 BL 302-6444 BL 302_6038 BL 530.5805 B0 275.0822 Sachnumme ED INTERPOL.-OSZ.- 100 HZ С С THOMSON COX767/10000/PF-20+5 CC 10NF-20+50x7x8R6000 E & PU & X9 / 4700 / 108 2000 DRALORIC EDPU6X9 /4700/10R2000 DRALORIC EDPU4X5150010XR2000 CC 120PF+-2X5X6N750 CE 47UF -10+100X40V 11X13 SIEMENS B41316-B7476-2 ROEDERST MKCT862-422/0 CK 22N1+--20X160V RM15KC CK 22N1+--20X160V RM15KC CC 27FF 2X N220/1A 3 R0HR DRALORIC N220/1A 27/2RD3X10LC ERO-TANTAL TA-ELKO ETR3-22/15 CC 1NF+-10263V K2000 DRALORIC EDPU4X5/63V1000/10X DRALORIC EDPU3X4/15/2X/NPO BL SN54LS193J 4BIT-COUNT -TEXAS SN54LS193J/R&S-LV BL SN54LS193J 4BIT-COUNT_ Texas SN54LS193J/R&S-LV TEXAS UA741MJG BL SN54LS290J DE2.ZAEHLER TEXAS SN54LS290J BL SN54LS290J DE2.ZAEHLER BL SN54LS290J DE2.ZAEHLER TEXAS SN54LS290J CC 2PF+-0,25PF P100/1838. CE 47UF -10+100%40V 11%13 SIEMENS B41316-87476-2 CE 47UF -10+100%40V 11%13 CE 47UF -10+100%40V 11%13 CE 47UF -10+100X40V 11X13 B41316~B7476~2 BL SN545169J 49IT-COUNTER 841316-97476-) BL HEF40278 2/JK-FLIPFLOP BL HEF4029B UP/DOWN-COUNT CE 22 UF+-20%16V 7X 5X11 MC140468CP MICROP. PLL D8243 4X4BIT-1/0-EXP-BL SN54574J 2/D-FLIPFLOP 220NF+-20X100V QUADER SN54LS04J 6/INVERTER CC 10NF-20+50X7X8R6000 CC 1, 5NF+-1024X5R2000 CC 4 7NF+-10%6X9R2000 DRALORIC EDPU6X9/47(CC 4,7NF+-10Z6X9R2000 MC14046BCP HEF40278P Schaltteilliste für HEF4029BP SN5451693 SN54LS04J Senennung / Beschreibung S N5 4 S 7 4 J CC 15PF+-2X3X4NP0 UA741MJG OP-AMP D 8243 16 0282 Datum MOTOROLA **SIEMENS** SIEMENS FEXAS TEXAS VALVO VALVO TEXAS INTEL N 8**C** ť 00 ROHDE & SCHWARZ MUNCHEN Kennzelchen È C16 C20 C25 **C1**2 C1 S C21 C22 C23 C2 6 010 5 B10 811 812 5 5.5 5 N 88 6**8** 5 83 85 86 87 94 8 92 O ANTICAS DE C All I

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ROHDE & SCHWARZ	MOHCHEN	Kennzeichen	, R67	R70	R71	R72	R73	R74.	R75	876	R 7 7	R78	879	R80	R81	R82	.R83	R84	R 8 5	R86	R87	80 8 80 8 8	X 0 X		891	R93	4 4 4	R95 R96	897	898	

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B10	CD40018E	BL 086.6960	
8 1 1	ALA CD40148E BUIT-SHIFTREG	BL 086,7038	
B12	R VAR 22 CD40148E 8017 CD40148E 8017	9L 086.7038	
813	ER VAR 22 SN54L524	BL 262_3634	
814	XAS SN54LS244J SN74LS04N 6/1NVERTER		
815	74LS04	BL 266.2010	
B16	TEXAS SN74LS040N BL SN74D5N 0+70 HEXINV YEVXS SN74D5N	BL 009.3460	
817	н 1 н 1 н 2	202-2157	
818	VAR 22 555V TIMER TICS NE55	80 418.1383	
BU27	FUER VAR 22 Fr JC-FASSUNG 16 POLIG EURO-DIP PU16 02 IM MAGAZI	FR 249_6091	
BU28	C-FASSUNG 24POLIG -01P BU2402 14 MAGAZIN	FR 087_5480	
C1	Ŧ.	cc 087.7019	
C 2	22PF+-2%	CC 087-6464	
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Diese Unierlage ist unser ມີຖອກໂນກ. Verviellähgung. ມາໄລຍົມຮູາະ Verwertung, Mittailung an andere ist atratioa und schedenersenstruchflich

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Schaltteilliste für SMS_B2 1	- 30-0H0	genenning / geschreibung	E 1GHZ	E1 106H2 17 1214-001	E1 106HZ 17 1214-001	F-CABLE	F-CABLE	E 22 E 30									
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	Ň	B0 275-0822	335_0368		6113	VALVO DIODE1N4448 Ad 1n4448 SI 75V 150MIA	AD 012.0700	335,0368
	TEXAS UA 741MJG BO LF256H BIFET-OP_AMP_ NSC LE 256 H DO LE256H BIFET-OP AMP	B0 302,5883 B0 302,5883	335.0368 335.0368		GL 14	VALVO DIODE1N4448 Ad 1N4448 SI 75V 15OM1A Valvo Diode1N4448	012	335-0368
	•		335-0368		٢3	LD 1,50UH1020,220HM0,745A Delevan Drossel 3025-24	LD 067_2886	335.0368
	BD HF-SCHALTER Z	913.4408	335.0368		L3	101	LD 067_2863	335,0368
	CC 33PF++10%100V2NP0 CHIP FRIF 8000-271-100-C06-3	-330 -330	335-0368		L.4	LD 1,000H10X1,100HM0,310A	LD 067_2863	335-0368
	50F+-20125V 51 41 7 50F+-20125V 51 41 7 50F+-20125V 51 41 7	CE 087.9334 CE 087.9334 CE 087.6964	335.0368 335.0368 335.0368		IJ	DELEVAN 10272,00400,038A DELEVAN DROSSEL1025-92	L.0 037_8005	335_0368
	DRALORIC EDPU6X9/330/22N750 CE 1.5UF+-20225V 5X 4X 7	0 CE 087-933	335.0368		R1	RF 0,05W 42,16 0HM+-1% DESTSTA DESTSTAT 46/1/0 05	030-0780	335.0368
	33PF+-2	CE 087.9334 CC 087.6487	335.0368 335.0368		R2	10K0H# ++5%	RF 069.1035	335-0368
	LORIC 1,5NF+-	3	335-0368	transe Snuđ	R 3	ň	RF 069.5601	335-0368
	DRALORIC EDPU4X5150010XR2000 CC 1.5NF+-1024X5R2000	00 cc 087_7048	335_0368	dElleivn Sárast	R4	RF 0,05 4 434,0 0HM +-1% 05 0 0HM +-1% 050 0HM +-1\% 050 0	030,1205	335.0368
c10	DRALORIC EDPU4X5150010XR200 CC 150PF+-2X6X9N150	CC 087.673	335.0368	an mun Sing an ar Sing an ar	R5	RL 0,25W 43,2K0HM+-1%TK50 bBALDAIC SMAD207/4% 2K-F=FC	RL 083-1774	335_0368
c11	DRALORIC EDPU6X9/150/2XN150 CC 10NF-20+5027X8R6000		335 .0368	 99⊟ Tear TuliettiM Eanereb	Ró	RF 0,25W390K0HM +-5%	RF 069.3944	335.0368
c12	THOMSON COX767/10000/PF-20 CC 10NF-20+50%7%8F6000	CC 087.752	335,0368	ertos bri Bouhaw Robi Bouhaw	R7	RF 0,254 1 MOHM +-5% BPALOPIC I CA0207/4-5%	RF 069.1058	335.0368
c13	THOMSON COX767/10000/PF-20 CC 2,7NF+- 5%100V NPO VIE	+5 CC 060_094	335.0368	n nə¥ətru ə	88	RF 0,2541,2K0HM +-5%	RF 069.1229	335-0368
C 1 5	ERIE 8133-100-C06-2,7NF CC 10NF+-10% 50V3K1200 CH	r-J CC 082.334	335 0368	şəqun şəq	R9	RF 0,254 47 0HM +-5% DPALORIC ICA020774-5%	RF 069.4705	335.0368
c16	ERIE 8013-271-W5R-103-4 CC 10NF+-10% 50V3K1200 CH	-K CC 082.3344	335.0368		R12	RF 0,2543,9K0HM +-5% 0k	RF 069.3921	335.0368
c 2 0	ERIE 8013-271-W5R-103-) CT 3 PF N333 F.GEDR.SCH	CT 066_804	1 0	;	R14	2 <u>0</u> .	RS 087.7590	335 . 0368
c21	STETTNER 5STRIKO042,5/5PFN033 CC.1,8PF+-0,5PF100V3,3NP0	4033 CC 092_7236	335.0368		5 1 4	RF 0,254390K0HM +-52 DRALORIC LCA0207/+-52390K	RF 069.3944	335.0368
611	AD 144448 SI 75V 150MIA	AD 012.0700	335,0368		R16	RF 0,25W560 0HM +-5% DRALORIC LCA0207/+-5%560	RF 069.5618	335 . 0368
612	4448 5	AD 012.0700	335.0368		R 20	R1 0,25W 499 0HM+-1XTK50 DRALORIC SMA0207/4990HM-F-0	RL 083.0410	335-0368
61.5		AE 012.8018	335.0368		R 2 1	RL 0,25W 2,43K0HM+-1%TK50 Draloric Sma0207/2,43K+F-0	RL 083_0884	335_0368
6L6	-290	AF 012.8018	335°0368		R22	RL 0,25W 2,55K0HM+-1XTK50 DRALORIC SMA/207/2,55K-F-C	RL 082.2354	335,0368
617	HEWLETT 5082-2900 AE BZX55/C8V2 0,5W 2+D1	AE 012.2490	335.0368		823	S 0,5W500K0HM+-20%10X10X	AS 247.7926	335.0368
6L8	THOMSON BZX55/C8V2 AD 114448 SI 75V 350MIA	AD 012.0700	335-0368		R 24	5W270 TC	RF 069_2748	335.0368
619	VALVO DIODE1N4448 AD 1N4448 SI 75V 15DMIA		335 . 0368		R25	RF 0,2542,7K0HM +-5% 0841.081C ICA02077+-5%	RF 069.2725	335.036
GL 10	VALVO DIODE1N4448 Ad 1n4448 SI 75V 150Mia	AD 012.0700	335.0368		826	RF 0.25% 10K0HM +-5%	RF 069.1035	335-0368
6L 12	VALVO DIODETN4448 AD 1N4448 SI 75V 150MIA	Ab 012.0700	335.0368		R 7 7	RF 0,25W 47K0HM +-5% DRALORIC LCA0207/+-5%47K	RF 069_4734	335,0368

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335.0716.01 335-0716-01 335.0716.01 335-0716-01 335-0716-01 335-0716-01 335.0716.01 335.0716.01 335_0716_01 335.0716.01 335-0716.01 335.0716.01 335.0716.01 335-0716-01 335.0716.01 335-0716-01 335.0716.01 335-0716-01 335_0716_01 335.0716.01 335.0716.01 335.0716.01 335-0716-01 335.0716.01 Blatt Nr. 1 - ENDE enthalten w 335.0716.01 SA Sachnummer RF 069-1041 RF 069-2725 069.2725 069.3344 AK 010-5163 012.0700 AD 012-0700 082.1764 RL 082.2348 RF 069.1041 RS 247.7961 069.1058 069.1035 022_8191 AD 012.0700 082.1764 913.6700 AD 012.0700 AD 012.0700 AE 012.2432 80 418-1383 CE 022.8185 092.0777 302.7034 Sachnummer RΕ 8 s. N ٩Þ Ľ ť RF ED SHS-B3 UEBERSP.SCH. ដ B CE 2,20F+-20X35V 7X 5X11 ERO-TANTAL TA-ELKOETR2,2,2/35 CE 1,00F+-20X35V 5X 4X.7 CKROSBX104KLEVELR SMA0207/5,11K-F-C DRALORIC LCA0207/+--521,0M RF 0,254100K0HM +--52 SMA0207/100K-F-C 100K0HM+-1XTK50 LCA0207/+-522,7K ето стати с ER0-TANTAL TA-ELKOETR1-1/35 CC 100NF+-10% 50V5K1200LR SHA0207/100K-F-C LCA0207/+-5X100K LCA0207/+-5x100K L CA0207 / + -5 Z2, 7K RL 0,25V 5,11K0HM+-1TTK50 10-2 NS'0 AK BCY59IX NPN 45V 200MA Siemens bcy59ix RS 0_542K0HM+-20210X10X5 BOURNS 3386X-1-202 CA3240E BIMOS BUAL OP. 1 CA3240E 100K0HM+-1XTK50 AD 114448 ST 759 150MIA VALVO 114448 AD 114448 SI 759 150MIA VALVO 114448 AD 1N4448 SI 75V 150MIA VALVO 14448 Ad 1144448 SI 75V 150MIA AD 1N4448 SI 75V 15DMIA BOURNS 3386X-1-202 RF 0,254 1 NOHM +-52 Schattelliste für Benennung / Beschreibung 8 ZX 55 / C 4 V 7 RF 0,2542,7K0HM +-5% RF 0,254100K0HH +-5% BO RESSSY TIMER Signetics NESSSY BD MESSKOPF SMFP 1 N 4 4 4 8 1N4448 AE BZX55/C4V7 Valvo BZX 09 1181 DRALORIC Datum RL 0,254 1 DRALORIC RL 0,254 1 DRALORIC DRALORIC DRALORIC DRALORIC AEROVOX Ϋ́ς VALVO VALVO 80 C RCA ROHDE & SCHWARZ MUNCHEN Kennzeichen--Ì GLS R 10 £11 R12 £13 613 614 GL 2 GLÓ 6L1 R 9 Ľ 83 86 - 2 R 8 2 53 5 8 82 83 5 23 ערבים להומוראיטר גיז האיים 3 בעימים איני אויקוטער איז אווייון איזןאיזאטער איז איז איזאין צעון איזאט איז איזער איזער איזער איז איזעראיזער איזער איזאער איז איזער) . (Blatt Nr. 1 335-0368 335-0368 335.0368 335.0368 335-0368 - ENDE enthalten in 335-0368 Ş Sechnummer 335.0368 AK BCY59IX NPW 45V 200MA AK UIULDIU SIEMENS TRANSIST_BCY59IX AK 010_3777 AK BCY79IX PNP 45V200MIA AK 010_3777 SIEMENS SI-TRANSISTORBCY79IX 335.0380 FP 242.3600 249-9684 FJ 249-9684 Sachnumme 2 N FJ EINBAUWINKELST. SHC Radiall R 112 669 FP ∐NDIREKT.STECKERL.36P. Berg 75160-102-36 FJ EINBAUWINKELST. SMC Padiall R 112 649 Schattelliste (ür Benennung / Beschreibung SCHALTER 10 KONTAKTE Detum 0481 RELAIS 뇞 : ROHDE & SCHWARZ MÛNCNEN Ì Kennzelchen The owner of 815 514 **ST2** RS1 **STS** 12

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Stromläufe Bestückungspläne Circuit diagrams Components plans





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Ansicht und Leitungsführung Bauteilseite View of tracks on component side

Für diese Zeichnung behalten wir uns alle Rechte vor

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Ansicht und Leitungsführung Lötseite View of tracks on solder side

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C83 1,5n 1,5n 1,5n	C55 4,7n GL55 BA182 C56 4,7n	$B4, B5$ $3 \underbrace{\overset{4}{\overbrace{}}_{2} 1$	B 6 • • • • • • • • 1 Drautsicht Top view	в (• 9 Т70,	с • Е Т 71, Т 72	+5V -15V -15V -15V	
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Ansicht und Leitungsführung Bauteilseite View of tracks on component side

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Ansicht und Leitungsführung Lötseite View of tracks on solder side



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		main period for each off	
ME 52 19 7.78	home Nk	Phasenregelung Phase control	z
ROHDE & SCHWARZ		302.5819	4km/1+ - 3 - 8
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Frequenz SMS	Umsetz – frequenz	MP 7	MP 8
frequency SMS	Converter frequency		
330 MHz	300 MHz	+15 V	-15V
360 MHz	340 MHz	- 15 V	-15V
390 MHz	420 MHz	+15 V	+15V
420 M Hz	460 MHz	-15 V	+ 15 V

Umsetzfrequenz converter frequency













Draufsicht top_view

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Ansicht und Leitungsführung Bauteilseite View of tracks on component side

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Ansicht und	Leitungsführung Lötseite	
View of track	ks on solder side	
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ROHDE & SCHWARZ	302.6215 302.4012V erste: 302.4012	
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Moße ohne Foleranzangabe		Moñstat. Haltzeug Wirkstoff	
IGME Tog Bearb 22.8.78 Gepr Norm	Name . Wm	Benemung Interpolationsoszillator-50 kHz Interpolation oscillator 50 kHz	z
	SCHWARZ	302 6415 4	N* Bi
Zigen SMS		reg 302.4012V erst 302.4012	

Ander unos Mittediuros



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		VG-Sachnr	
ohne angobe		Mañstab Haibzeug, Werkstoff -	
Tog 2.8.78	Name Wm,	Benennung Interpolationsoszillator-50 kHz Z Interpolation oscillator 50 kHz	F
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Ansicht und Leitungsführung Bauteilseite

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E Tag rb 4.9.78 r	Nome Wm.	^{Benennung} Interpolationsoszillator – 100Hz Interpolation oscillator 100Hz	z
	SCHWARZ	Zeichin -Nr 302 . 6615 2	
Gerol SMS		reg : V 402.4012 V erste z 402.4012	BI



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Fur diese Zeichnur 11479 uns die Bechnur 1

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Ansicht und Leitungsführung Lötseite View of tracks on solder side

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Image: Signal and Signal				
Image: Signal and Signal			·.	
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			Zeichn - Nr 302.6615 reg + V 402.4012V erste 2 402.4012 Blott-Nr 3 v Bt v Bt	



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Ansicht und Leitungsführung Bauteilseite View of tracks on component side





Ansicht und Leitungsführung Lötseite View of tracks on solder side

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Ansicht und Leitungsführung Bauteilseite



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Anderungs-	Tog -	2	ROHDE & S	SCHWARZ	Zeichn - Nr 302.7911	Biati-Ne 3
			Nom		Keyboard / display	
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Ansicht und Leitungsführung Bauteilseite View of tracks on component side





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he	ROHDE & SCHWARZ MUNCHEN			Zeichn - Hr 302.6815 reg : V. 302.4012V erster z. 302.4012		

Anderungs-Mitterlung

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