



Model 7059 Low Voltage Scanner Plug-In Card Instruction Manual

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Keithley Instruments, Inc. warrants the following items for 90 days from the date of shipment: probes, cables, rechargeable batteries, diskettes, and documentation.

During the warranty period, we will, at our option, either repair or replace any product that proves to be defective.

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SPECIFICAITONS

MODEL 7059 LOW VOLTAGE SCANNER CARD

CHANNELS PER CARD: 10.

CONTACT CONFIGURATION: 2-pole Form A, common guard connection.

CONNECTOR TYPE: Screw terminal, No. 18 AWG maximum wire size.

RELAY DRIVE CURRENT: 13mA per relay typical.

MAXIMUM SIGNAL LEVEL: 200V, 100mA, 2VA peak (resistive load).

CONTACT LIFE: 10 * closures (cold switching); 10* closures (at maximum signal level).

CONTACT RESISTANCE: $< 2\Omega$ to rated life.

CONTACT POTENTIAL: $<1\mu$ V input to output with copper leads (<200nV typical <1 minute after actuation).

WARM UP: 1 hour for thermal stability.

ACTUATION TIME: < 2ms, exclusive of mainframe.

CHANNEL ISOLATION: >10¹² Ω and <10pF.

INPUT ISOLATION,

Differential: >10° Ω and <50pF.

Common Mode: >10° Ω and <150pF.

COMMON MODE VOLTAGE: <200V peak.

ENVIRONMENT,

Operating: 0° to 50°C, up to 35°C at 70% RH.

Storage: -25° to 65°C.

DIMENSIONS, WEIGHT: 32mm high \times 114mm wide \times 272mm long (1¹/₄" \times 4¹/₂" \times 10³/₄"). Net weight 0.35kg (12¹/₂ oz.).

NOTE

Because of the high impedance of the board special care should be taken in both handling and using the board to prevent degradation of performance. Handle the board only by the edges when using it and keep the board free of body oils, dirt and contaminants. To clean the board use Freon[®] TMS or TE or equivalent and a clean cotton swab or soft brush. Care should be taken to ensure that the solvent is not contaminated before using it on the board. After the solvent has been applied blow dry the board with dry nitrogen gas.

SAFETY SYMBOLS AND TERMS

The symbol \triangle denotes that the user should refer to the operating instructions.

The symbol \mathcal{N} denotes that a high voltage may be present on the terminal(s).

The **WARNING** used in this manual explains dangers that could result in personal injury or death.

The **CAUTION** used in this manual explains hazards that could damage the instrument.

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SECTION 1 GENERAL INFORMATION

1.1 INTRODUCTION

The Model 7059 is a low-voltage scanner plug-in card which is field-installable in the Model 705 and Model 706 scanner mainframes. The Model 7059 will switch up to 10 channels. For low-level transducer and thermocouple output switching the Model 7059 is designed to introduce a minimum of thermal voltage error (< 1 μ V input to output with copper leads), and ensure that high isolation (10¹² Ω) is maintained between input signals. Expected relay life (10⁶ closures) is obtained when signals less than 10V or 10mA are switched. However, peak signals up to 200V or 100mA may be switched with the scanner. Quick disconnect screw terminals are used to facilitate input and output connections to the scanner plug-in card (relay card). The plug-in card is easily removed through the rear panel of the scanner mainframe.

1.2 WARRANTY INFORMATION

Warranty information is stated on the inside front cover of this manual. If there is a need for service, contact the Keithley representative or authorized repair facility in your area. Check the back cover of this manual for addresses. The service form supplied at the end of the manual should be used to provide the repair facility with adequate information concerning any difficulty.

1.3 MANUAL ADDENDA

Improvements or changes to this manual will be explained on an addendum included with this manual.

SECTION 2 OPERATION

2.1 INTRODUCTION

This section provides information needed to use the Model 7059 with the Model 705 and Model 706 mainframes.

2.2 WIRING AND INSTALLATION

- 1. Wiring Configuration—The Model 7059 has a guarded, 2-pole switching configuration. The Model 7059 will switch any one of the 10 signals to one output, or switch one signal to any one of 10 outputs.
 - A. Wiring is accomplished by means of barrier strip terminals.
 - B. Signal path resistance is typically less than 0.5Ω per contact; less than 2Ω at end of life.
 - C. A guard surrounds all analog signal paths..
- Installation Refer to the Model 705 and Model 706 Instruction Manuals for scanner card installation instructions.

2.3 OPERATING CONSIDERATIONS

- Signal Level 10V peak, 10mA peak with a resistive load for expected life. Absolute maximum peak instantaneous rating: 200V, 100mA, or 2VA with a resistive load.
- 2. Isolation Guarded interchannel resistance is nominally greater than $10^{12}\Omega$ and less than 10pF at room temperature, at less than 70% relative humidity.
- 3. Maximum Levels -- 200V peak between signal line pairs or from signal lines to guard or mainframe (digital) common.
- 4. Operating Environment -- 0° to 50°C up to 35°C at 70% relative humidity.



Figure 1. Plug-In Card Installation and Assembly

2.4 OPERATING HINTS

The clamp-type screw terminals will accept up to #18 AWG wire. In order to ensure thermal offset less than $1\mu V$ solid copper wires should be used throughout the measurement hook-up.

To most effectively eliminate error voltages produced by leakage currents, the GUARD terminal should be connected to the reference connection on the channel which is most sensitive to error (that is, the one which has the lowest source voltage versus the highest series resistance). For instance, two sources are connected to the Model 7059; one having an equivalent source of 1V with a series resistance of 100k Ω , and the other having a source voltage of .1V with a 1k Ω series resistance. The 100k Ω source resistance will develop 100 times the error voltage as the 1k Ω source resistance. Its source voltage however, is only ten times as large on the 1V/100k Ω channel. The GUARD would therefore be connected to the reference point of this channel (see Figure 2).

Reactances in the system will cause transients during switching. These should not exceed the ratings given in the specifications. If they do, relay contact life will be degraded.



Figure 2. How To Use GUARD Connection

SECTION 3 MAINTENANCE

3.1 INTRODUCTION

This section contains a performance verification procedure. Since there is no calibration adjustments, no recalibration is necessary. Recommended maintenance would include inspection of the scanner plug-in board and card-edge connectors to ensure good electrical contact. In industrial environments annual cleaning using dry nitrogen gas and Freon[®] TMS or TE is recommended. The verification procedure should be performed every 12 months or at the time maintenance is performed on the mainframe.

3.2 REQUIRED TEST EQUIPMENT

Recommended test equipment is given in Table 1. Test equipment other than recommended may be substituted if specifications equal or exceed the stated characteristics.

ltem	Description	Minimum Specification	MFG	Model
Α	Microvoltmeter	Zero drift less than 150nV.	КІ	181
В	Accessory Shorting Plug		КІ	1488
С	Accessory Low Thermal Cable		кі	1506
D	Chart Recorder		H-P	7035B
E	Signal Generator	TTL compatible square wave to 10Hz, and 1 pulse per 30 min.		
F	Scanner Mainframe		KI	705
G	Ohmmeter or Multimeter	1Ω Sensitivity	кі	195
Н	Accessory Kelvin Leads		KI	1641
	Oscilloscope	Triggered Sweep	TEK	7000 series
J	Power Supply	10.00V, 100V DC	KI	230
К	Electrometer	10 ⁻¹⁰ A Sensitivity	_KI	614 or 619

Table 1. Recommended Test Equipment

3.3 PERFORMANCE VERIFICATION

This section gives procedures needed to verify that the operation of the Model 7059 is within specifications. The thermal offset should be carried out in a temperature controlled environment of $23^{\circ} \pm 1^{\circ}$ C. Other tests may be performed within environmental limits given in the specifications.

3.3.1 Thermal Offset Test

1. The input of the Model 181 should be shorted with the Model 1488 shorting plug. After power is applied, the Model 181 should be allowed to stabilize for four hours. During this time the analog output of the Model 181 should be connected to the chart recorder and a recording made to establish a baseline for the voltage measurements (see Figure 3).

- 2. The 10 input connections on the Model 7059 plug-in card should be shorted together between HI and LO with a short piece of solid copper wire. That is, connect all the HI and LO terminals together as shown in Figure 4. (The OUTPUT terminals of the Model 7059 should be connected to short copper wires to facilitate connecting the alligator clips on the Model 1506 cable.) With the Model 1506 cable attached to the output, the plug-in card should then be inserted into the mainframe and power applied to the mainframe. Set the Model 705 to the reset mode and allow it to warm up along with the Model 181.
- 3. After the warm up time has elapsed and a stable baseline is visible on the chart recorder, the shorting plug on the Model 181 should be removed and the Model 1506 cable from the Model 7059 connected in its place. Set the Model 705 scanner mainframe to Channel 1, Channel mode and the Single program mode. The scanner mainframe should now be clocked at a rate of 1 channel every 15 (900 second interval time) minutes. The thermal EMF generated in each channel will be visible as deflection from the baseline on the recording. The maximum deflection should be no more than $\pm 1\mu$ V from the baseline for any channel.





Figure 4. Shorting HI and LO of all Channels on the Card

3.3.2 Contact Resistance Test

- Set up—Connect a short (1" or less) piece of copper wire (#18 AWG) to the input and also the output of the pole (signal HI or LO) or the channel to be tested (see Figure 5 for the test set up diagram).
- 2. Zero the Model 195 with the Model 1641 leads shorted together.
- 3. After the Model 195 is zeroed connect the leads from the Model 195 to the barrier strips on the Model 7059 via the short piece of copper wire on the terminal strips. Arrange the leads such that the plug-in card can be inserted into the Model 705 mainframe without disturbing the test lead connections to the terminal strips.

4. Insert the Model 7059 into the mainframe, apply power and select the channel which is to be tested. The total signal path resistance can now be measured. The reading should be <0.5 Ω upon initial shipment from the factory and <2 Ω after usage.



Figure 5. Test Set Up For Contact Resistance Test

3.3.3 Isolation Test

Channel Isolation

- A. This test measures the leakage resistance between two channels on the board. One channel is to be open and the other closed. Set up the test circuit shown in Figure 6.
- B. Short the HI and LO connections of each channel on the Model 7059.
- C. Set the Model 705 to the Channel mode, Channel 1 and the Step mode. Set the electrometer to Amps and program the Model 230 to output 100V. Take the electrometer out of ZERO CHECK. Program Channel 1 as open and the other channels as closed.
- D. Take the reading on the electrometer. The reading should be less than 1×10^{-10} A. Using Ohm's Law calculate the channel isolation. For example: $R = E/I = 100V/1 \times 10^{-10}$ A = 1×10^{12} Ω. Due to the capacitance of the circuit, the offset current may be high until the capacitance of the circuit is charged up. Wait until the readings settle out.
- E. Manually scan through Channel 1 through 10 repeating step C and D for each channel.

Input Isolation, Differential (Guarded)

- A. This test measures the differential input isolation which is the leakage resistance between a guarded channel's HI and LO connections. Set up the test circuit shown in Figure 7.
- B. Set the Model 705 to the Channel mode, Channel 1 and the Step mode. Set the electrometer to Amps and program the Model 230 to output 100V. Take the electrometer out of ZERO CHECK.
- C. Take the reading on the electrometer. The reading should be less than 1×10^{-7} A. Using Ohm's Law calculate the isolation (leakage resistance). For example: $R = E/I = 100V/10^{-7}A = 1 \times 10^{9}\Omega$. Due to the capacitance of the circuit, the offset current may be high until the capacitance is charged up. Wait until the readings settle out.
- D. Manually scan Channels 1 through 10 repeating step B and C for each channel.

Input Isolation, Common Mode

- A. This test measures the leakage resistance between signal lines and power line ground. Set up the test circuit shown in Figure 8.
- B. Short the input HI and LO terminals of each channel with a short piece of solid copper wire. Do not connect the channels together, just short the HI and LO terminals.
- C. Insert the Model 7059 into the mainframe and set the Model 705 to the Channel mode, Channel 1 and the Step mode.
- D. Set the electrometer to Amps and program the Model 230 to output 100VDC. Take the electrometer out of ZERO CHECK.
- E. Take the reading on the electrometer. The reading should be less than 1×10^{-7} A. Using Ohm's Law calculate the isolation (leakage resistance). For example: $R = E/I = 100V/1 \times 10^{-7}$ A = 1×10^{9} Ω. Due to the capacitance of the circuit, the offset current may be high until the capacitance is charged up. Wait until the readings settle out.

F. Manually scan Channels 1 through 10 repeating step D and E for each channel.



Figure 6. Channel Isolation Test Set Up







Figure 8. Common Mode isolation Test Set Up

SECTION 4 REPLACEABLE PARTS

4.1 INTRODUCTION

This section contains replacement parts information, a schematic diagram and component layout for the Model 7059.

4.2 REPLACEABLE PARTS

Parts are listed alpha-numerically in order of their circuit designation. Table 2 contains parts list information for the Model 7059.

4.3 ORDERING INFORMATION

To place an order, or to obtain information concerning replacement parts, contact your Keithley representative or the factory. See the inside front cover for addresses. When ordering include the following information:

- 1. Instrument Model Number
- 2. Instrument Serial Number
- 3. Part Description
- 4. Circuit Description (if applicable)
- 5. Keihtley Part Number

4.4 FACTORY SERVICE

If the instument is to be returned for service, please complete the service form which follows this section and return it with the instrument.

4.5 COMPONENT LAYOUT AND SCHEMATIC DIAGRAM

Figure 9 contains a component layout of the Model 7059 while, Figure 10 contains the Model 7059 schematic diagram.

Circuit Desig.	Description	Keithley Part No.
C101	Capacitor, 10uF, 25V, Aluminum Electrolytic	C-314-10
J1001 J1002 J1003 J1004 J1005	3 pin Terminal Strip (2 required) 10 pin Terminal Strip (3 required) 3 pin Terminal Strip 10 pin Terminal Strip 10 pin Terminal Strip	CS-457-2 CS-457-1 CS-475-1 CS-457-2 CS-457-2
K101 K102 K103 K104 K105 K106 K107 K108 K109 K110	Relay Relay Relay Relay Relay Relay Relay Relay Relay	RL-77 RL-77 RL-77 RL-77 RL-77 RL-77 RL-77 RL-77 RL-77 RL-77 RL-77
	MECHANICAL PARTS Clamp, Assembly, Upper a. Clamp, Upper b. Strip, Rubber Clamp, Assembly, Lower a. Clamp, Cable, Lower b. Strip, Rubber No. 6-32 x 5/16 Phillips Pan Head Screw	7055-303-03 7055-305 26621 7055-308 7055-307 26621
	(2 required) No. 6-32 x 1 Phillips Pan Head Screw (2 required)	

Table 2. Model 7059 Replaceable Parts



Figure 9. Model 7059 Component Location Drawing





SERVICE FORM

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Show a block diagram of Also, describe signal sour		all instruments connected (whether po	wer is turned on or not).
	nt being performed? (factory, control	lled laboratory, out-of-doors, etc.)	
		Ambient Temperature?	
Relative humidity?	Other?		
Any additional information	on. (If special modifications have bee	en made by the user, please describe.)	

Be sure to include your name and phone number on this service form.



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