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JOHN FLUKE MFG. CO., INC.

P. O. Box 7428 Seattle, Washington 98133



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AUGUST 1972

ADDENDUM MODEL 8100B DIGITAL MULTIMETER

INTRODUCTION

The Model 8100B is a Model 8100A without the GUARD terminal and enclosed in different styled plastic covers. Except for connecting the inner guard to INPUT LO, the instruments are, electrically, identical. Mechanical differences include the covers, carrying handle, removal of GUARD terminal (J3), and relocation of the 115/230 volt slide switch to the rear panel for direct access

MANUAL CHANGES

The manual requires only minimum changes. References in the text and schematic concerning the GUARD terminal should be disregarded. The List of Replaceable Parts, Section 5, should be changed according to Table 1.

REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	тот оту		USE CODE
J3		DELETE						
3		Cover, Bottom	303164	89536	303164	1		
4		Cover, Top	291989	89536	291989	1		
5	124	Handle, Carrying				L		
		Frame	310045	89536	310045	1		
		Grip	284836	89536	284836	2	1	
		Knob, male half	309047	89536	309047	2		
		Knob, female half	309054	89536	309054	2		
		** **						

Table 1. CHANGES-LIST OF REPLACEABLE PARTS

JOHN FLUKE MFG. CO., INC.

P. O. Box 7428 Seattle, Washington 98133





Rev. 1 7/1/71 Rev. 2 10/21/71 Rev. 4 2/11/72

Rev. 3 11/19/71

OCTOBER, 1969

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CHANGE/ERRATA INFORMATION TARGE ADDR

MANUAL

MODEL 8100A DIGITAL MULTIMETER TITLE:

Add resistor R212, 1M, between cathode of CR39 and smith

For listing of C33, Stock No. should be 234484.

October 1969, Rev. 4, 2/11/72 **ISSUE:**

SCHEMATIC NO. 1 (Ser. No. 3410 third 3419 and 4098 and on) Please make changes in this manual according to the following change and/or errata information: (216 to 150k, and delete C55, 4 bf.

CHANGE #1

Page 4-17 (Table 4-7. AC Calibration)

In step "e", change Input Signal frequency to 15 kHz. In step "g", change Input Signal frequency to 20 kHz.

Page 5-2

Add the following Serial Number Effectivities:

R Model 8100A serial number 5050 thru 6349.

S Model 8100A serial number 123 thru 5915

T Model 8100A serial number 5916 and on.

U Model 8100A serial number 123 thru 6279

V Model 8100A serial number 6280 and on

W Model 8100A serial number 123 thru 6349

X Model 8100A serial number 6350 and on Y Model 8100A serial number 61300 and on.

Page 5-21

Add "W" to Use Code column for listing of C51 and C55. Add another listing of C51 as follows:

/ C51 / G1–W3 / Cap, mica, 100 pf ±5%, 500V / 148494 / 14655 / CD15F101J / REF / 0 / X /

Page 5-31

Add "W" to Use Code column for R194. Add another listing for R194 as follows:

/R194 / G1-W2 / Res, comp, 2k ±5%, ¼W / 202879 / 01121 / CB 2025 / 1 / 0 / X /.

Add "U" to Use Code column for R204. Add another listing for R204 as follows:

/R204 / I3-X1 / Res, met flm, 499k ±1%, 1/8W / 268813 / 91637 / MFF1-8499K PORM 1 PCT / 1 / 0 / V /.

Add "S" to Use Code column for R211. Add another listing for R211 as follows:

/R211 / E5-M4 / Res, comp, $3.3k \pm 5\%$, 4W / 148056 / 01121 / CB3325 / REF / 0 / T / .

Add resistor R212 to parts list as follows:

/ R212 / 0 / Res, comp, 1 M Ω ±5%, ¼W / 182204 / 01121 / CB1055 / REF / 0 / Y /.

Add resistor R216 to parts list as follows:

/R216/0/Res, comp, 100 k ± 5%, ¼W / 148189 / 01121 / CB 1045 / REF / 0 / R /.

1/5/74

FORM NO. A-742

8100A ERRATA OTTAMPORMI ATAPPS 300AHO

Page 2 of 2

Add another listing for R216 as follows:

/R216 / 0 / Res, comp, 150k ±5%, ¼W / 182212 / 01121 / CB1545 / REF / 0 / X /.

SCHEMATIC NO. 1 (Ser. No. 3410 thru 3419 and 4098 and on)

Change C51 to 100 pf, R194 to 2k, R216 to 150k, and delete C55, 4 pf.

Add note six to NOTES as follows:



Add to CHANGES as follows:

(2)

C51 was 100 pf, R194 was 1.6k, R216 was 100k, and C55 was removed at S/N 6350 and on.

SCHEMATIC NO. 4

Add resistor R212, 1M, between cathode of CR39 and emitter of Q5. Add to CHANGES as follows:

(3)

R212, 1M, added at S/N 61300 and on

ERRATA #1

Page 5-21

For listing of C33, Stock No. should be 234484.

CHANGE # 2

Page 2-2, Paragraph 2-9, and Section VI, Accessories and Options, Page 6-4, Paragraph 6-15 Add after the above paragraphs the following note:

> Battery manufacturers recommend that nickle-cadmium batteries should not be stored for extended periods of time without recharging at least every 90 days. Storage temperatures below 25°C are recommended.

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Section 1 Introduction & Specifications

INTRODUCTION 1-1.

1-2. The Fluke Model 8100A Digital Multimeter is a versatile, accurate instrument, which measures ac and dc voltage in four ranges to 1000 volts and ohms in five ranges to 10 megohms. Standard features of the Model 8100A include full guarding, a switched two-pole active filter, autopolarity, pushbutton function and range selection, and full four-digit readout plus "1" for overranging. Fluke's unique Recirculating-Remainder (R²)A/D conversion technique enables multiple use of discrete and integrated components for the various operating modes, thereby reducing parts count and power consumption while increasing reliability.

Accessories and options for the Model 8100A 1-3. include the rechargeable battery pack (Option -01), High Frequency Probe, High Voltage Probe, Switched AC/DC Current Shunts, Front Panel Dust Cover, and Rack Mount Kit. A detailed description of the battery option and each accessory is given in Section VI of the manual.

1-4. ELECTRICAL SPECIFICATIONS

DC Volts

RANGE

Rev. 1

 ± 1 , ± 10 , ± 100 , ± 1000 volts dc with 20% overrange on all ranges.

RESOLUTION

0.01% of range (100 uv on 1v range maximum).

ACCURACY

30 days, 20°C to 30°C: ±(0.02% of input

+0.01% of range). 90 days, 15°C to 35°C: ±(0.05% of input +0.01% of range).

DC INPUT RESISTANCE

Constant 10 megohms on all ranges.

FILTER

Switch selected two-pole, linear phase active filter.

RESPONSE TIME TO RATED ACCURACY 0.5 seconds with filter out. 1.5 seconds with filter in.

REJECTION

		INTER	FERENCE	FREQUE	NCY
	1.1	60	Hz	50 1	tz Diras
	DC	FILTER	FILTER	FILTER	FILTER
CMRR (1K in Low Lead)	120 db	120 db	100 db	120 db	100 db
NMRR		60 db	20 db	47 db	9 db

NOTE: CMRR = Common Mode Rejection Ratio NMRR = Normal Mode Rejection Ratio CMRR approaches infinity when instrument is battery operated.

POLARITY

Automatic, instantaneous selection and display.

OVERLOAD

 ± 1200 vdc or $\pm 1700v$ peak ac can be applied indefinitely on any range without damage.

AC Volts

RANGE

1, 10, 100, 1000 volts ac with 20% overrange on all ranges.

RESOLUTION

0.01% of range (100 uv on 1v range is maximum).

ACCURACY

90 days, 15°C to 35°C:

MIDBAND	EXTENDED
50 Hz to 10 kHz	30 to 50 Hz and 10 to 20 kHz
±(0.2% of input +0.05% or range)	±(0.5% of input + 0.1% of range)

INPUT IMPEDANCE

1 megohm shunted by <30 pf.

RESPONSE TIME TO RATED ACCURACY 3 seconds

MAXIMUM DC NORMAL MODE VOLTAGE ±1200 vdc.

NOTE: The sum of DC Normal Mode Voltage and peak ac voltage must not exceed ±1700v.

OVERLOAD

 ± 1200 vdc or $\pm 1700v$ peak ac from 30 Hz to 20 kHz can be applied indefinitely on any range without damage.

Resistance

ACC WEST

RANGE

1k, 10k, 100k, 100k, and 10 M Ω with 20% overrange on all ranges.

RESOLUTION

0.01% or range (0.1 ohm on 1k range is maximum).

ACCURACY	1k to 1000k	10M
30 days 20 ⁰ C to 30 ⁰ C:	.±(0.05% of input +0.01% of range)	±(0.1% of input + 0.01% of range)
90 days, 15 ⁰ C to 35 ⁰ C:	_±(0.1% of input + (ranges	0.02% or range) on all

CONFIGURATION

Two terminal, constant current.

VOLTAGE ACROSS INPUT

1.0v at full range.
 1.2v at 20% overrange.
 9v with open terminals.

POWER IN UNKNOWN R

 ≤ 1 milliwatt in range and ≤ 1.44 milliwatts at 20% overrange on the 1K range, decreasing by an order of magnitude per range to 100 nanowatts at 10 megohm.

RESPONSE TIME TO RATED ACCURACY

	1k to 1000k	10M
Filter Out	0.5 seconds	3.0 seconds
Filter In	2.0 seconds	10.0 seconds

OVERLOAD

1k Range	130 vrms may be applied indefinitely
el cili si cin cin ci	without damage.
10k to 10 MΩ	230 vrms may be applied indefinitely
when the state of the state	without damage.

MAXIMUM INPUTS

See OVERLOAD specifications	by
function.	
100 vdc or peak ac.	
1200 vdc or 230v rms at 60 Hz.	
	function. 100 vdc or peak ac.

POWER

8 watts from 115V/230V, $\pm 10\%$, 50 to 500 Hz line with internal battery option -01. Eight hours continuous operation from the rechargeable nickel-cadmium batteries.

1-5. ENVIRONMENTAL SPECIFICATIONS

HUMIDITY RANGE

Meets all specifications to 80% relative humidity for temperatures $\leq 35^{\circ}$ C and 70% relative humidity for temperatures above 30°C to 50°C.

SHOCK AND VIBRATION Meets requirements of MIL-T-21200 G and MIL-E-16400F.

STORAGE TEMPERATURE -40° C to $+75^{\circ}$ C. -40° C to $+60^{\circ}$ C for batteries.

Operating Temperature 0°C to +50°C.

Temperature Coefficient

FUNCTION	TEMPERATURE COEFFICIENT
DC Volts	±(0.0025% of reading + 0.001% of range)/ ⁰ C
AC Volts	<u>+(0.015% of reading + 0.005%</u> of range)/ ^Q C
кΩ	<u>+(0.0035%</u> of reading + 0.0015% of range)/ ^O C
10 MΩ	<u>+(0.01% of reading + 0.002%</u> of range)/ ^O C

NOTE: Add temperature coefficient only outside of temperature limits noted in 30 and 90 day specifications.

1-6. MECHANICAL SPECIFICATIONS

Weight

8 pounds without batteries. 10 pounds with batteries.

Mounting

Tilt-down carrying handle detents into custom non-marring

feet and serves as a tilt-up bail for bench use. The Model 8100A outline drawing is shown in Figure 1-1, with approximate overall dimensions.

1-7. GENERAL SPECIFICATIONS

Selection

Function and range selection is manual via mechanically interlocked pushbutton switches.

Display

Four decade neon in-line readout with polarity neon for dc volts and fifth digit for 20% overrange. Automatic decimal location.

Sample Rate

Three samples per second.

Accessories Available

High Frequency Probes High Voltage Probes Switched AC/DC Current Shunts Front Panel Dust Cover Rack Mount Kit



Section 2 **Operating Instructions**

INTRODUCTION 2-1.

This section contains operating instructions and 2-2. applications information for the Model 8100A Digital Multimeter. Included in the instructions is a detailed description of the instrument controls, terminals, and indicators and an operational check, which verifies satisfactory operation of the instrument using only front panel controls. If any problem is encountered in operating the instrument, contact the nearest John Fluke Sales Representative or write directly to the John Fluke Manufacturing Company. Please include the instrument serial number when writing.

INSTALLATION 2-3.

The Model 8100A is supplied with non-marring 24. feet and tilt-down bail for bench or field use. Rack mounting kits for installation of one instrument or two instruments side-by-side in a standard 19 inch rack are available as accessories and are described in Section VI of the manual.

ACCESSORIES 2-5.

The following Model 8100A accessories are avail-2-6. able at additional cost. A detailed description of the accessories is given in Section VI of the manual.

High Frequency Probe	Front Panel Dust Cover
High Voltage Probes	Rack Mount Kit
Switched AC/DC Current Shunts	

INPUT POWER REQUIREMENTS 2-7.

The instrument operates on either 115 or 230 volt, 2-8. 50 Hz to 500 Hz ac power or directly from the optional battery pack. Before applying ac power to the instrument, remove the bottom dust cover and note the position of the 115/230 volt slide switch at the rear of the instrument. If the switch does not indicate the desired operating voltage, proceed as follows:

- Place the 115/230 volt switch in the position that a. corresponds to the desired operating voltage.
- Ensure that the proper line fuse for the selected b. voltage is installed before operating the instrument. For 115 volt operation, the fuse should be 1/8 ampere slow-blow and for 230 volt operation, the fuse should be 1/16 ampere slow-blow.
 - Replace the bottom dust cover and connect the instrument to the line.

WARNING!

c.

The round pin on the polarized three-prong plug connects the instrument case to power system ground. If a three-to-two-wire adapter is used or if the instrument is powered from batteries with the line cord disconnected, ensure that the instrument case is connected to a

high quality earth ground. This can be done by connecting a wire between the center pin on the rear panel ac connector and earth ground.

2-9. The optional battery pack supplies the operating power for the instrument whenever the ac power cord is disconnected from the ac line. The battery is charged by connecting the instrument to the ac line and turning the instrument on. The front panel meter monitors the charge on the battery during battery operation and during line operating it indicates LINE OPR (full scale). A detailed description of the battery option is provided in Section VI of the manual.

2-10. OPERATING FEATURES

2-11. The name and function of the front and rear panel controls, terminals, and indicators are shown in Figure 2-1.



Figure 2-1. CONTROLS, TERMINALS, AND INDICATORS (Sheet 1 of 2)

.

REF. NO.	NAME	FUNCTION
1	INPUT Terminals	Input connections for dc voltage, ac voltage, and resistance measurement.
2	GUARD Terminal	Connects to internal guard chassis. When properly connected externally, provides increased ac and dc common mode rejection. GUARD terminal may be connected directly to the LO terminal or disconnected from the LO terminal and driven by a separate GUARD potential.
3	FILTER ON-OFF Switch	Push on-push off switch selects the two-pole active input filter when de- pressed (ON) and reduces the amount of filtering when released (OFF).
4	Function Switches	Four pushbutton switches select the desired operating mode: VAC (ac voltage mode), VDC (dc voltage mode). K Ω (Kilohm mode), and 10 M Ω (10 megohm mode).
	RANGE Switches	Four pushbutton switches select the full-scale input range for voltage or resistance measurement. Ranges of 1, 10, 100, and 1000 are provided for each function except 10 $M\Omega$, which is fixed independent of the RANGE switch .
6 	POWER Switch	Push ON - push OFF switch controls input power to the instrument. When depressed, instrument is ON; when released, instrument is OFF.
7 (14) 449 (1) (4) -	Battery Meter	Indicates charge condition of battery when the battery option is installed and the instrument is operating from the batteries. When the instrument is on the line and the batteries are charging, the meter indicates LINE OPR (full scale).
8	Polarity Indicator	Neon indicator tube indicates "+" when positive voltages are applied to the instrument and "-" when negative voltages are applied to the instrument.
9	Overrange Indicator	Neon indicator displays "1" for full-scale readout and 20% overrange on each range. When a maximum overrange input is applied to the instrument, in any operating mode, the readout will be 11999, with the decimal point positioned according to range.
10 110	Readout Tubes	Neon indicator tubes display the instrument readout from left to right, with the most significant digit displayed by the left-most tube. Each tube also displays a decimal point which, is operated by the RANGE switches.
11	115/230 Volt Switch	Slide switch selects the ac operating voltage for the instrument. Switch is not accessible until bottom dust cover is removed. NOTE: Refer to paragraph 2-7 for conversion instructions.
12	AC Line Connector	Mates with three-wire line cord for connection to 115/230 volt, 50 Hz to 500 Hz ac line.

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Figure 2-1. CONTROLS, TERMINALS, AND INDICATORS (Sheet 2 of 2)

C.

e.

2-12. OPERATIONAL CHECK

2-13. This test verifies satisfactory operation of the instrument, using only front panel controls. It is intended as a quick functional check only. In consideration of possible wide variation in ambient temperature, the tolerances on readouts have been extended accordingly.

- a. Connect the Model 8100A to a source of 50 Hz to 500 Hz ac power or use the optional battery pack, as described in paragraph 2-7.
- b. Jumper the LO terminal to the GUARD terminal with the shorting link provided and set the Model 8100A controls as follows:

POWER	ON
Function	VDC
RANGE	1000
FILTER	ON

- The instrument readout should be 000.0 ± 3 digits. The polarity indicator should register either "+" or "__"
- Connect a shorting link between HI and LO INPUT terminals. Press, in turn, the 100 volt, 10 volt, and 1 volt RANGE switches. The instrument readout should be 00.00 ±3 digits, 0.000 ±3 digits, and .0000 ±1 digit, respectively.
 - Press the VAC function switch and the 1000 volt RANGE switch. The instrument readout should be between 000.0 and 001.5, with shorted INPUT terminals. The polarity indicator should not illuminate.
- f. Remove the INPUT shorting link and press the K Ω function switch and 1 volt RANGE switch. Instrument readout should be full scale at 1.1999 (instrument attempts to measure infinite resistance between input terminals).
 - Short circuit the INPUT terminals. Instrument readout should be .0004 or less. Remove the short at the input.

NOTE!

Resistance of connections and shorting link must be less than 100 milliohms to obtain the indicated reading. Press the 10 M Ω function switch. Instrument readout should be 11.999.

h.

i.

b.

d.

Short circuit the INPUT terminals. Instrument readout should be 0.004 or less.

2-14. A detailed performance test is given in paragraph 4-19 of the Maintenance section of the manual. It verifies performance of the instrument within the basic accuracy specifications using standard input signals and front panel controls only. It is suggested for use as a receiving inspection test for the instrument and to determine if the instrument is in calibration.

2-15. DC VOLTMETER OPERATION

2-16. The Model 8100A measures dc voltages in both polarities from 0 to 1000 volts, with 20% overrange capability in each of the four ranges. An active 2-pole filter is included and provides two modes of filtering. It may be switched in, if desired, to reduce the instrument response to noise. To operate the instrument as a dc voltmeter, proceed as follows:

- Press the VDC function switch to place the instrument in dc voltage mode.
 - Select the desired full-scale voltage range by pressing the corresponding RANGE switch.
- Select increased noise rejection by operating the FILTER switch (see filter specifications in the Section I). Note that response time to rated accuracy is increased from 0.5 seconds to 1.5 seconds with filter IN.
 - Connect the voltage to be measured to the INPUT terminals. The automatic polarity circuitry of the Model 8100A will activate appropriate circuitry in the instrument and the front panel polarity indicator will register a "+" if the HI input terminal is positive with respect to the LO terminal and "-" if the HI input terminal is negative with respect to the LO terminal.

2-17. AC VOLTMETER OPERATION

2-18. The Model 8100A measures ac voltage from 0 to 1000 volts at frequencies of 30 Hz to 20 kHz, with 20% overrange capability in each of four ranges. To operate the instrument as an ac voltmeter, proceed as follows:

g.

- Press the VAC function switch to place the instrument in ac voltage mode.
- b. Select the desired full-scale voltage range by pressing the corresponding RANGE switch.
- c. Connect the voltage to be measured to the INPUT terminals.

2-19. OHMMETER OPERATION

2-20. The Model 8100A measures resistance in five fullscale ranges of 1k, 10k, 100k, 1000k, and 10 megohms, with 20% overrange capability in each range. To operate the instrument as an ohmmeter, proceed as follows:

- a. Select the K Ω function switch for measurement of resistances between 0 and 1000k and select the 10 M Ω switch for measurement of resistances between 1 and 10 megohms.
- Select the desired full-scale resistance range in kilohm mode by pressing the corresponding RANGE switch. In 10 megohm mode, the range is fixed independent of the four range switches.
- c. Connect the resistance to be measured to the INPUT terminals.

NOTE!

In both $K\Omega$ and 10 $M\Omega$ modes, the internal voltage arrangement is such that the HI input terminal is positive with respect to the LO input terminal.

2-21. OPERATING NOTES

2-22. Overload Protection

2-23. The Model 8100A is fully protected against overload in each function and on all ranges. In ac and dc voltage modes, ± 1200 volts dc or 1700 volts peak ac from 30 Hz to 20 kHz can be applied indefinitely on any range without damage. In ohms mode, 130 volts rms may be applied indefinitely on the 1 kilohm range without damage, and 230 volts rms may be applied indefinitely on the remaining ohms ranges without damage.

2-24. Use of Guard Terminal

2-25. The instrument contains an isolated inner chassis, which is connected to the GUARD terminal on the front

panel. When properly connected, the guard chassis reduces measurement errors due to common-mode currents. Ordinarily, the GUARD terminal is strapped to the LO terminal; however, it may also be driven by a separate guard voltage, with connections at a remote point. It should be noted that the guard voltage must be within 100 volt dc or peak ac of the LO input terminal.

2-26. APPLICATIONS

2-27. Transistor Testing

2-28. In K Ω and 10 M Ω modes, the Model 8100A produces a maximum of 9 volts, with 10 kilohms to 10 megohms in series, depending on range. The maximum current flowing in the unknown resistance is 0.7 milliamp. The maximum power dissipated in the unknown resistance is 2.0 milliwatts on the 1 kilohm range, decreasing by an order of magnitude per range to 200 nanowatts maximum at 10 megohms. Consequently, the instrument may be used to safely test most transistors. In both K Ω and 10 M Ω modes, the internal voltage arrangement is such that the HI input terminal is positive with respect to the LO terminal.

2-29. DBM Measurement

2-30. To use the Model 8100A as an ac output meter, terminate the circuit to be measured in its characteristic impedance and connect the Model 8100A directly across the load. For example, to measure output levels of a circuit having a characteristic impedance of 600 ohms, connect a 600-ohm load to the Model 8100A INPUT terminals. The resulting readout may be converted to dbm using the information provided in Figure 2-2.

MODEL 8100A READOUT	DBM LEVEL FOR 1 VOLT RANGE (1 mw in 600 Ω)	
.0138	-35	
.1092	-17	
.2182	-11	
.3083	tes in 18 million et al 🗕 🔒 🖉	
.4352	- 5	
.5490	- 3	
.6150	- 2	
.6897	ere allocation and a second second	
.7746	- 0	
.8700	+ 1	
.9760	+ 2	

Add 20 db for each range above one volt.

Figure 2-2. AC VOLTAGE TO DBM CONVERSION TABLE FOR 600- OHM CIRCUITS

2-31. Measurement of High Resistances

2-32: In this application, the Model 8100A together with a stable external dc voltage is used to measure resistances in excess of 10 megohms.

a.

Measure and record the external voltage, E_{bb}; then connect the equipment as shown in Figure 2-3.



Figure 2-3. CIRCUIT CONNECTIONS FOR MEASURE -MENT OF HIGH RESISTANCES

OVERON SEVELATION

- b. Record the Model 8100A readout.
- c. Using the following calculations, determine the value of the unknown resistance, R_x .

$$R_x = \left(\frac{E_{bb}}{E_m} - 1\right) R_m$$

Where E_{bb} = External DC Voltage (volts dc) E_m = Model 8100A readout in step b. (volts dc) R_m = 10 Megohm (10⁷) ±0.03%

2-33. Use As A Null Detector

I

2-34. The Model 8100A, with battery option, is ideally suited to many applications requiring a null detector. The high dc input resistance (10 megohms) and the autopolarity feature of the instrument make it ideal for monitoring small voltage differences in bridge circuits, potentiometers, and other measuring devices. Where circuit adjustment is required, the circuit is simply adjusted for a readout of all zeros on the Model 8100A, which provides 100 microvolt resolution on the 1 volt range.

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Section 3 Theory of Operation

3-1. INTRODUCTION

3-2. This section contains the theory of operation of the Model 8100A Digital Multimeter. In the general discussion, the instrument function is examined at the block diagram level. The detailed circuit description is keyed to the schematics at the back of the manual.

3-3. GENERAL THEORY

3-4. System Description

The Model 8100A consists of three main sections: 3-5. the buffer, the analog-to-digital (A-to-D) converter, and the display. A simplified block diagram of the instrument is shown in Figure 3-1. The buffer accepts the input signal and converts it into a voltage suitable to drive the A-to-D converter. It scales and conditions all inputs and it performs ac to dc conversion. The A-to-D converter accepts the buffer output, determines the polarity of the voltage, and converts the voltage into a binary coded decimal (BCD) output. The analog storage circuit is used to retain digit information between measurement cycles so that a continuous display is provided. The display circuit accepts the BCD output from the A-to-D converter and converts it into a 10-line decimal output, which operates the readout tubes. The power supplies provide operating voltages for each of the sections, a stable master reference voltage, and clock signals, which are the time base for the entire instrument.

3-6. Buffer

3-7. The principal parts of the buffer are a resistive 11-19-71



Figure 3-1. MODEL 8100A SIMPLIFIED BLOCK DIAGRAM

input divider, which scales the dc input voltages to 1 volt full scale; an active, switchable, two-pole input filter, providing an instrument response of either 0.5 or 1.5 seconds, which removes unwanted ac signals from the dc input; and a low-drift, high-gain amplifier having a high input impedance. Depending on the function selected at the front panel, the buffer is connected in one of four modes of operation: dc voltage (VDC), kilohms (K Ω), 10 megohm (10 M Ω), or ac voltage (VAC).

3-8. When the instrument is in dc voltage mode, the buffer is connected as shown in Figure 3-2. The dc input voltage (E_{in}) is applied to a voltage divider. The divider

output is applied through the input filter to the buffer amplifier. The amplifier is connected as an inverting voltage follower and provides an output to the A-to-D converter which is the negative of the scaled-down input. The two diodes inside the feedback loop provide a threshold step, which supplies polarity information to the A-to-D converter. Resistor R_L is the output load resistance.



SIMPLIFIED DIAGRAM

3-9. In kilohm mode, the buffer is connected as shown in Figure 3-3. The same input divider, filter, and amplifier



Figure 3-3. KILOHM MODE SIMPLIFIED DIAGRAM

are used, except the input divider is connected to the +7 volts reference. In this circuit arrangement, the input divider is the input resistor of an operational amplifier in which the +7 volt reference forms the input voltage and the unknown resistor (R_{χ}) forms the feedback resistance. A voltage proportional to R_{χ} is applied to the A-to-D converter and is equivalent to -0.7 volts full scale when the proper input divider tap is selected.

3-10. In 10 megohm mode, the buffer is connected as shown in Figure 3-4. The +7 volt reference voltage is





divided down to 1 volt to limit the full-scale output of the buffer to 1 volt. In all other respects, the operation in 10 megohm mode is analagous to kilohm mode operation.

3-11. In ac voltage mode, the buffer is connected as shown in Figure 3-5. In this arrangement, the amplifier is



used as an operational rectifier. A pair of rectifier diodes is placed inside a strong negative feedback loop, and a negative ac feedback signal proportional to the input voltage is developed across resistor R_e . A half wave rectified dc signal proportional to the input is developed across R162. The proper scale factor is determined by the feedback resistors, which are controlled by the front panel range switches. The positive half-cycles across R162 are filtered by a two-stage RC filter to provide a positive one volt output to the A-to-D converter that is scaled in terms of the rms value of a sine wave. Table 3-1 gives the fullscale buffer outputs for each of the instrument functions.

Table 3-1. BUFFER OUTPUTS

FUNCTION	FULL-SCALE BUFFER OUTPUT (VOLTS DC)	
+VDC	1003530 884 894	-1
-VDC		+1
ΑΤΟ ΚΩ ΜΟΗΓΙΖ		-0.7
10 ΜΩ	-	-1
VAC	and Statements	+1

3-12. A-to-D Converter

3-13. The A-to-D converter employs the unique recirculating-remainder (\mathbb{R}^2) A-to-D conversion technique developed by FLUKE. A simplified diagram of the A-to-D converter and associated circuitry is shown in Figure 3-6. Accompanying the diagram is a chart showing the sequence of operation for an input of 0.6352 volts.

3-14. The A-to-D converter digitizes the input serially in four 4-millisecond time periods, with each period divided equally into digitizing and display periods, A and B. At the start of the measurement sequence, period 1A, the Ato-D converter samples the 0.6352 volt input. Then the analog output voltage from the X10 amplifier causes the comparator to output to the voltage controlled oscillator (VCO), and the VCO produces pulses which are entered into the counter. When the total pulse count equals the most significant digit of the input or 6, the counter stops. The ladder produces a series of six 0.1 volt steps which correspond to the counter output. The remainder of 0.0352 volt is amplified by the X10 amplifier and held in sample and hold capacitor C1. See C1 in Figure 3-6. The display circuitry decodes the counter output and displays the 6 in period 1B. At the beginning of period 2, the 0.6352 volt input is disconnected from the input of the A-to-D converter and the 0.352 volt output of the sample and hold circuit is digitized and displayed as the new input. Successively, the remainders of 0.52 and 0.2 volts are digitized and displayed in the same manner. Although the four digits are digitized and displayed one at a time, the process proceeds at a sufficiently high rate of speed so that the display appears continuous to the eye.

3-15. The input voltage is sampled every 300 milliseconds. Since it only requires 20 milliseconds to digitize the input, a storage circuit is provided which stores a voltage representing each of the four digits on each of four capacitors. This stored information supplies the input during the remaining fourteen 20-millisecond periods, until the voltmeter is ready to sample the input voltage again.

3-16. Refer to the simplified A-to-D converter diagram in Figure 3-7 and assume that a +0.6352 volt signal is applied to FET switch Q16. The polarity sensing circuit turns on Q16 and turns off Q12 and Q17 in response to the positive input signal and the stage -1 signal from the ring counter. (Note: The positive input refers to the signal at the input of the A-to-D converter. The actual instrument input signal is negative, but is inverted by the buffer.) The input signal is thus applied to the positive input of amplifier A2. The feedback loop for A2 is closed through resistor R98, which is chosen to give an amplifier gain of 70. Thus, when the output of A2 is 7 volts, the voltage at es will be 0.1 volts. With 0.6352 volts at the positive input, amplifier A2 will attempt to place the same voltage at the negative input of A2; however, as the output of A2 swings past 7 volts, the comparator produces an output that turns on the VDC. The VCO produces a series of pulses, which are entered into the counter. The counter output causes the ladder to place a corresponding series of 0.1 volt steps into point e. When six pulses have entered the counter, the ladder output will be 0.6 volts and the output of A2 will drop below 7 volts. At this point, the VCO stops generating pulses and the voltage at es remains at 0.6 volts plus a remainder of 0.0352 volts which is applied to es through R98. The resulting voltage at e_R will then be exactly 0.352 volts or, in other words, ten times the remainder. The amplified remainder is applied through switch Q35 to the first sample and hold capacitor, C34.

3-17. Digitizing the 6 requires approximately 2 milliseconds or half of the first digitizing period, as shown in Figure 3-6. The 6, which is stored in the counter during the digitizing process, is then displayed by the display circuit. After approximately 2 milliseconds of display time, the display is turned off and the counter is reset to zero. Switch Q35 is then turned off, leaving capacitor C34 charged to 0.352 volts. At the same time, Q16 is switched off, removing the input from the A-to-D converter for the duration of the digitizing cycle, and the ring counter switches to stage 2, turning on switches Q36 and Q39. The 0.352 volts is then applied through Q36 to the input of A2 and is digitized as the new input signal. The resulting amplified remainder of 0.52 volts is stored in the second sample and hold capacitor, C35, while the 3 is digitized and displayed in the second readout tube. The process is repeated twice more, digitizing both the 5 and the 2, with the amplified remainder alternately supplied by the two sample and hold capacitors.

3-18. If the A-to-D converter input voltage had been negative, the polarity sensing circuit would have turned on



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switch Q12 and Q17 instead of Q16 during the first period, and the voltage would have been applied to the negative input of A2, with Q17 holding the positive input of A2 at ground. With a negative input, both the ladder and resistor R98 are used as current summing resistors, since the negative input of amplifier A2 will be at virtual ground. The scaling resistor is the operational input resistance and is adjusted to set the amplifier gain to proper value. After the first digit is digitized, Q12 and Q17 are switched off and the remainder terms are digitized in the same manner as previously explained.

3-19. Analog Storage

3-20. A simplified diagram of the analog storage circuit is shown in Figure 3-8. This circuit accepts the 4-bit BCD output of the counter and converts it into an analog voltage which corresponds to the displayed digit. The analog voltage is applied to the storage capacitors through FET input switch Q28 during the display half of each clock cycle. Each of the four analog voltages is stored in a separate capacitor which is switched into the circuit by the ring counter at the same time that the corresponding digit is being displayed. When the measure/store multivibrator switches to the storage mode, FET output switch Q29 is turned on and the input voltage is disconnected from the input of the A-to-D converter. The analog voltages in the





storage circuit are then applied, digit by digit, to the A-to-D converter input. The voltages are digitized, displayed, and then re-stored in the analog storage circuit. The storage readout cycle lasts for approximately 300 milliseconds, after which time the measure/store multivibrator switches to measure mode and the A-to-D converter input is reconnected to the input signal. There are approximately 14 cycles of storage for every measurement cycle, which provides a continuous display that changes (input is sampled) three times per second.

3-21. Display

3-22. The 4-line BCD output from the A-to-D converter, containing the digit information, is applied to the decoder/driver. The decoder/driver provides a 10-line output, which is connected to the readout tube cathodes as shown in Figure 3-9. All readout tube cathodes are connected in





parallel so that the same digit is turned on in each tube. However, only the readout tube corresponding to the proper digit will be turned on by the ring counter. Therefore, all four digits are displayed on a time-share basis using one counter and one decoder/driver. The display cycle occurs during the second half of each digitizing period, as shown in Figure 3-6, whereas digitizing in the A-to-D converter occurs during the first half of the period.

3-23. Power Supply and Clock

All operating voltages for the Model 8100A are 3-24. produced by a series of regulated power supplies that derive their voltages from a -18 volt regulator (see Figure 3-10). In line operated instruments, the -18 volt regulator is supplied raw voltage by a transformer/rectifier; and in battery operated instruments, voltage is supplied by rechargeable batteries. The -18 volt regulator output is used throughout the instrument as an operating voltage and is also applied to the inverter input. The inverter provides +17 volts dc to the +15 volt regulator which, in addition to supplying instrument operating voltages, serves as source for the +7 volts reference. The inverter also provides 200 volts, which drives the readout tubes, and +5 volts to operate the integrated logic circuitry. Finally, the inverter supplies the 250 Hz clock signal.



Figure 3-10. POWER SUPPLY AND CLOCK SIMPLIFIED DIAGRAM

3-25. CIRCUIT DESCRIPTION

3-26. Buffer

INPUT RANGE DIVIDER. In dc voltage mode, 3-27. the input binding posts are connected across a four-position range divider consisting of three closely matched wirewound resistors, R158, R159, and R160 and a precision metal film resistor, R156. Resistor R157 is adjusted to provide an exact ratio between R156 and the other divider resistors. Depending on the range selected, the divider scales the input voltage down by a ratio of 1, 10, 100, or 1000 so that the output voltage of the divider will always be 1 volt full scale. In kilohm mode, the bottom of the input divider (R160) is disconnected from the LO binding post and connected to the +7 volt reference. When so connected, the divider acts as the current determining network for ohms measurement. Selection of the proper range places the required portion of the divider in series with the +7 volt reference. In all kilohm ranges, the voltage developed across the unknown resistor is 0.7 volts at full scale. In 10 megohm mode, the bottom end of the divider is connected to divider R152, R154, which is adjusted by R153 to provide an output of 1 volt. This 1 volt output sets the current through the entire range divider to provide the proper scale factor; full scale output in 10 megohm mode is 1 volt.

3-28. INPUT FILTER The output of the range divider is applied to an active, two-pole filter consisting of resistors R27 and R28 and capacitors C10 through C13. When the FILTER switch is ON, the filter consists of R27 and C10 in the first section and R28 and C12 in the second section and provides a settling time of 1.2 seconds and noise rejection of 60 db at 60 Hz. When the FILTER switch is OFF, C11 is placed in series with C10 and C13 is placed in series with C12, thereby reducing the settling time to 0.25 second and the noise rejection to 20 db at 60 Hz. Diodes CR11 and CR12, in conjunction with R27, protect both the filter and the buffer amplifier from excessive input voltages. In the 10 volt dc range, R27 is shorted out, since the output impedance of the range divider is 1 megohm and R27 is not needed.

3-29. BUFFER AMPLIFIER The buffer amplifier consists of transistors Q77, Q79, and Q80 and amplifier A1. The first stage amplifier is Q77, a low-noise, low-drift JFET pair operating in common-source configuration. Potentiometer R175 is the zero adjustment control, which in conjunction with R176, R177, and R193 is used to reduce the initial offset of Q77 to zero. The output of Q77 drives A1, a monolithic operational amplifier that provides most of the voltage gain. Capacitor C51 is placed around A1 to provide a smooth roll-off through unity gain. Transistor Q79 is a common base output stage which raises the output impedance of the amplifier as high as possible. To avoid degrading the high output impedance, Q79 operates into a constant current load, Q80.

In dc voltage, kilohm, and 10 megohm modes, the 3-30. feedback loop for the unity gain buffer amplifier is completed through diodes CR14 and CR15 which provide a voltage step around zero to drive the polarity sensing circuit. Resistor R174 is the load resistor for the amplifier. In ac voltage mode, the buffer amplifier is connected as an operational amplifier, with R24 functioning as the input resistor. The input signal is coupled through capacitor C9 to the gate of Q77 and the negative feedback loop is completed through the range resistors, R29, R30, R31, and R32. Diodes CR14 and CR15 act as an operational rectifier in ac voltage mode. A positive, half-wave rectified dc voltage, proportional to the average value of the ac input, is developed across R162 and R174. This voltage is smoothed by a two-stage filter composed of resistors R35 and R36 and capacitors C19 and C20. Potentiometer R34 R34 (ACV CAL) is adjusted to provide a filter output of 1 volt for a full scale input on any instrument range. Because the input capacity of Q77, together with additional stray capacities, causes an additional pole in the loop response of the ac converter, trimmer capacitors C14 and C15 are provided on the two lowest ranges to adjust the frequency response through 20 kHz. Frequency adjustment is not necessary on the upper two ranges because of the low resistance of R31 and R32.

3-31. A-to-D Converter

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3-32. CLOCK The clock signal, which is the master timing signal for the A-to-D converter, is taken from several windings on the inverter transformer, T102. T102-2 sup- Converter Amplifier are used on the Model 8100A. Con-

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plies a gate control signal for the analog storage circuit. The signal at T102-10 is used to strobe the readout tubes and synchronize the ring counter. Both T102-10 and T102-8 signals are applied to NAND gate A3A to provide a delayed reset pulse for counter A4. The timing diagram for the A-to-D converter is shown in Figure 3-11. Note that the measurement cycle is actually one period longer than was shown in the simplified diagram of Figure 3'6. The added period is set aside at the beginning of each measurement cycle for removal of the zero offset of the A-to-D converter amplifier.

3-33. RING COUNTER The five-stage ring counter consists of transistors Q19 through Q24 and associated components. The ring counter will free run at a frequency slightly less than the clock frequency. However, the clock signal forces the counter to speed up, thereby synchronizing the five counter periods with the clock. The purpose of Q24 is to prevent two pulses from entering the ring at one time. At the end of the fifth period in the measurement cycle, the ring counter supplies a synchronizing signal to the measure/store multivibrator.

POLARITY SENSING CIRCUIT . The polarity 3-34. sensing circuit consists of transistors Q13, Q14, Q15, and associated components. At the beginning of the second period in every measurement cycle, Q13 is turned on by the second stage signal from the ring counter. As soon as Q13 turns on, the polarity sensing circuit responds to the voltage which appears at R40. If the instrument input voltage is negative, the voltage at R40 will be positive, transistor Q15 will conduct, and the signal at the collector of Q15 in conjunction with the signal from the measure/ store multivibrator (Q25) will turn on FET switch Q16. Switch Q16 then connects the positive buffer output voltage at test point 9 (TP9) to the positive input of the A-to-D converter amplifier. If the instrument input is positive, Q14 will conduct and the signal at the collector of O14 in conjunction with the signal from Q25 will turn on FET switches Q12 and Q17. The negative buffer output will then be applied through scaling resistors R50 and R51 and switch Q12 to the negative input of the A-to-D converter amplifier. At the same time, the positive input of the amplifier will be connected to ground through switch Q17. Resistors R186 and R187 provide a small offset correction voltage to the positive input. The polarity indicator remains on during storage cycles, since the polarity sensing circuit monitors the input signal. During storage cycles, Q12, Q16 and Q17 are held off by a signal from the measure/store multivibrator.

Two different circuit configurations of the A-to-D 3-35.

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terna tasi na campisan sing p	Figure 3-11. TIMING DIAGRAM

sequently, two different schematics for the following circuits are provided at the rear of this manual. Refer to the serial numbers listed in the schematic block diagram title block to determine which diagram is applicable to your instrument. At the beginning of each paragraph of circuit descriptions, serial number effectivity is also listed.

3-36. A-to-D CONVERTER AMPLIFIER. This paragraph applies to instruments bearing serial numbers 123 thru 3409 and 3420 thru 4097. The amplifier consists of dual FET Q53, operational amplifier A2, and associated circuitry. Transistors Q76 and resistor R110 comprise a clamp, which prevents amplifier A2 from saturating while the output is above 7 volts. Transistor Q83 and resistor R184 clamp the output of amplifier A2 on strong negative signals, such as FET switching transients. The remainder feedback loop is completed through the comparator, which consists of transistor Q54 and diodes CR29 and CR30, and through the remainder resistor R98 to the input of A2. During the first period, or zeroing period, of each measurement cycle, transistors Q58 and Q18 are switched on by the ring counter. Transistor Q58 connects the output of the amplifier to the zero-holding capacitor C37, while Q18 connects the positive input of the amplifier to ground. This operation places the offset of Q53 across C37 and effectively removes it for the balance of the measurement cycle. The amplifier is able to satisfy itself through these feedback loops as long as the input does not exceed 0.1 volts. When the input exceeds 0.1 volt, the output of A2 attempts to rise above 7 volts, which causes the comparator to output to the VCO through current source Q61.

3-37. A-to-D CONVERTER AMPLIFIER. This paragraph applies to instruments bearing serial numbers 3410 thru 3419 and 4098 and on. The amplifier consists of dual FET Q53, operational amplifier A2, and associated Transistors Q76 and resistor R110 comprise circuitry. a clamp which prevents amplifier A2 from saturating while the output is above 7 volts. Diode CR47 clamps the output of amplifier A2 on strong negative signals, such as FET switching transients. The remainder feedback loop is completed through the comparator, which consists of transistors Q86 through Q89, and through the remainder resistor R98 to the input of A2. The amplifier is able to satisfy itself through these feedback loops as long as the input does not exceed 0.1 volt. When the input exceeds 0.1 volt, the output of A2 attempts to rise above 7 volts. which causes the comparator to output to the VCO through current source Q89. During the first period or zeroing periof of each measurement cycle, transistors Q58, Q85 and Q18 are switched on by the ring counter. Transistor Q58 connects the output of the amplifier to the zero holding capacitor C37, while Q18 connects the positive input of the

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amplifier to ground. This operation places the offset of Q53 across C37 and effectively removes it for the balance of the measurement cycle.

3-38. VOLTAGE CONTROLLED OSCILLATOR. This paragraph applies to instruments bearing serial numbers 123 thru 3409 and 3420 thru 4097. The VCO consists of multivibrator Q59, Q60. The VCO has no output until it is supplied current by current source Q61. When the output of A2 exceeds 7 volts, the difference is passed on to Q61 by the comparator and Q61 begins to conduct. The pulse repetition rate of the VCO is proportional to the magnitude of the driving current. The greater the current flowing into the base of Q59, the greater the number of pulses per unit time. The output of the VCO is applied to the input, pin 1, of counter A4.

3-39. VOLTAGE CONTROLLED OSCILLATOR. This paragraph applies to instruments bearing serial numbers 3410 thru 3419 and 4098 and on. The VCO consists of multivibrator Q59, Q60. The VCO has no output until it is supplied current by current source Q89. When the output of A2 exceeds 7 volts, the difference is passed on to Q89 by the comparator and Q89 begins to conduct. The pulse repetition rate of the VCO is proportional to the magnitude of the driving current. The greater the current flowing into the base of Q59, the greater the number of pulses per unit time. The output of the VCO is applied to the input, pin 1, of counter A4.

3-40. COUNTER The counter is a monolithic ripple counter, having an output which corresponds to a standard 8-4-2-1 binary code. The VCO output is applied to the clock input of the counter and the reset pulse from A3A is applied to the clear input. The output of counter A4 is applied to the decoder/driver, A5, and the ladder.

3-41. LADDER The output of counter A4 is applied to the ladder switches and their drivers, transistors Q40 through Q51. The ladder switches drive two ladders. The primary ladder consists of precision resistors R79, R84, R89, R94, and R77 and produces an output that corresponds to the actual value of the digital input. The secondary ladder, which drives only the analog storage circuit, consists of resistors R75, R78, R83, R88, and R93 and produces an output that is slightly in excess of the actual value of the digital input. Each ladder comprises a 4-bit, weighted-resistor digital-to-analog converter. The analog output of the ladders is weighted in terms of seventieths of the reference voltage (+7 volts) so that they can be programmed for a 0.1 volt output for each digital bit at the input.

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SAMPLE AND HOLD. The sample and hold cir-3-42. cuit consists of FET switches Q34 through Q39 and capacitors C34 and C35. During the second period of the measurement cycle, the amplified remainder is applied to C35; however, Q35 is inhibited from conducting during the first half of the period by the clock signal which is coupled through diode CR36 to its gate. During the second half of the second period, Q35 is switched on and the amplified remainder is applied to capacitor C34. During the third period, Q35 is switched off, Q36 is switched on, and the remainder is applied through FET switches Q34 and Q27 to the input of amplifier A2. At the same time, Q39 is switched on and the next remainder is applied to capacitor C35. During the fourth period, Q39 is switched off and Q37 and Q38 are switched on. The input is then supplied by the remainder voltage held on C35, while the next amplified remainder is stored on C34. In the final period of the measurement cycle, only Q34 is switched on to supply the final remainder voltage to the input of A2. Upon completion of the fifth period, the A-to-D converter input is connected to the instrument input voltage (if in measure mode) or the analog storage output voltage (if in storage mode) and the circulation of the new remainder proceeds in the same manner as before.

3-43. Analog Storage

3-44. The analog storage circuit consists of the secondary ladder (discussed in paragraph 3-41), the measure/store multivibrator, and the storage circuit.

3-45. MEASURE/STORE MULTIVIBRATOR. The measure/store multivibrator, consisting of transistors Q25 and Q26, is basically a one-shot multivibrator with dual synchronizing inputs. The measurement cycle, which is approximately 20 milliseconds in duration, is coincident with conduction of Q25; and the storage cycle, which is approximately 330 milliseconds in duration, is coincident with conduction of Q26. The multivibrator is triggered at the end of each measurement cycle by the trailing (negative) edge of the signal at the collector of Q24. During the measure cycle, a signal is coupled to the polarity sensing circuit from the collector of Q25. This signal enables switch Q16 for positive signal measurement or switches Q12 and Q17 for negative signal measurement. In storage mode, this signal is not present and, consequently, the input voltage will not be connected to the A-to-D converter.

3-46. STORAGE CIRCUIT. The storage circuit consists of FET switches Q28 through Q33 and capacitors C30 through C33. The output of the secondary ladder is supplied to the appropriate storage capacitor through Q28, which is switched on during display time. The first digit is stored in C30, the second in C31, the third in C32, and the fourth in C33. When the measure/store multivibrator switches to storage mode, Q29 is switched on and the analog voltages stored in the capacitors are applied, serially, to the input of A2.

3-47. Display

3-48. The decoder/divider is a monolithic BCD-to-decimal decoder, which accepts the 4-bit BCD output of counter A4, decodes each digital word, and selects one of ten decimal output drivers. The ten driver outputs are applied to the readout tube cathodes.

3-49. The readout tubes are operated by the 200 volt square wave, which originates in the power supply and corresponds in frequency to the clock. During the second half of each clock period (display time), the inverter supplies the full 200 volts to the anode drive line. The anodes are turned on by switches Q67 through Q70 and current sources Q71 through Q74. The switches, which are operated by the ring counter, turn on only one readout tube during each period of the measurement cycle. At the same time, the output of the decoder/driver is applied to the appropriate cathode of each of the four readout tubes and the digit is then visually presented in proper decimal position.

3-50. The overrange digit lamp, DS2, is operated by transistor Q66, which is connected in an AND configuration with gate A3C. When the output of counter A4 reaches ten, transistor Q66 conducts and turns on DS2. The function of gate A3B and transistor Q65 is to clamp the output of the VCO when counter A4 has a count of eleven during the first period of the measurement cycle (first digit) or a count of nine during any subsequent periods. The polarity sign indicator, V5, is operated by transistors Q81 and Q82, which are driven by the polarity sensing circuit.

3-51. Power Supply

Power for the Model 8100A is supplied either by 3-52. the ac power line via power transformer T101 or by the internal rechargeable battery pack, BT1 (Option-01). The secondary of T101 is tapped for two output voltages. The higher voltage is rectified by diodes CR32 and CR33 and is used to charge the batteries through ballast lamp DS11. The ballast lamp regulates or limits battery charging current to ensure a safe level of charging current regardless of battery condition. The lower voltage is rectified by diodes CR1 and CR2 to provide operating voltage for the instrument. The voltage at filter capacitor C1 is always greater than the battery voltage during power line operation; therefore, diode CR34 is back biased, thereby preventing drain from the batteries during power line operation. The rectified power line voltage is fed through fuse F2 to a series regulator consisting of pass element Q1, driver Q2, and voltage amplifier Q3. This regulator provides -18 volts to operate the inverter and supply negative power supply voltages for the instrument. Meter M1 is placed across the pass element to provide an indication of battery condition.

3-53. The inverter is a transformer-coupled multivibrator consisting of driver transistors Q4 and Q5, transformer T102, and RC timing network C4, R6, and R7. The inverter derives its operating voltage from the -18 volt supply and is energized as soon as instrument power is applied. It operates at a frequency of approximately 250 Hz. The square wave at the primary of T102 is rectified by diodes CR5 and CR6 to provide positive voltage to operate the +15 volt regulator. The +15 volt regulator consists of pass element Q6, driver Q7, and voltage amplifier Q8. The +15 volt regulator obtains its reference from the zener diode portion of reference amplifier Q11. In addition to supplying positive power supply voltages to operate the instrument, the +15 volt regulator also supplies the input for the +7 volt reference supply.

3-54. The +7 volt reference supply consists of compound emitter follower Q9, Q10, which is driven by reference amplifier Q11. The reference voltage is set precisely by R23 (-DC CAL control). Since the +7 volt reference supply must sink as well as supply current, resistor R181 is shunted across the output to draw a constant 2 milliamps from the supply. Also, diode CR10 is placed across the output of the supply to prevent current reversal during overload in the K Ω or 10 M Ω modes.

3-55. A high voltage tap in the primary of T102 supplies voltage to a half-wave voltage doubler consisting of diode CR3 and capacitor C6. The 200 volt output of the doubler is used to operate the readout tubes, V1 through V4; the overrange indicator, DS2; and the polarity indicator, V5. Filtered dc voltage for operation of the integrated circuit logic is provided by diodes CR7 and CR8 and capacitor C44 in the secondary of T102.

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Section 4 Maintenance

4-1. INTRODUCTION

4-2. This section contains information and instructions concerning preventive and corrective maintenance for the Model 8100A Digital Multimeter. Preventive maintenance consists primarily of cleaning the instrument and should be performed as often as operating conditions require. Corrective maintenance consists of troubleshooting, calibration, and performance test procedures, which are designed to aid in maintaining instrument operation within specifications. Section 3 of the instruction manual is an important supplement to the troubleshooting section, since a thorough knowledge of instrument theory is indispensable in troubleshooting.

4-3. A calibration interval of 90 days is recommended to ensure instrument operation within the 90-day specifications stated in Section I of the manual and a calibration interval of 30 days is recommended for operation within the 30-day specifications. The performance of the instrument should be verified in accordance with the performance tests in paragraph 4-19 before calibration is attempted. An instrument that meets all of the requirements of the performance tests need not be calibrated. An instrument that does not meet the requirements of the performance tests will require troubleshooting or calibration.

4-4. SERVICE INFORMATION

4-5. Each instrument manufactured by the John Fluke Manufacturing Company is warranted for a period of one

year upon delivery to the original purchaser. Complete warranty information is contained in the Warranty page located at the rear of the manual. Factory authorized calibration and repair service for all Fluke Instruments is available at various world wide locations. A complete list of factory authorized service centers is located at the rear of the manual. If requested, an estimate will be provided to the customer before any repair work is begun on instruments which are beyond the warranty period.

4-6. TEST EQUIPMENT

4-7. The equipment recommended for performance testing, troubleshooting, and calibration of the Model 8100A, together with the minimum specifications required in each case, is listed in Table 4-1. If the recommended equipment is not available, other equipment which meets the required specifications may be used.

4-8. GENERAL MAINTENANCE

4-9. Access/Disassembly

a.

4-10. All circuit board components except indicator tubes are accessible after removing the covers and guards. The indicator tube access procedure is given in paragraph 4-15. The following procedure may be used to gain access to all components except tubes.

Remove the top and bottom covers and the narrow trim strips just above and below the readout tubes.

*

	SPECIFICATIONS REQUIRED	RECOMMENDED EQUIPMENT	
DC Voltage Source	Range: 0 to ±1000 vdc Accuracy: ±0.005% of setting Stability: ±0.0005% of setting or 20 uv (whichever is greater).	Fluke Model 343A DC Voltage Calibrator	
AC Voltage Source	Voltage Range: 0 to 100 vrms Voltage Accuracy: ±0.05% of setting (50 Hz to 10 kHz)	Hewlett Packard Model 745A AC Calibrator	
	±0.1% of setting (10 kHz to 20 kHz) Voltage Stability: ±0.002% of setting per 24 hours. Frequency Range: 1 kHz to 20 kHz		
Precision Resistors 1.0000k 10.000k 100.00k 1000.0k	Accuracy: ±0.01% Power Capability: 2 mw minimum	General Radio Type 1440 Standard Resistor	
Precision Resistor 10 MΩ	Accuracy: ±0.01%	nesetti sionalesi nashin di quinsis in turnis in sulficio printing as conto a Las	
Autotransformer	Voltage: 0 to 130 vac Current: 3 amp	General Radio Model W5MT3AW Metered Variac	
Wattmeter	Power: 0 to 200w	galai il gaad moontation of .	
Multimeter		Fluke Model 853A Differential Multimeter	
High Impedance or Differential DC Voltmeter	Range: 0 to ±1000 vdc Accuracy: ±0.02% of input Input Impedance: 10 ⁷ ohms minimum	Fluke Model 8300A Digital Voltmeter	
Oscilloscope Vertical Sensitivity: 0.1v/cm minimum Sweep Speed: 0.5 usec/cm minimum Probe: High Impedance (10 MΩ)		Tektronix Type 545A Oscilloscope with Type 1A1 Dual Trace Plug-In	

Table 4-1. TEST AND CALIBRATION EQUIPMENT

4-2

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Remove the top guard (1) by disconnecting the spade-lug guard-connecting wire located on the right rear of the guard, (2) by disconnecting the two battery leads at the printed circuit board (if battery option is installed), and (3) by removing the four guard mounting screws, which are located on the instrument side rails.

CAUTION!

Do not remove the six screws located on the top of the guard. These screws are for battery pack access only and should not be removed until the guard is completely removed from the instrument.

Remove the bottom guard by removing the four screws which hold it in place.

4-11. Fuse Replacement

4-12. Instrument fuses are located inside the instrument. The line fuse is located in a fuse block attached to the inside of the rear panel and is accessible from the bottom of the instrument after removing the bottom cover. The power supply fuse is mounted in fuse clips, which are attached to the printed circuit board near the power transformer. This fuse is accessible from the top of the instrument after removing the top cover. Correct values for the fuses are as follows:

REFERENCE

D

b.

c.

ESIGNATION	FUNCTION	RATING	
F1	Line fuse	115 volt operation: 1/8 ampere slow-	f.
		blow 230 volt operation: 1/16 ampere slow-	
ann)		blow	4- 4-1
F2	Power supply fuse	AGC 1/4 ampere	pla

4-13. 115/230 Volt Conversion

4-14. The Model 8100A may be operated from either 115 or 230 volt ac power, depending upon the connection of the power transformer (T101) primary winding. Convert the instrument from one type of power line operation to the other by the following procedure:

a. Disconnect the instrument from the power line.

- Place the 115/230 slide switch, located at the rear of the instrument, in the position which corresponds to the desired operating voltage.
 - Ensure that the proper line fuse for the selected voltage is installed (paragraph 4-11) before operating the instrument.

4-15. Lamp and Tube Replacement

c.

d.

e.

a.

b.

C.

4-16. The ballast lamp, DS11, is soldered in place on the circuit board and is located at the rear of the instrument near the power transformer. It is accessible after removing the top and bottom covers. The readout tubes, V1 through V4, and the polarity indicator, V5, are mounted in tube sockets and are located inside the molded black tube cover at the front of the circuit board. To gain access to the tubes, proceed as follows:

a. Remove the top and bottom covers and the narrow trim strips just above and below the readout tubes.

b. Remove the tilt-down bail assembly.

- c. Remove the small nut and washer located just to the right of the INPUT terminals (viewed from the top) on the inside of the front panel.
 - Remove the eight screws (four on either side) which hold the front panel assembly to the side rails.

Pull the front panel assembly forward to the extent permitted by the INPUT terminal wiring.

Remove the molded tube cover by removing the two nuts located beneath the tube cover on the underside of the circuit board.

-17. Switch Maintenance

4-18. The following procedures cover service and replacement of the pushbutton switches.

4-19. GENERAL

- Remove the covers, trim strips, and guard. See paragraph 4-9 for guard removal instructions.
- Set all switches to the nondepressed position.

If switch contacts are to be serviced, locate which of the 19 switch modules is to be dis-

d.

e.

assembled (Figure 4-1) and refer to the corresponding disassembly procedure, paragraphs 4-20 through 4-22.

If a complete switch module is to be replaced, refer to paragraphs 4-23 and 4-24.

To remove pushbuttons, pry the button off by applying pressure to the rear of the button from the inside of the front panel. Note that on some instruments buttons are glued on the switch shaft. When replacing buttons, use a small amount of contact cement. Apply glue to shaft only.

4-20. PROCEDURE NO. 1 - DISASSEMBLY OF REAR SWITCHES (Figure 4-2)

a. Remove the extension shaft associated with the switch by depressing the switch shaft and removing the extension shaft as shown in Detail A, Figure 4-2.

Remove the retaining clip and spring from the switch shaft.

NOTE!

The retaining clip may be re-installed on the switch shaft from either the board side or from the opposite side (cover side). Either method is satisfactory.

Remove the switch shaft by pushing it slowly out the back of the switch module. The moveable switch contacts will spring out of their slots in the shaft as this is done (see Detail B, Figure 4-2).

CAUTION!

Under no circumstances should attempts be made to re-form or add tension to the contacts by bending them.



b.

Figure 4-1. SWITCH MODULE LAYOUT AND DISASSEMBLY PROCEDURES

d. Inspect the switch contacts for contamination and, if necessary, clean them with a cotton swab saturated in alcohol. After cleaning, the contacts must be re-coated with a thin coat of grease: Use RYKON 2EP grease (American Oil Co.) or equivalent.

e. Assemble the switch by reversing the foregoing procedure. Exercise caution to ensure the contacts are not deformed during assembly.

4-21. PROCEDURE NO. 2 - DISASSEMBLY OF FRONT SWITCHES (EXCEPT POWER SWITCH)

a. Remove the extension shaft associated with the

switch by depressing the switch shaft and removing the extension shaft as shown in Detail A, Figure 4-2.

Remove the switch shaft by sliding the latch bar to the side and pushing the shaft slowly out the back of the switch module (see Detail A, Figure 4-3). Use a pointed tool to move the latch bar. The latch bar is conveniently actuated from the shaft opening at an adjacent switch, in which case the pushbutton will have to be removed from the adjacent switch shaft also. The moveable contacts will spring out of their slots in the shaft as



b.

c.

Figure 4-2. DISASSEMBLY OF REAR SWITCH MODULES

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Remove the pushbutton from the switch.

d.

the switch shaft is removed (see Detail B, Figure 4-2).

CAUTION!

Under no circumstances should attempts be made to re-form or add tension to the contacts by bending them.

Inspect the switch contacts for contamination and, if necessary, clean them with a cotton swab saturated in alcohol. After cleaning, the contacts must be re-coated with a thin coat of grease: Use RYKON 2EP grease or equivalent. Assemble the switch by reversing the foregoing procedure. Exercise caution to ensure the contacts are not deformed during assembly.

NOTE!

The latch bar will have to be slid to the side, as in step (c), to replace the switch shaft.

4-22. PROCEDURE NO. 3 - DISASSEMBLY OF POWER SWITCH (Figure 4-3)

Remove the pushbutton from the switch.



e.

a.

Figure 4-3. DISASSEMBLY OF FRONT SWITCH MODULES

Remove the spring retaining clip, the spring, the push-push clip, and the push-push pin (see Detail B Figure 4-3).

ь.

C.

Remove the switch shaft by pushing it slowly out the back of the switch module. The moveable switch contacts will spring out of their slots in the shaft as this is done (see Detail B, Figure 4-2).

CAUTION!

Under no circumstances should attempts be made to re-form or add tension to the contacts by bending them.

d. Inspect the switch contacts for contamination and, if necessary, clean them with a cotton swab saturated in alcohol. After cleaning, the contacts must be re-coated with a thin coat of grease: Use RYKON 2EP or equivalent.

e. Assemble the switch by reversing the foregoing procedure. Exercise caution to ensure the contacts are not deformed during assembly. 4-23. SWITCH MODULE REPLACEMENT – REAR SWITCHES (Figure 4-4).

- Unsolder all wires and components connected to terminals of switch modules.
- Desolder entire rear switch set by removing the solder from all switch pins on component side of main PCB.

CAUTION!

Use a desoldering tool and exercise extreme caution to avoid lifting land patterns or otherwise damaging the PCB.

Remove switch set from PCB.

- Bend up the locking tab (on switch support) which holds the defective switch module in position. Remove and descard defective switch module. See Detail A, Figure 4-4.
 - Install new switch module by reversing foregoing procedure.



a.

b.

c.

d.

e.

Figure 4-4. MODULE REPLACEMENT – REAR SWITCH SET


Figure 4-5. LOCATION OF CIRCUIT AREAS AND TEST POINTS ON PCB.

4-39. Power Supply

4-40. Supply levels are referenced in such manner that unless all output levels are correct, most levels will be incorrect. This occurs because the -18 volt aupply is refer-

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enced to the +15 volt supply, which is in turn referenced to the +7 volt supply. If the -18 volt supply is high, the +15 volt and the +200 volt supplies will also be high. If the -18 volt, +15 volt, or +7 volt supply is low, all supply output voltages will be low.

4.41. To measure the output voltages, use a voltmeter accurate to at least $\pm 0.025\%$ and proceed as follows:

- a. Set the function, RANGE, and FILTER switches to any position.
- b. Connect the voltmeter common to 8100A common (TP1) and measure the voltages shown in Table 4-5. Voltages should be as indicated.

Table 4-5. POWER SUPPLY VOLTAGE REQUIREMENTS

VOLTAGE TEST POINT	NOMINAL	LIMITS
Either terminal	-23	Battery Operation:
of fuse F2	-24	-19 to -24
		Line Operation:
		-24 Minimum
Collector	-18	-17.5 to -18.5
(case of Q1)		REPROPER DATA
+Terminal of C5	+17	+16.5 to +17.5
+Terminal of C7	+15	+14.5 to +15.5
+Terminal of C8	+7	+6.993 to +7.007
+Terminal of C44	+5	+4.5 to +5.5
+Terminal of C65	+200	+185 to +205

4-42. The following procedures can be used to isolate which supply output is low; either procedure makes the -18 volt supply independent of the other supplies for its reference. Disconnect the +15 volt end of resistor R5 and connect it to a stable external +15 volt source, or disconnect R5 from the base of Q3 and connect a 1330 Ω ±1% resistor from the base of Q3 to signal ground. Location of R5 and Q3 is shown in Figure 4-5.

3.2. A sector and any constant sector where a sector is a sector of the sector and the sector



Figure 4-6. INTEGRATED CIRCUIT PIN ORIENTATION

4-43. Buffer

4-44. In 1 volt dc range, the voltage between INPUT LO and signal ground (TP1) should very nearly equal the input 11-19-71

voltage between INPUT HI and LO. At full range (-1.1999 or +1.1999 volts) the deviation should be less than 60 microvolts; at zero volts input, deviation should be less than 10 microvolts.

4-45. When measuring buffer performance, it should be noted that the INPUT LO terminal is not 8100A signal ground, as shown in Figure 4-7. This circuit may become





unstable and oscillate if a large capacitance (500pf or more) is placed across R174. To avoid this problem, it is recommended that test equipment ground be connected to INPUT LO instead of TP1 when the 8100A is measuring dc volts. In ac voltage mode, INPUT LO and signal common are equivalent.

4-46. To check the buffer in both ac and dc voltage modes, proceed as follows:

a. Set the 8100A controls as follows:

ON
VDC
1
ON

- b. Connect +1.0000 volts dc to the INPUT terminals.
 - Connect a high impedance voltmeter between instrument common (TP1) and TP9. The voltmeter should indicate -1.0000 ± 0.0003 volts dc.
- d. Disconnect the voltmeter high lead from TP9 and connect it to the positive (+) terminal of capacitor C18.
 - Press the VAC function switch and connect a 1.0000 volt, 1 kHz signal to the instrument INPUT terminals. The voltmeter should indicate 1.0000 ± 0.0025 volts dc. If the A-to-D converter and display sections of the 8100A are working, the 8100A readout should correspond to the voltmeter indication.

4-47. A-to-D Converter

4-48. It is convenient to use VAC mode to check the A-to-D converter and display circuitry, because of this mode the INPUT LO terminal and 8100A circuit common are equivalent, as shown in Figure 4-8. The ac input signal is



VAC MODE.

connected between INPUT HI and LO. If buffer problems are suspected, however, the buffer can be bypassed by connecting a low impedance ($\leq 820k$) dc source of zero to +1.1999 volts dc between INPUT LO and TP9, (Figure 4-9). The resulting readout should correspond to the A-to-D converter input.



Figure 4-9. BUFFER, VAC MODE, SHUNTED BY EXTERNAL DC SIGNAL

4-49. 300 MS Sample Rate Suppression

4-50. The A-to-D converter samples the input at a fixed rate of once each 300 milliseconds. The sampled reading is then stored on capacitors C30 through C33 and is read out of storage repeatedly until the next sample is taken. The storage mode can be eliminated for test purposes by installing a jumper between TP4 and TP5. This will force the A-to-D converter to sample the input during period 1 of every measurement cycle, i.e., once each 20 milliseconds.

4-51. Auto-Zero Suppression

4-52. Amplifier A2 in the A-to-D converter is zeroed automatically by circuitry consisting of Q58, R180, and C37. Some failures in the converter may cause the autozero circuit to overcompensate and mask the trouble. To disable the auto-zero circuit, connect a jumper between TP6 and TP7. A few digits offset will be present in the readout and it will stay at zero for several digits of input on one polarity, but, when the input is raised the output should follow properly.

c.

e.

4-53. Ring Counter

4-54. The signal at TP3 (Figures 4-10 and 4-11) is convenient for evaluation of the ring counter. The signals at the collectors of Q20 through Q24 are identical, but are displaced in time.



Figure 4-10. TP3 - RING COUNTER WAVEFORM.





- In the waveform shown in Figure 4-10, t1 should be 3.8 to 4.2 milliseconds. If t1 is not correct, check clock signal duration. It should be 4 ± 0.2 milliseconds duration.
 - The trailing edge of the signal at TP3 should be a 10 ± 0.5 volt step. Factors which determine this step amplitude are repetition rate and the values of C26 and R56.
- A marginal waveform is shown in Figure 4-11 and described as follows:
 - Spike in center indicates near minimum pulserepetition rate. Sync bias level, controlled by R61, and synchronizing signal amplitude affect center spike.
 - Trailing edge droop is mainly a function of Q19 gain: HFE should be greater than 100 at 1 milliamp, 0.5 volts. Droop occurs at maximum pulse repetition rate.
 - 3. Spikes following the pulse occur if the ring counter is near an oscillation condition: Check capacitor C57.
 - 4. Trailing edge switching time should be between 2 and 15 microseconds. Switching time is a function of the transistor and the synchronizing signal. Fall times as long as 40 microseconds have been observed when sync bias is incorrect.

The ring is subject to failure if the output lines are loaded by a faulty transistor, such as Q65, Q30, Q34, or any other device connected to the ring output lines. This type of failure causes the ring to oscillate at random intervals. To isolate ring problems, connect TP11 to the +15 volt supply and check that each output line is between -17 and -18 volts. Remove the +15 volt connection from TP11 and check that each collector saturates to within -0.3 volts of signal ground when the corresponding transistor turns on. Test jumpers are provided at ring positions 1, 2, 3 and 4, and can be cut to isolate loading problems in the ring.

The ring counter synchronizing signal, TP11, should appear as shown in Figure 4-12.



Figure 4-12. RING COUNTER - SYNC SIGNAL.

4-55. Measure/Store Multivibrator

4-56. Waveforms associated with the measure/store multivibrator are shown in Figure 4-13.



Figure 4-13. MULTIVIBRATOR WAVEFORMS.

4-57. Linearity

d.

e.

Set the 8100A controls as follows: Function VDC RANGE 1 FILTER OFF

a.

b.

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h. '

Apply each of the following voltages, in turn, to the INPUT terminals. The instrument readout should be as indicated.

NOTE!

The readout limits apply only to instruments that have just been calibrated.

INPUT	MODEL 8100A
(Volts DC)	READOUT LIMITS
-1.1999	-1.1998 to -1.2000
-1.0999	-1.0998 to -1.1000
-0.9999	9998 to -1.0000
-0.8999	8998 to9000
-0.7999	7998 to8000
-0.6999	6998 to7000
-0.5999	5998 to6000
-0.4999	4998 to5000
-0.3999	3998 to4000
-0.2999	2998 to3000
-0.1999	1998 to2000
-0.0999	0998 to1000

If voltages are not correct, the ladder resistors should be checked for proper value.

4-58. Waveforms

c.

a.

c.

4-59. In the following test, a dc test voltage is applied to the instrument and the main signal waveforms are examined, using an oscilloscope.

Set the 8100	A controls as	follows:
POWER	ON	
Function	VDC	
RANGE	di seri t ana ^{ti} na	
FILTER	ON	

b. Apply any stable dc voltage, within the range of the instrument, to the INPUT terminals.

Connect the oscilloscope between each of the test points shown in Table 4-6 and instrument common (TP1 or TP5), using ac coupling; set the oscilloscope controls as shown. A stylized version of the desired signal is shown on the main schematic diagram. Note that the A-to-D converter amplifier and VCO waveforms will vary, depending on input voltage value. Table 4-6. SIGNAL LOCATIONS AND OSCILLOSCOPE SETTINGS FOR WAVEFORM CHECK.

Stuge with 1	TEST	OSCILLO	SCOPE
an a bailet	POINT	VERTICAL SENSITIVITY	SWEEP SPEED
Clock	Junction of R53 and R54	1v/cm	0.5 ms/cm
Ring Counter	ТРЗ	10v/cm	5 ms/cm
A-to-D Converter [*] Amplifier Output	TP2	5v/cm	2 ms/cm
vco	Collector of Q60	2v/cm	2 ms/cm
Q35 Gate	Gate of Q35	10v/cm	5 ms/cm
Counter Reset	Pin 2 of A4	2v/cm	1 ms/cm

4-60. CALIBRATION

4-61. Equipment required for calibration is given in Table 4-1. Calibration should be performed under the following test conditions; ambient temperature $25^{\circ}C + 5^{\circ}C$, realtive humidity less than 70%. Adjustment locations are silk-screened on the bottom guard cover.

4-62. Preliminary

Ъ.

a.

b.

C.

- a. Connect the 8100A to an autotransformer adjusted for 115 volts.
 - Set 8100A FUNCTION to VDC and RANGE to 1000 volts.
- c. Attach all covers and allow the 8100A to warm up for at least 1 hour.

4-63. Zero Adjustment

- Set the 8100A to the 1000 volts dc range and short the input terminals.
- Remove the top and bottom outer covers.
- Connect a dc differential voltmeter between TP1 and the LO INPUT terminal, with common lead

2-11-72

connected to INPUT LO and high lead connected to TP1.

d. Adjust the ZERO control (R175) for zero volt dc (±10 uV). If unable to obtain zero by adjusting R175, refer to the detailed procedure given in Figure 4-14.



8100A

8100A

C.

d.

- 4-64. A-to-D Converter Offset

c.

d.

a.

b.

c.

a.

b.

c.

- a. Disconnect the differential voltmeter from TP1.
- b. Set the 8100A to the 1 volt dc range with the FILTER switch ON.
 - Apply +0.00055 volt dc to the input terminals.
 - Adjust the A-D OFFSET control (R206) for a readout alternating between .0005 and .0006. Check for proper adjustment by ensuring that the readout is (1) steady at +.0006 with a +0.00059 volt input and (2) steady at +.0005 with a +0.00053 volt input.
- e. Reverse input polarity and ensure that readout is (1) steady at -.0006 with a -0.00059 volt input and (2) steady at -.0005 with a -0.00054 volt input.
- 4-65. -VDC Cal.
 - Set the 8100A to the 1 volt dc range with the FILTER switch ON.
- b. Apply -1.00055 volts dc to the INPUT terminals.
- c. Adjust -DCV CAL control (R23) for a readout alternating between -1.0005 and -1.0006. Check for proper adjustment by ensuring that the readout is (1) steady at -1.0006 with a -1.00058 volts input and (2) steady at -1.0005 with a -1.00052 volts input.
- 4-66. +VDC Cal.
- a. Set the 8100A to the 1 volt dc range with the FILTER switch ON.
- b. Apply +1.00055 volts dc to the INPUT terminals.
- c. Adjust +DCV CAL control (R50) for a readout alternating between +1.0005 and +1.0006. Check for proper adjustment by ensuring that the readout is (1) steady at +1.0006 with a +1.00058 volts input and (2) steady at +1.0005 with a +1.00052 volts input.

4-67. Trip Point Adjustment

a. Set the 8100A to the 1 volt dc range with the FILTER switch ON.

b. Apply -0.09965 volt dc to the INPUT terminals.

Adjust the DCV TRIP POINT ADJ control (R208) for a readout alternating between -.099X and -.1000. Check for proper adjustment by ensuring that readout is (1) steady at -.099X with a -0.09993 volt input and (2) steady at -.1000 with a -0.09997 volt input.

NOTE!

The "X" digit may be any of several numbers, depending on adjustment of the REMAINDER ADJ control, which will be adjusted in the next section of the calibration procedure. The actual value does not affect trip point adjustment.

Reverse input polarity and ensure that readout is (1) steady at +.1000 with a +0.09999 volt input and (2) steady at +.099X with a +0.09992 volt input.

4-68. Remainder Adjustment

- Set the 8100A to the 1 volt dc range.
- Apply -0.09965 volt dc to the INPUT terminals.
 - Adjust the REMAINDER ADJ control (R149) for a readout alternating between -.0996 and -.0997.
 Check for proper adjustment by ensuring that readout is (1) steady at -.0997 with a -0.09966 volt input, (2) steady at -.0996 with a -0.09962 volt input, and (3) steady at +.0999 and -.0999 with +0.0999 and -0.0999 volt inputs respectively.

4-69. Input Divider Adjustment

Press the 100 volt RANGE switch.

Apply -100.00 volts dc to the INPUT terminals and adjust the INPUT DIV ADJ control (R157) for a readout of -100.00.

Apply -10.000 volts dc to the INPUT terminals and press the 10 volt RANGE switch. The instrument readout should be -10.000.

4-17

- Press the 1000 volt RANGE switch and apply -1000.0 volts dc to the INPUT terminals. The instrument readout should be -1000.0.
- Re-adjust the INPUT DIV ADJ control, if necessary, for an exact readout in steps (b), (c), and (d).

4-70. Kilohms Cal

- a. Set the 8100A function to $K\Omega$, RANGE to 1000, and FILTER to ON.
- b. Connect a 100.00 k Ω resistor to the INPUT terminals.
- c. Adjust K OHM CAL control (R48) for a 100.00 readout.
- d. Set the 8100A to the 1 k Ω range.
- e. Connect a 1.0000 k Ω resistor to the INPUTterminals. Readout should be 1.0000 ± 0.0001 k Ω .
- f. Set the 8100A to the 10 k Ω range.
- g. Connect a 10.000 k Ω resistor to the INPUT terminals. Readout should be 10.000 ±0.001 k Ω .
- h. Set the 8100A to the 1000 k Ω range.

- Connect a 1000.0 k Ω resistor to the INPUT terminals. Readout should be 1000.0 \pm 0.1 k Ω .
- Set the 8100A to the 1 k Ω range.
- k. Short the INPUT terminals with a shorting bar. Readout should be < .0001.

4-71. 10 Megohm Cal

i.

j.

- a. Set the 8100A function to 10 M Ω and FILTER to ON.
- b. Connect a 10 M Ω resistor to the INPUT terminals.

c. Adjust the 10 MEG CAL control (R153) for a readout of 10.000 ±2 digits. Allow adequate time for instrument to settle before finalizing adjustment.

4-72. VAC Cal

- a. Set 8100A function to VDC and RANGE to 1000. Ensure that readout is +000.0. If not, perform the zero adjustment procedure, paragraph 4 - 63.
- b. Set 8100A function to VAC and RANGE to 1000. Readout should be between 000.0 and 000.3.
 - Set 8100A RANGE as shown in Table 4-7 and perform the indicated operations.

STEP	8100A RANGE	INPUT SIGNAL	OPERATION
d.	1	1V, 1 kHz	Adjust ACV CAL (R134) for a readout of 1.0000.
е.	1	1V, 20 kHz	Adjust 1V HF ADJ (C14) for a readout of 1.0000.
f.	(194 <mark>1</mark>)1483	1V, 10 kHz	Readout should be between .9993 and 1.0008. If not, re-adjust C14 to bring reading within tolerance.
g.	tion 1: the	1V, 10 kHz	Readout should be between .9975 and 1.0020.
h.	Beffers 1 ack (b)	100 MV, 2 kHz	Readout should be between .0998 and .1002.
i.	. ity 1 sig	100 MV, 10 kHz	Readout should be between .0996 and .1004.
j.	angen hty o	100 MV, 20 kHz	Readout should be between .0992 and .1008.
k.	10	10V, 2 kHz	Readout should be between 9.995 and 10.005
1.	10 mm	10V, 20 kHz	Adjust 10V HF ADJ (C15) for a readout of 10.000.
m.	100	100V, 1 kHz	Readout should be between 99.93 and 100.07.
n.	100	100V, 20 kHz	Readout should be between 99.70 and 100.30.
ο.	100	100V, 10 kHz	Readout should be between 99.70 and 100.30.
р.	1000	1000V, 2 kHz	Allow time for settling. Readout should be between 998.3 and 1001.7.
q.	1000	1000V, 10 kHz	Readout should be between 998.3 and 1001.7.
r.	1000	1000V, 20 kHz	Readout should be between 996.0 and 1004.0.
s.	STRUCTURE ST	historia i fossos	Calibration of the 8100A is complete

Table 4-7. AC CALIBRATION

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d.

e.

Section 5 List of Replaceable Parts

d.

e.

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5-1. INTRODUCTION

5-2. This section of the manual contains a listing of replaceable components for this instrument. The first listing contains a complete breakdown of all the major assemblies followed by subsequent listings that itemize the components on each major assembly. An illustration accompanies each major assembly listing to aid in locating the listed components.

5-3. Assemblies and subassemblies are identified by a reference designation beginning with the letter A followed by a number (e.g., A1 etc.). Electrical components appearing on the schematic diagram are identified by their schematic diagram reference designation. Components not appearing on the schematic diagram are consecutively numbered throughout the parts list. These components are identified with whole numbers on the arrow call-out illustrations and by index numbers on the grid illustrations. Flagnotes are used throughout the parts list and refer to special ordering explanations that are located in close proximity to the flagnotes.

5-4. COLUMN DESCRIPTION

a. The REF DESIG column indexes the item description to the associated illustration. In general the reference designations are listed under each assembly in alpha-numeric order. Subassemblies of minor proportions are sometimes listed with the assembly of which they are a part. In this case, the reference designations for the components of the subassembly may appear out of order.

The INDEX NO. column lists coordinates which locate the designated part on the associated grid illustrations.

c. The DESCRIPTION column describes the salient characteristics of the component. Indention of the description indicates the relationship to other assemblies, components, etc. In many cases it is necessary to abbreviate in this column. For abbreviations and symbols used, see Appendix B.

- The six-digit part number, by which the item is identified at the John Fluke Mfg. Co., is listed in the STOCK NO. column. Use this number when ordering parts from the factory or authorized representatives.
- The Federal Supply Code for the item manufacturer is listed in the MFR column. An abbreviated list of Federal Supply Codes is included in the Appendix.
- The part number which uniquely identifies the item to the original manufacturer is listed in the MFR PART NO. column. If a component must be ordered by description, the type number is listed.
- The TOT QTY column lists the total quantity of the items used in the instrument and reflects the latest Use Code. Second and subsequent listings of the same item are referenced to the first listing with the abbreviation REF. In the case of optional subassemblies, plug ins, etc. that are not always part of the instrument, or are deviations from the basic instrument model, the TOT QTY column lists the total quantity of the item in that particular assembly.
 - Entries in the REC QTY column indicate the recommended number of spare parts necessary to support one to five instruments for a period of two years. This list presumes an availability of common electronic parts at the maintenance site. For maintenance for one year or more at an isolated site, it is recommended that at least one of every part in the instrument be stocked. In the case of optional subassemblies, plug-ins, etc. that are not always part of the instrument, or are deviations from the basic instrument model, the REC QTY column lists the recommended quantity of the item in that particular assembly.

The USE CODE column identifies certain parts which have been added, deleted or modified during

b.

the production of the instrument. Each part for which a Use Code has been assigned may be identified with a particular instrument serial number by consulting the Serial Number Effectivity List at the end of the parts list. Sometimes when a part is changed, the new part can and should be used as a replacement for the original part. In this event a parenthetical note is added in the DESCRIPTION column.

5-5. HOW TO OBTAIN PARTS

5-6. Standard components have been used wherever possible. Standard components may be ordered directly from the manufacturer by using the manufacturer's part number, or parts may be ordered from the John Fluke Mfg. Co. factory or authorized representative by using the Fluke part number. In the event the part you order has been replaced by a new or improved part, the replacement will be accompanied by an explanatory note and installation instructions, if necessary.

5-7. You can insure prompt and efficient handling of your order to the John Fluke Mfg. Co. if you include the following information:

- a. Quantity
- b. FLUKE Stock Number
- c. Description
- d. Reference Designation
- e. Instrument model and serial number

Example: 2 each, 177105, Transistors, 2N3565, Q107-108 for 845AR, s/n 168.

If you must order structural parts not listed in the parts list, describe the part as completely as possible. A sketch of the part, showing its location to other parts of the instrument, is usually most helpful.

5-8. SERIAL NUMBER EFFECTIVITY

5-9. A Use Code column is provided to identify certain parts that have been added, deleted, or modified during production of the Model 8100A. Each part for which a use code has been assigned may be identified with a particular instrument serial number by consulting the Use Code Effectivity List below. All parts with no code are used on all instruments with serial numbers above 123.

USE CODE SERIAL NUMBER EFFECTIVITY NONE Model 8100A serial number 123 and on.

- Model 8100A serial numbers 123, 132, 133, 136, 138, 140, 143-145, 148, 152-156, 160, 164, 166, 171 and on.
- Model 8100A serial numbers 124-131, 134, 135, 137, 139, 141, 142, 146, 147, 149-151, 157-159, 161-163, 165, 167-170.

C Model 8100A serial numbers 123 to approximately serial number 400.

D Model 8100A approximately serial number 400 and on.

E Model 8100A serial number 123 thru 1999.

F Model 8100A serial number 2000 and on.

- G Model 8100A serial number 123 thru 2499.
- H Model 8100A serial number 2500 and on.
- I Model 8100A serial number 123 thru 2844.
- J Model 8100A serial number 2845 and on.
- K Model 8100A serial number 3410 thru 3419 and 4098 thru 4148.
 - Model 8100A serial number 4149 and on.
- M Model 8100A serial number 3410 thru 3419, 4098 thru 4365 and 4429 thru 4477.
- N Model 8100A serial number 4366 thru 4428 and 4478 and on.
- O Model 8100A serial number 123 thru 3409 and 3420 thru 4097.
- P Model 8100A serial number 3410 thru 3419 and 4098 and on.

Q Model 8100A serial number 4260 and on.

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5-2

REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	a south same	REC	USE CODE
		DIGITAL MULTIMETER - Figure 5-1	8100A	9 - 1991 - 1994				
A1		Printed Circuit Assembly (See Figures 5-2 and 5-3)	269753	89536	269753	1		о
A1		Printed Circuit Assembly (See Figures 5-4 and 5-5)	294421	89536	294421	1		Р
F1		Fuse, slow blow, 1/8 amp, 250v (for 115v operation)	166488	71400	MDL	1		
F1		Fuse, slow blow, 1/16 amp, 250v (for 230v operation)	163030	71400	MDL			
J1 .		Binding post, red, HI	275552	32767	820-65	1		
J2		Binding post, black, LO	275560	32767	820-45	1		2 626 (d)
J3	See 1	Binding post, blue, GUARD	275578	32767	820-55	1		
P1	State of	Connector, male, 3 contact	222612	82389	AC3G	1	Ĩ.	
R1		Res, comp, 10m ±10%, ½w	108142	01121	EB1061	1		0
S11		Switch, slide, dpdt	226274	82389	46256-LF	1		×
T101	gu n in it	Transformer, power	275354	89536	275354	1	1	
W1		Line cord (not illustrated)	226100	70903	17258	1	1	
XF1		Holder, fuse	103283	71400	4405	1		
1	Cardina a	Cable, AC Power	269779	89536	169779	1		
2	Care 2. 14	Cable, Input	269761	89536	269761	1		
3	1	Cover, bottom (not illustrated)	269639	89536	269639	1	- 1	
•	14. Mar 14	Cover, top (not illustrated)	269647	89536	269647	1	h	1.1
5		Handle, carrying	231456	89536	231456	1		
5		Push button, gray	268896	71590	J52304	9		1
1	des 1	Push button, green	268862	71590	J61993	-1-		
3		Shaft, switch extension	269688	89536	269688	9	1	
2		Shorting link	101220	24655	0938-9712	1		



Figure 5-1. 8100A DIGITAL MULTIMETER (Sheet 1 of 2)

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3

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Figure 5-1. 8100A DIGITAL MULTIMETER (Sheet 2 of 2)

11-19-71

REF DESIC	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	Anne - see	REC QTY	USE
A1		PRINTED CIRCUIT ASSEMBLY Figures 5-2 and 5-3	269753	89536	269753	REF		0
		NOTE!	· · ·		÷.		0.21	
		This assembly used in S/N 123 thru 3409 and 3420 thru 4097. For replacement, however, order the new configuration, part number 294421.	•					ana ng
A1	G1-W2	IC, operational amplifier	268961	12040	SL6267	1	1	Е
A1	G1-W2	, IC, operational amplifier	271502	12040	LM301A	2	1	F
A2	E1-R5	IC, operational amplifier	271502	12040	LM301A	REF	- The	
A3	C4-05	IC, DTL, triple 3-input NAND gate	266312	04713	MC862P-6909	1		1.5
A4 A5	C4-N5	IC, DTL, Binary counter IC, Decoder driver ,DM/SN7441AN, NS8840N	267153	04713 89536	MC839P-6921 267211	1		
C1	B1-Z4	Cap, elect, 250 uf $+50/-10\%$, 40v	178616	73445	C437ARG250	1	2	
C2	B3-R5	Cap, cer, 0.0012 uf ±10%, 500v	106732	71590		2		E
C2	B3-R5	Cap, cer, 3300 pf ±20%, 1 kv	106674	56289	C023B102- G332M	1		F
C3	F1-X5	Cap, elect, 50 uf +75/-10%, 25v	148650	06001	76F02KL500	2	ALL I	- Sicki
C4	C1-R5	Cap, plstc, 0.47 uf, factory selected	279422	89536	279422	1		$-3\phi_{1}f$
C5	F4-Z4	Cap, elect, 50 uf +50/-10%, 25v	168823	73445	C426ARF50	3		81.33
26	B4-N1	Cap, elect, 2 uf +50/-10%, 150v	267310	56289	30D205F150- BB4	1	1	
27	H1-X5	Cap, elect, 50 uf +75/-10%, 25v	148650	06001	76F02KL500	REF		1.000
C8	I4-Y3	Cap, elect, 50 uf +50/-10%, 25v	168823	73445	C426ARF50	REF	And In	
C9	I3-V1	Cap, plstc, 0.22 uf ±10%, 1 kv	275495	84411	Type 663UW	1	14.21	1.520.2
210	H2-V5	Cap, plstc, 0.1 uf ±20%, 120v	167460	84411	Type 863UW	1	267.21	BED.
211	H4-U5	Cap, plstc, 0.033 uf ±10%, 250v	234492	73445	C280AE/A33K	2		145313
212	H5-V4	Cap, plstc, 0.068 uf ±20%, 120v	260570	84411	863UW68391	1	40.4	22.54
213	H4-U4	Cap, plstc, 0.027 uf ±10%, 250v	267120	73445	C280AE/A27K	1	ajr	17:0
14	D4-V5	Cap, var, 0.5–5 pf, 500v	267914	72982	532-001	1	pi i i	100
15	D1-V5	Cap, var, 1–8 pf, 500v	267906	72982	532-000	1	NG BL	823.
18	D3-X2	Cap, elect, 50 uf +50/-10%, 25v	168823	73445	C426ARF50	REF	111	-US Ro
19	E2-W4	Cap, plstc, 1.0 uf ±10%, 250v	190330	73445	C280AE/AIM	3		
20	E5-W4	Cap, plstc, 0.22 uf ±10%, 250v	194803	73445 0	280AE/A220K	2		183
	13-02	Cap, elect, 5 uf +50/-10%, 64v	218966	73445	C426ARH5	1	Tel.	
	I1-N3	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	5		
	I1-N1	Cap, plstc, 0.1 uf, factory selected	279430	89536	79430	REF		
1.1.1.1.1.1.1.1	I1-M4	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	REF		10.4% 11 a
	I1-M1	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	REF	a He	-
and the second second	I1-N5	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	REF		190
Melline I	H4-05	Cap, mica, 330 pf ±5%, 500v	148445	14655	CD15F331J	1	6	19.5
28	H4-04	Cap, mica, 100 pf ±5%, 500v	148494	14655	CD15F101J	2		100
29	I1-P1	Cap, plstc, 0.47 uf ±10%, 250v	184366	73445 0	280AE/A470K	1	1	1

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR		ΤΟΤ ΔΤΥ	11122510	USE CODE
C30	H1-02	Cap, plstc, 0.022 uf ±10%, 250v	234484	73445	C280AE/A22K	5		
C31	G5-02	Cap, plstc, 0.022 uf ±10%, 250v	234484	73445	C280AE/A22K	REF		
C32	G4-02	Cap, plstc, 0.022 uf ±10%, 250v	234484	73445	C280AE/A22K	REF		
C33	G2-02	Cap, plstc, 0.022 uf ±10%, 250v	234484	73445	C280AE/A22K	REF		
C34	G5-O5	Cap, plstc, 0.047 uf ±20%, 120v	260562	84411	863UW47391	1		
C35	G5-P4	Cap, plstc, 0.1 uf ±10%, 250v	161992	73445	C280AE/A100K	4	- 28	
C36	E3-S1	Cap, mica, 33 pf ±5%, 500v	160317	14655	CD15E330J	3		いの言語
C37	F4-R4	Cap, plstc, 1.0 uf ±10%, 250v	190330	73445	C280AE/A1M	REF		
C38	C3-R4	Cap, cer, 0.0012 uf ±10%, 500v	106732	71590	CF-122	1	1111 1111	1.1
C39	C3-S2	Cap, mica, 470 pf ±5%, 500v	148429	14655	CD19F471J	1	• •	A
C39	C3-S2	Cap, mica, 220 pf ±5%, 500v	237008	14655	CD19F221J	1		В
C42	C4-P4	Cap, plstc, 0.047 uf ±10%, 250v	162008	73445	C280AE/A47K	1	6.(8)	10
C42	D1-P4	Cap, plstc, 0.033 uf ±10%, 250v	234492	73445	C280AE/A33K	REF	(<u>2</u> 8)	- 5
C44	B2-Q3	Cap, plstc, 1.0 uf $\pm 10\%$, 250v	190330	73445	C280AE/A1M	REF	122	15
C44	E1-S2	Cap, mica, 47 pf $\pm 5\%$, 500v	148536	14655	CD15E470J	2	- a.	
C45	F4-R1	Cap, mica, 47 pf ±5%, 500v	148536	14655	CD15E470J	REF		
C40	H4-S1	Cap, elect, 1 uf +75/-10%, 25v	165886	56289	30D105G- 025BA4	1	18-19 1.00	
C50	G2-X2	Cap, plstc, 0.1 uf ±10%, 250v	161992	73445	C280AE/A100	REF		
C51	G2-W4	Cap, mica, 150 pf $\pm 5\%$, 500v	148478	14655	CD15F151J	1	1.15	1
C51	G1-X1	Cap, mica, 33 pf ±5%, 500v	160317	14655	CD15E330J	REF	248	
C52	D5-W3	Cap, mica, 100 pf \pm 5%, 500v	148494	14655	CD15F101J	REF	7568	
1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1	14-Q4	Cap, plstc, 0.22 uf ±10%, 250v	194803	7344	C280AE/A220	K REF	1216	1
C54	G4-N1	Cap, mica, 4 pf ±5%, 500v (Figure 5-1)	190397	1465	5 CD15C040K	1		
C55	I1-M5	Cap, mica, 33 pf ±5%, 500v	160317 -	1465	5 CD15E330J	REI	7	
057	H4-Y5	Cap, plstc, 0.1 uf $\pm 10\%$, 250v	161992	7344:	5 C280AE/A100	K REI	-	
C58		Cap, plstc, 0.022 uf $\pm 10\%$, 250v	234484	7344	5 C280AE/A22I	K REI	i la c	
C59	H4-Z2	Cap, cer, .001 uf $\pm 20\%$, 3 kv	105635	5628	9 29C300	1	i bi	1
C60 C61	D1-X5 E4-U5	Cap, cer, 150 pf $\pm 20\%$, 3 kv	277004	0065	6 HVD4150 ±20, 3 KV-2	1	143	
C62	H2-X2	Cap, plstc, 0.1 uf ±10%, 250v	161992	7344	5 C280AE/A100	K RE	F	
C63 C64	E1-M5	Cap, factory selected (may or may not be installed) Cap, plstc, 2.2 uf ±10%, 250v	222232	7344	5 C280MAE/ A2M2	1	i i i i Neti	1
	DIVA	Diode, silicon, 1 amp, 100 piv	116111	0527	7 IN4817	7	100	
CR1	B1-Y4	Diode, silicon, 1 amp, 100 piv	116111	0527	7 IN4817	RE	F	1
CR2 CR3		Diode, silicon, 1 amp, 100 piv	112383	Star Tor Int	7 IN4822	2		
	DO IN I	an and a second se		ana na Ta	ang kang kang sang sang sang sang sang sang sang s		98-1256 -1846	

Rev. 1

REF DESIC		DESCRIPTION	STOCK NO	MFR	MFR PART NO		TREC	
CR4	D3-02	Diode, silicon, 150 ma	203323	03508	DHD1105	11		
CR5	B3-R4	Diode, silicon, 1 amp, 100 piv	116111	Construction of the	1N4817	REF	i in	
CR6	B3-R2	Diode, silicon, 1 amp, 100 piv	116111		1N4817	REF	1.	
CR7	B4-P5	Diode, silicon, 1 amp, 100 piv	116111		1N4817	REF	1-1-1	-
CR8	B4-P3	Diode, silicon, 1 amp, 100 piv	116111		1N4817	REF		
CR9	D3-01	Diode, silicon, 150 ma	203323	1. 200 100	DHD1105	REF	e la s	1
CR10	I3-Y2	Diode, silicon, 1 amp, 100 piv	116111	10000	1N4817	REF		297
CR11	I1-W1	Diode, silicon, 10 ma, 2 piv	180554	-	CD12599	2		
CR12	I1-W1	Diode, silicon, 10 ma, 2 piv	180554	1	CD12599	REF		
CR13	I2-W1	Diode, silicon, 75 ma, 25 piv	241422	03508		3		G
CR13	I2-W1	Diode, silicon, controlled FV	234468	07910		2		н
CR14	F1-W3	Diode, silicon, 75 ma, 25 piv	241422	03508		2		п
CR15	F1-X1	Diode, silicon, 75 ma, 25 piv	241422	03508		REF	120	
CR16	G4-R3	Diode, silicon, 150 ma	203323	Contract of	DHD1105	REF	aug	
CR17	H1-Q5	Diode, silicon, 200 ma, 25 piv	190272	93332		4		14(2)
CR18	H1-S2	Diode, silicon, 150 ma	203323	0.000	OHD1105	REF		
R19	84-Q5	Diode, silicon, 150 ma	203323	1.021-0	DHD1105	REF	121	0201
R20 1	D1-P4	Diode, silicon, 150 ma	203323	C. Break	OHD1105	REF	şe diş	2547
R23 1	D1-S1	Diode, silicon 150 ma	203323		0HD1105	REF		5247
R26 E	24-X1	Diode, silicon, 200 ma, 25 piv	190272	93332 1	A CANADA BARREL DE DAVE DE L	REF		9400 1420 -
R27 E	24-W3	Diode, silicon, 200 ma, 25 piv	190272	93332 1		REF	ц., Г.	2.pt
R29 E	02-S1	Diode, silicon, 150 ma	203323	03508 D	형왕 같은 말 같	REF	$\{2\}$	030
R30 D	2-R1	Diode, silicon, 150 ma	203323	03508 D	111212	REF		
R35 G	5-R3	Diode, silicon, 200 ma, 25 piv		93332 11		REF		840 840
R36 G	1-P2	Diode, silicon, 150 ma		03508 D	5 1 SF	REF	11-(1) 	050
R37 F	1-X1	Diode, zener, 3.9v		07910 11	1.11		2	1825.
R38 F	1-N3	Diode, silicon, 150 ma		03508 D		REF		F
R39 B	5-N1	Diode, silicon, 1 amp, 600 piv		05277 11		REF		F F
R40 I3	-W1	(Figure 5-2) Diode, silicon, controlled FV		07910 CI	HIL WET	REF	- 64 L	H
52 E3	3-K5	Lamp, Neon	. ta.	74276 A	SALTHIE A		1011 - 2	ico:
cz	2-Y2	Fuse, fast acting, ¼ amp, 250v	1 1 1 K 1 K	71400 Ty	and states in the			30
			10/014	,1400 I y	he war	1	EL B	101

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	a nonemen	REC QTY	USE CODE
Q1	F2-Y4	Tstr, silicon, NPN	183004	95303	40250	1	1	-
Q2	H1-Y3	Tstr, silicon, PNP	195974	04713	2N3906	9		
Q3	H3-Y3	Tstr, silicon, PNP	195974	04713	2N3906	REF		
Q4	B3-S2	Tstr, silicon, NPN	272237	07263	2N4946	2	2	1.19%
Q5	B2-S4	Tstr, silicon NPN	272237	07263	2N4946	REF	The second	(- 693) (- 693)
Q6	G3-Z3	Tstr, silicon, PNP	195974	04713	2N3906	REF		
Q7	H1-Z3	Tstr, silicon, NPN	218396	04713	2N3904	11		1
Q8	H3-Z3	Tstr, silicon, NPN	218396	04713	2N3904	REF		
Q9	H3-Y4	Tstr, silicon, PNP	195974	04713	2N3906	REF	Veltri	100
Q10 .	H3-Z1	Tstr, silicon, NPN	168716	07263	S19254	1		Servi
Q11	H5-Z5	Tstr, factory selected & matched to R15 R22	1>					ľ st
Q12	G2-R4	Tstr, factory selected & matched to Q52	2>		a de la constante de la constant		14.03	100
Q13	H3-Q3	Tstr, FET, N-channel	288324	15818	U1994E	18	4	1521
Q14	H3-R1	Tstr, silicon, PNP	195974	04713	2N3906	REF		- Is mit i
Q15	H3-R3	Tstr, silicon, PNP	195974	04713	2N3906	REF		
Q16	G3-R5	Tstr, FET, N-channel	288324	15818	U1994E	REF		1000
Q17	G3-R2	Tstr, FET, N-channel	261578	15818	U1897E	1	2	
Q18	G2-Q5	Tstr, FET, N-channel	288324	15818	U1994E	REF	100	$\simeq 2$
Q19	13-N5	Tstr, silicon, PNP	281758	01295	SKA5153	5	4	100
Q20	I3-N3	Tstr, silicon, PNP	281758	01295	SKA5153	REF		÷
Q21	I3-N1	Tstr, silicon, PNP	281758	01295	SKA5153	REF		1.4
Q22	I3-M4	Tstr, silicon, PNP	281758	01295	SKA5153	REF	6913	08132
Q23	I3-M1	Tstr, silicon, PNP	281758	01295	SKA5153	REF	e enti	12.92
Q24	III-L5	Tstr, silicon, PNP	195974	04713	2N3906	REF	in and	in in
Q25	13-P3	Tstr, silicon, PNP	281741	01295	SKA5198	1	1	
Q26	I3-P4	Tstr, silicon, PNP	195974	04713	2N3906	REF	5 - 1- 21	1.2.4
Q27	H1-P4	Tstr, FET, N-channel	288324	15818	U1994E	REF	1992	- 195E
Q28	G3-P5	Tstr, FET, N-channel	288324	15818	3 U1994E	REF		8
Q29	H1-P5	Tstr, FET, N-channel	288324	15818	3 U1994E	REF		100.00
Q30	H1-N5	Tstr, FET, N-channel	288324	15818	3 U1994E	REF	7	
Q31	G5-N5	Tstr, FET, N-channel	288324	15818	3 U1994E	REF	- 1- 11- 11; 	12
Q32	G4-N5	Tstr, FET, N-channel	288324	15818	3 U1994E	REF		્યું હતું.
Q33	G3-N5	Tstr, FET, N-channel	288324	15818	8 U1994E	REF	19 51 41103	
Q34	H1-04	Tstr, FET, N-channel	288324	1581	8 U1994E	REI	e diane	6825
Q35	G3-04	Tstr. FET. N-channel	288324	1581	8 U1994E	REI	7	
Q36	H1-P1	Tstr, FET, N-channel	288324	15818	8 U1994E	REF	7	A CONTRACTOR OF
Q37	G3-P1	Tstr, FET, N-channel	288324	1581	8 U1994E	REF	a Seleni	1 24
Q38	H1-P2	Tstr, FET, N-channel	288324	1581	8 U1994E	REF	inen:	1 4
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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	\$ 5 DFE 0.955	REC QTY	USE CODE
Q39	G3-P2	Tstr, FET, N-channel	288324	15818	U1994E	REF		
Q40	F1-P2	Tstr, germanium, NPN	182691	01295	GA3937	4	2	
Q41	E3-P2	Tstr, germanium, PNP	182709	01295	GA3938	4	2	
Q42	E1-P2	Tstr, silicon, NPN	218396	04713	2N3904	REF		
Q43	F1-05	Tstr, germanium, NPN	182691	01295	GA3937	REF		
Q44	E3-05	Tstr, germanium, PNP	182709	01295	GA3938	REF		
Q45	E1-05	Tstr, silicon, NPN	218396	04713	2N3904	REF		
Q46	F1-02	Tstr, germanium, NPN	182691	01295	GA3937	REF		
Q47	E3-02	Tstr, germanium, PNP	182709	01295	GA3938	REF		
Q48	E1-02	Tstr, silicon, NPN	218396	04713	2N3904	REF		
Q49	F1-N5	Tstr, germanium, NPN	182691	01295	GA3937	REF		
Q50	E3-N5	Tstr, germanium, PNP	182709	01295	GA3938	REF		
Q51	E1-N5	Tstr, silicon, NPN	218396	04713	2N3904	REF		
252	E3-R2	Tstr, Factory selected & matched to Q12	2>		n her (f)			53
Q53	G1-R5	Tstr, FET, dual, N-channel	257501 257501	17856	DN423	1	1	1 1536
Q54	D3-R2	Tstr, silicon, NPN	222869	03508	X16E2300	1	1	
258	F1-R2	Tstr, FET, N-channel	288324	15818	U1994E	REF		6. 6.2
Q59	C4-S1	Tstr, silicon, NPN	218396	04713	2N3904	REF		
Q 60	C2-S1	Tstr, silicon, NPN	218396	04713	2N3904	REF		e en
Q61	D3-S1	Tstr, silicon, PNP	281766	01295	SKA5152	2	1	
Q65	E2-M5	Tstr, FET, N-channel	288324	15818	U1994E	REF		
Q66	E1-M2	Tstr, silicon, NPN	245480	07263	S24496	3	1	
Q67	H1-M5	Tstr, silicon, NPN	218511	95303	60994	4	2	
Q68	G3-M5	Tstr, silicon, NPN	218511	95303	60994	REF		10134 - 1014 - 1014
Q69	F5-M5	Tstr, silicon, NPN	218511	95303	60994	REF		1 4 N
270	F2-M5	Tstr, silicon, NPN	218511	95303	60994	REF		
Q71	H1-M2	Tstr, silicon, PNP	266619	07263	2N4888	4	2	
Q72	G3-M2	Tstr, silicon, PNP	266619	07263	2N4888	REF		0. ST #
201 C					an mana an ang Second Chi Second Chi			o ota
	dan da						-	10.000 10.000
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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	And the second s		REC QTY	USE CODI
Q73	F5-M2	Tstr, silicon, PNP	266619	07263	2N4888	REF		191. té
Q74	F2-M2	Tstr, silicon, PNP	266619	07263	2N4888	REF		
Q75	G1-R2	Tstr, silicon, NPN	218396	04713	2N3904	REF		- 1910
Q76	E1-R2	Tstr, silicon, PNP	281766	89536	281766	REF		
Q77	G4-W2	Tstr, FET, dual, N-channel	267963	17856	DN503	1	i	
Q79	F2-W2	Tstr, silicon, PNP	195974	04713	2N3906	REF		
Q80	F3-W4	Tstr, silicon, NPN	218396	04713	2N3904	REF		1000
Q81	F4-L2	Tstr, silicon, NPN	245480	07263	S24496	REF		an F
Q82	G1-L1	Tstr, silicon, NPN	245480	07263	S24496	REF		V.as
Q83	E4-R2	Tstr, silicon, NPN	218396	04713	2N3904	REF		
R1	D5-Y3	Res, comp, 470Ω ±10%, 1w	109710	01121	GB4711	1		>3/6
R2	G4-Y3	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	5		1.15
R3	G4-Y3	Res, comp, 33k ±5%, ¼w	148155	01121	CB3335	2	1.63	1.127
R4	G4-Y2	Res, met flm, 38.3k ±1%, 1/8w	241372	91637	Type MFF1/8	1	1	1 22
R5	H2-Y1	Res, met flm, 34k ±1%, 1/8w	261602	91637	Type MFF1/8	4		
R6	B4-R1	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF1/8	5	1.1.1	1.183
R7	B5-S2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF1/8	REF	2 4- 73	
R8	B5-R1	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	5	22-572 -	F83
R9	G3-Z4	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	REF	1	1.58
R10	G4-Y4	Res, comp, 270Ω ±5%, ¼w	160804	01121	CB2715	2		1.1
R11	G4-Z2	Res, comp, 1k ±5%, 4w	148023	01121	CB1025	REF	7	
R12	G4-Z1	Res, comp, 100k ±5%, ¼w	148189	01121	CB1045	1	1	577
R13	H2-Z5	Res, met flm, 12.1k ±1%, 1/8w	234997	91637	Type MFF 1/8	1	-423	
R14	H2-Z4	Res, met flm, 2.94k ±1%, 1/8w	261628	91637	Type MFF 1/8	1	1.01	
R15	G4-Y5	Res, met flm, factory matched to Q11			Continent Training			
R16	G4-Y4	& R22 Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	7	6211	19
R17	G4-Z1	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	6		
R18	I3-AA2	Res, met flm, 10k ±1%, 1/8w	168260	91637	Type MFF 1/8	1	i de la	
R19	I3-AA1	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF	7	
R20	I3-Z4	Res, met flm, $412\Omega \pm 1\%$, 1/8w	261636	91637	Type MFF 1/8	1		
R21	13-Z5	Res, met flm, 19.1k ±1%, 1/8w	234963	91637	Type MFF 1/8	3		

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DES	IG NO	DESCRIPTION	STOC NO	К	R MFR		REC	USE
R22	I3-Z5	Res, met flm, factory selected and matched to Q11 & R15		STATE.				
R23	I1-Z2	Res, var, ww, 10k ±10%, 2w	272740	714	50			1
R24	I2-S4	Res, met flm, 1M ±0.1%, 2w	272740	12-12-2	1.	3	an y	A.
R27	F4-V4	Res, comp, 1M ±10%, 2w	268227		-71-2.20		24	i i a ge
R28	G3-V4	Res, comp, 1.2M ±5%, ¼w	188425			1		
R29	G4-N2	Res, met flm, 449.1k, matched (Figure 5-2)	3		21 CB1255	1		
R30	G4-M5	Res, met flm, 49.9k, matched (Figure 5-2)	3	Р. 	la service com	1.00		
R31	G4-M5	Res, met flm, 4.99k, matched (Figure 5-2)	3	200 - 220 700 - 2	110 No. 192	1.73		ten del
R32	G4-M4	Res, met flm, 499Ω, matched (Figure 5-2)	3		17 A.B.	1.028		cha l
R34	C3-W5	Res, var, ww, 10k ±10%, 2w	272740	71450	D Type 115	REF		BANK .
R35	E3-X1	Res, comp, 220k ±5%, ¼w	160937	0112		REF		Rig-
R36	E3-W2	Res, comp, 820k ±5%, 4w	220541	01121				SCR.
R37	G4-S2	Res, comp, 220k ±5%, 4w	160937	01121		REF		178
39	G5-Q3	Res, comp, 18k ±5%, ¼w	148122	01121			H AL	1258
240	H5-S2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	a se garante	11.00	19	101
42	H5-R2	Res, met flm, 499k ±1%, 1/8w	268813	91637	1.1.1.1.1.10	1111		1
43 43 45	H1-R3 H1-R3 G5-Q5	Res, met flm, 499k $\pm 1\%$, 1/8w Res, met flm, 453k $\pm 1\%$, 1/8w Res, comp, 22k $\pm 5\%$, 4w	268813 295709 148130	91637 91637	Type MFF 1/8 Type MFF 1/8 CB2235	REF		I J
.46	H2-S2	Res, comp, 22k ±5%, 4w	148130	01121	CB2235	REF		81.28 J.
47	H3-S2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF 1/8	「おおり」に	in the second	
48	G1-T4	Res, var, ww, 200 Ω ±20%, 1-4/w	144766	71450	a charge i branch a standa	1	11	8 7 1
49	G3-T2	Res, ww, 6.45k ±0.5%, ½w	184416	89536	184416	1		
50	H2-T4	Res, var, ww, 15Ω ±20%, 1-4/w	163634	71450	Type 110		11	855
51	G5-T2	Res, ww, 2.85k ±0.1%, ½w	253450	89536	253450	1	1	835 J.
52	G5-S2	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	REF		838 - 1 - 1
3	H5-O3	Res, comp, 3.3k ±5%, ¼w		01121		REF	新生	33, j
4]]	14-03	Res, comp, 1k ±5%, ¼w	148023	States 12	NUE STATE	REF		
6 1	4-N5	Res, comp, 47k ±5%, ¼w	a construction	17533.44		REF	Non-	

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	· · · · · · · · · · · · · · · · · · ·	211-02-19-25A	USE CODE
R57	14-N3	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF		
R58	I4-M5	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF		
R59	14-M3	Res, comp, 47k ±5%, 44w	148163	01121	CB4735	REF		181
R60	14-L5	Res, comp, 47k ±5%, 4w	148163	01121	CB4735	REF		8.293 (192)
R61	I4-M1	Res, met flm, 200k ±1%, 1/8w	261701	91637	Type MFF 1/8	2	8 a 1	111.4
R62	11-01	Res, comp, 22k ±5%, 4/w	148130	01121	CB2235	REF		3.733
R63	11-N4	Res, comp, 22k ±5%, 4/w	148130	01121	CB2235	REF		(1月)
R64	I1-N2	Res, comp, 22k ±5%, 4w	148130	01121	CB2235	REF		
R65	I1-M5	Res, comp, 22k ±5%, 4/w	148130	01121	CB2235	REF	1.8	
R66	Н5-М3	Res, comp, 22k ±5%, 4w	148130	01121	CB2235	REF		1992
R67	Н5-М3	Res, comp, 270k ±5%, ¼w	220061	01121	CB2745	1		
R68	12-05	Res, comp, 150k ±5%, 4w	182212	01121	CB1545	2		
R69	12-04	Res, comp, 470k ±5%, ¼w	188441	01121	CB4745	2		$ \Im \Xi T $
R70	H5-P4	Res, comp, 150k ±5%, ¼w	182212	01121	CB1545	REF		072
R71	H5-Q1	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	REF	(M)	10.51
R72	H5-P3	Res, comp, 1.5M ±5%, ¼w	182857	01121	CB1555	2		8.3
R73	H5-P3	Res, comp, 47k ±5%, 44w	148163	01121	CB4735	REF		62.85
R74	H5-P5	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	14		14
R75	F4-Q2	Res, met flm, 8.66k ±1%, 1/8w	260364	91637	Type MFF 1/8	1		2435
R77	G1-01	Res, ww, 3.4483k, matched	4					11
R78	F4-05	Res, met flm, 422k ±1%, 1/8w	276626	91637	Type MFF 1/8	1		2655
R79	F5-P3	Res, ww, 200k, matched	4>		$M_{\rm eff} = M_{\rm eff} + M_{\rm eff} + M_{\rm eff}$		Q.	-1933
R80	D4-P3	Res, comp, 10k ±5%, 4w	148106	01121	CB1035	REF		24.4
R81	D4-P2	Res, comp, 15k ±5%, 4w	148114	01121	CB1535	5	10 - 2	sie
R82	D4-P3	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	6	154	19145
R83	F2-P1	Res, met flm, 232k ±1%, 1/8w	276618	91637	Type MFF 1/8	1	58	1.548
R84	F3-04	Res, ww, 100k, matched	$ \overline{4} \rangle$	12.000	adamini per p		4.9	1.12
R85	D4-05	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF	257	154
R86	D4-05	Res, comp, 15k ±5%, ¼w	148114	01121	CB1535	REF		l uzer
R87	D4-P1	Res, comp, 1.6k ±5%, ¼w	266197		CB1625	REF	- 2.4	
R88	F3-01	Res, met flm, 121k ±1%, 1/8w	229369		Type MFF 1/8	2	[44.]	1.9619

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	10000		
R89	F4-02	Res, ww, 50k, matched			iniko isse			
R90	-D4-03	Res, comp, 10k ±5%, ¼w	148106	01121		REF		1.00
R91	D4-02	Res, comp, 15k ±5%, ¼w	148114	01121		REF	1 1	
R92	D4-04	Res, comp, 1k ±5%, ¼w	148023	01121	and exchanges sourced the	REF		al ide
R93	F2-01	Res, met flm, 121k ±1%, 1/8w	229369	91637		a part of		2世(194
R94	F3-01	Res, ww, 50k, matched	$ \overline{4}\rangle$	E 2083	and the second s	180	4) (68.111
R95	D4-N5	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF	섥	n Mésu
R96	D4-N4	Res, comp, 15k ±5%, ¼w	148114	01121	सोरियो (तिस्वे ⁰	REF	13. 6	gin ing
R97	D4-N5	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	REF	42.6	
R98	F2-P4	Res, ww, 200k, matched	4>	A ALLESS				045-225
R99	G2-P4	Res, met flm, 11.8k ±1%, 1/8w	277145	91637	Type MFF 1/8	1.1.2		4.4.0.4
R100	G2-P3	Res, met flm, 2k ±1%, 1/8w	277137	91637	Type MFF 1/8	1 233		Ste field
R101	H5-R2	Res, met flm, 19.1k ±1%, 1/8w	234963	91637	Type MFF 1/8	1		
R102	E4-S1	Res, met flm, 34k ±1%, 1/8w	261602	91637	Type MFF 1/8	and the second second		
R103	E4-S1	Res, met flm, 34k ±1%, 1/8w	261602	91637	Type MFF 1/8			
R104	E3-R4	Res, met flm, 45.3k +1%, 1/8w	234971	91637	Type MFF 1/8			
R108	D2-R2	Res, comp, 4.7k $\pm 5\%$, $\frac{1}{4}$ w	148072	01121	CB4725	2		
R110	D5-S1	Res, comp, 10k ±5%, ¼w	148106	01121		REF		
R111	H2-S2	Res, met flm, 19.1k ±1%, 1/8w	234963	91637	Type MFF 1/8			
R112	H4-R2	Res, comp, 2.2M ±5%, ¼w	198390	01121	CB2255	1	1424	
R116	H5-Q1	Res, comp, 180k ±5%, ¼w	193441	01121		9		
R118	D4-S1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1045	REF	2.25	11222
R119	C4-R2	Res, comp, 10k $\pm 5\%$, $\frac{1}{4}$ w	148106	01121	CB1035	REF		新兴 历
R120	C2-R2	Res, comp, 33k ±5%, ¼w	148155	01121		REF		
R121	C3-R2	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	5		
R122	D4-R2	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF		22136
R123	D5-R2	Res, comp, 82k ±5%, ¼w	188458	01121	CB8235			DALLAS
8124	D1-R2	Res, comp, 4.7k ±5%, ¼w	148072	01121	СВ4725	1 PEE		to:Uk
R127	F2-M3	Res, comp, 22k ±5%, ¼w	148072			REF		0
127	F2-M3	Res, comp, 12k ±5%, ¼w	159731	110000-00000	CB2235 CB1235	REF		C
128	E2-M4	Res, comp, 10k ±5%, ¼w	148106		CB1233	REF	2	D
130	E4-M3	Res, comp, 62k ±5%, ¼w	A CONTRACTOR OF A		CB6235	1	R	1.57

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	And the second states of the second	τοτ ατγ	20201-01-020	USE CODE
R131	H3-N2	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF		1 Defi
	G5-M3	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF		000
Ř132	G5-M3 G5-N2	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF	601	- cen
R133 R134	G2-M3	Res, comp, 180k $\pm 5\%$, $4w$	193441	01121	CB1845	REF	arc	1 578
R134	G2-M3 G2-N2	Res, comp, 180k $\pm 5\%$, 4w	193441	01121	CB1845	REF		2.00
R135	G2-IN2 F4-M3	Res, comp, 180k ±5%, 4w	193441	01121	CB1845	REF		1 203
R137	F4-N2	Res, comp, 180k ±5%, 4w	193441	01121	CB1845	REF		
R138	F1-M3	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF	8 . S	1 2139
R139	E5-N1	Res, comp, 4.7k ±5%, ¼w	148072	01121	CB4725	REF	241	E
R140	E5-N1	Res, comp, 3.9k ±5%, ¼w	148064	01121	CB3925	1		E
R141	G5-M3	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF	R.R.	282
RI42	G2-M3	Res, comp, 10k ±5%, 4w	148106	01121	CB1035	REF	89) 	1985
R142	F4-M3	Res, comp, 10k $\pm 5\%$, $\frac{1}{4}$ w	148106	01121	CB1035	REF	62	6016
	E5-M3	Res, comp, 10k $\pm 5\%$, $\frac{1}{4}$ w	148106	01121	CB1035	REF	1925	international sector
R144 R145	E3-M3 E4-M3	Res, comp, $1.1k \pm 5\%$, $4w$	267336	01121	CB1125	1		in the second
	C3-P4	Res, comp, 1.6k $\pm 5\%$, 4w	266197	01121	CB1625	REF	10.3	bavia
R146	l selen i l		266197	01121	CB1625	REF		
R147	C5-P4	Res, comp, 1.6k ±5%, ¼w			A Suppose			Set 1
R149	E4-Q3	Res, var, ww, 500 $\Omega \pm 10\%$, 1-4w	112433	71450				
R152	F2-T3	Res, met flm, 200k $\pm 1\%$, 1/8w	261701	91637	a and a second second	REF		0.510
R153	E4-T4	Res, var, ww, 10k ±10%, 2w	272740	71450	Type 115	REF	122	
R154	F3-T3	Res, met fim, 34k ±1%, 1/8w	261602	91637	Type MFF 1/8	REF	2 DAVE	当族才信
R155	B3-R4	Res, comp, 15Ω ±5%, ¼w	147876	01121	CB1505	1	1221	The search
R156	E4-S5	Res, met flm, 8.987M ±0.1%, 2w	261453	03888	Type PME80	1	1	a de la com
R157	C5-T1	Res, var, comp, 50k ±30%, 4w	281667	71450	Type U201	1	10	
R158	C2-T2	Res, ww, 900k, matched	5>					1 act
R159	B5-U3	Res, ww, 90k, matched	5		and the		1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
	D3-T5	Res, ww, 10k, matched	5		Asserting 17	12	SE	122
R160	1 10244	the second s	226225	91637	Type MFF 1/8	REF		1 25 1
R161	D3-W4	Res, met flm, 14.7k $\pm 1\%$, 1/8w	268409	91637	a second and a		in not	1.15
R162	1 Caster 1	Res, met flm, $1.18k \pm 1\%$, $1/8w$				the second	le ca	
R163	G4-X1	Res, met flm, 35k ±0.1%, 1/8w	229443	9163	and a second second			170
R164	G3-X1	Res, met flm, 35k ±0.1%, 1/8w	229443	9163	e successive and the second	and internet	Cal Cal	1001
R165	H2-X1	Res, met flm, 46.4k ±1%, 1/8w	188375	9163	7 Type MFF 1/3	8 1	NA SER	00

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	A States	REC	USE CODI
R166	F4-X1	Res, comp, 12k ±5%, ¼w	159731	01121	CB1235	REF		4 -55
R169	F4-W2	Res, comp, 270Ω ±5%, ¼w	160804	01121	CB2715	REF	s. Eq	nesk
R170	F1-W2	Res, comp, 15k ±5%, 4w	.148114	01121	CB1535	REF	38	
R172	F5-X1	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF	105	.e.s.X
R174	E1-W4	Res, met flm, 453Ω ±1%, 1/8w	267393	91637	Type MFF 1/8	1		1 관계
R175	12-W5	Res, var, ww, 10Ω ±10%, 2w	183921	71450	Type 115	1	1	1.5%
R176	I2-X3	Res, met flm, 28.7Ω ±1%, 1/8w	272823	91637	Type MFF 1/8	1		Sofia.
R177	H2-W5	Res, met flm, 13.7Ω ±1%, 1/8w	272815	91637	Type MFF 1/8	1	10%-	
R179	F4-X1	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF		uith EVE
R180	F2-S1	Res, comp, 470k ±5%, ¼w	188441	01121	CB4745	REF		
R181	I1-Y3	Res, comp, 3.3k ±5%, 4w	148056	01121	CB3325	REF	1652	
R184	E5-R2	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		
R185	E5-S1	Res, comp, 1.5M ±5%, ¼w	182857	01121	CB1555	REF	1.4	
R186	G2-Q3	Res, comp, factory selected	6					
R187	G1-Q5	Res, comp, 3.3Ω ±5%, ¼w	182279	01121	CB33G5	1		
R188	G5-Q4	Res, comp, 2.2k ±5%, 4/w	148049	01121	CB2225	REF		
R189	H1-R3	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF		
R191	G2-04	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	REF	ninnendelsin Sam S	
R193	H2-W5	Res, comp, $6.2\Omega \pm 5\%$, $4w$	272831	01121	CB62G5	961		2 1941
R194	F5-W3	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	REF	- 51	
R195	I1-Y4	Res, comp, 470Ω ±5%, ¼w	147983	01121	CB4715	2		
R196	diseriana a	Res, comp, 470Ω ±5%, ¼w	147983	01121	CB4715	REF	-	
R197	13-Q2	Res, comp, $27\Omega \pm 5\%$, $\frac{1}{4}$ w	160812	01121	CB2705	³ 21	and the second	-
R198	H2-X3	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF	1	
S1 thru S10	A4-M4	Switch assembly, front, S1 – S10	279406	89536	279406	100 1 1000	1	
	F4-M4	Switch assembly, rear, S1 – S10 (Figure 5-2)	279448	89536	279448	191 1 -	E	
T102	B3-O3	Transformer, inverter	253583	89536	253583	1	1	
V1	D5-K4	Tube, neon readout	266502	83594	B-5750S	4	4	
V2	D2-K4	Tube, neon readout	266502	83594	B-5750S	REF		

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	C. ALCOUNTED THE	REC QTY	USE COD
V3	С3-К4	Tube, neon readout	266502	83594	B-5750S	REF	1.	se d
V4	B5-K4	Tube, neon readout	266502	83594	B-5750S	REF	19	1.1.1 J
V5	F1-K4	Tube, polarity indicator	272922	83594	B-5856ST	1	1	ism's
XA3, XA4	C5-01	Socket, IC, 14 contact	276527	23880	TSA-2900-14W	2		13
XA5	D1-N2	Socket, IC, 16 contact	276535	23880	TSA-2900-16W	1		
XDS2	E3-L2	Socket, lamp	279372	89536	279372	1		
XF1	0.82	Fuse holder clip	284984	84613	3621-2	2		
XV1 thru	D3-L2	Socket, tube	268714	83594	SK207	5		
XV5	1,003			i March		\$ Post		92 Q
	一、夏田樹	Cover, tube (not illustrated)	281550	89536	281550	1		e f
	F5-Y5 .	Heat sink, Q1	186759	89536	186759	1	-11	gi il
	in dias	giorat neisel sector					22 1	aji na
	- Calata					1 13	82- E	ben A
	in the		a de trabaño	and the second	ANG MA	1.25	257 13	
	1.5	· LINERAL POTOD SCREET		a ingente.		福祉的	10 Å	Star
	3 1.5235	and the second second second second			and Art	1400	271	5 - 3 8 - 3
						1 and	ener (
R150 2155 R_56	en	 11, R15 and R22 are a matched set and must nee Amplifier Set, part number 269803. 12 and Q52 are a J-FET matched set and must must be and must be addressed an						
121		29 thru R32 are a matched set and must be r	eplaced as a set. I	For repla	cement, order A	C Con	verter	
1/2 1/2		esistor Set, part number 257014.						
	26	77, R79, R84, R89, R94 and R98 are a mate 59795. However, the resistors may be replac esignation and all information stamped on th	ed individually if a	model, se	rial number, fu	ll refer	ence one.	
	26 de	77, R79, R84, R89, R94 and R98 are a mate 59795. However, the resistors may be replac	ed individually if 1 e old resistor are i nust be replaced a	model, se ncluded	rial number, fu when ordering a	ll refer a new c	one.	
no si Rica Rica Rica Rica	20 de	77, R79, R84, R89, R94 and R98 are a matc 59795. However, the resistors may be replac esignation and all information stamped on th 158, R159 and R160 are a matched set and 1	ed individually if 1 e old resistor are in nust be replaced a 37.	model, se ncluded is a set. I	rial number, fu when ordering a	ll refer a new c	one.	



Figure 5-2. PRINTED CIRCUIT ASSEMBLY (S/N 123 THRU 3409 AND 3420 THRU 4097)

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Figure 5-3. PRINTED CIRCUIT ASSEMBLY (S/N



ASSEMBLY (S/N 123 THRU 3409 AND 3420 THRU 4097)

REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	C SPRINTING	REC QTY	USE CODI
C23	H5-N4	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	REF	1913	
C24.	H5-N2	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	REF	56	1000
C25	H5-M5	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	REF	1421	- 643
C26	H5-O3	Cap, plstc, 0.1 uf, factory selected	279430	89536	279430	REF		
C27	H4-P2	Cap, mica, 330 pf ±5%, 500v	148445	14655	CD15F331J	1		1912 A.S.
C28	H4-P2	Cap, mica, 100 pf ±5%, 500v	148494	14655	CD15F101J	1	5.0	1 683
C29	I1-P4	Cap, plstc, 0.47 uf ±10%, 250v	184366	73445	C280AE/A470K	1	10	1
C30	H1-05	Cap, plstc, 0.022 uf ±10%, 250v	234484	73445	C280AE/A22K	6	1.4/1	100
C31	G5-O5	Cap, plstc, 0.022 uf ±10%, 250v	234484	73445	C280AE/A22K	REF		3
C32	G4-05	Cap, plstc, 0.022 uf ±10%, 250v	234484	73445	C280AE/A22K	REF	13 13 13	1.662
C33	G2-O5	Cap, plstc, 0.022 uf ±10%, 250v	23448	73445	C280AE/A22K	REF	61684 1	1.28
C34	G5-P2	Cap, plstc, 0.047 uf ±10%, 120v	260562	84411	863UW47391	1	[:98-]	올랐다
C35	G4-P5	Cap, plstc, 0.1 uf ±10%, 250v	161992	73445	C280AE/A100K	5		190
C36	E5-S1	Cap, mica, 33 pf ±5%, 500v	160317	14655	CD15E330J	3	s≷₿	1.190
C37	F4-R5	Cap, plstc, 1.0 uf ±10%, 250v	190330	73445	C280AE/A1M	REF	1252	135
C38	C5-R5	Cap, cer, 0.0012 uf ±10%, 500v	106732	71590	CF-122	1	ERR	n R I
C39	G5-83	Cap, mica, 470 pf ±5%, 500v	148429	14655	CD19F471J	1	1-1 2-	X81.3
C42	D1-Q2	Cap, plstc, 0.047 uf ±10%, 250v	162008	73445	C280AE/A47K	1	147.74	- 89D
C43	D3-Q1	Cap, plstc, 0.033 uf ±10%, 250v	234492	73445	C280AE/A33K	REF	Kin]	6157
C44	B4-Q5	Cap, plstc, 1.0 uf ±10%, 250v	190330	73445	C280AE/A1M	REF	6-211	1093
C45	E3-S3	Cap, mica, 47 pf ±5%, 500v	148536	14655	CD15E470J	2	14.6.4	S(A)
C46	F4-R1	Cap, mica, 47 pf $\pm 5\%$, 500v	148536	14655	CD15E470J	REF	6.11	21917
C48	H3-S2	Cap, elect, 1 uf +75/-10%, 25v	165886	56289	30D105G- 025BA4	10 19		4790) 8790
C50	G1-W4	Cap, plstc, 0.1 uf ±10%, 250v	161992	73445	C280AE/A100K	REF	401	8190
C51	G1-W3	Cap, mica, 150 pf ±5%, 500v	148478	14655	CD15F151J	1	20	- EISO
C52	G2-W1	Cap, mica, 33 pf ±5%, 500v	160317	14655	CD15E330J	REF	GE	8680
C54	I3-R1	Cap, plstc, 0.22 uf ±10%, 250v	194803	73445	C280AE/A220K	REF	101	01310
C55	G4-N2	Cap, mica, 4 pf ±5%, 500 v (Figure 5-4)	190397	14655	CD15C040K	1	in a	CR20
C57	H4-N3	Cap, mica, 33 pf ±5%, 500v	160317	14655	CD15E330J	REF	ALC: N	1
C58	H2-Y2	Cap, plstc, 0.1 uf ±10%, 250v	161992	73445	C280AE/A100K	REF	and a second	



Figure 5-5. PRINTED CIRCUIT ASSEMBLY (S/N 3410 T)

IBLY (S/N 3410 THRU 3419 AND 4098 AND ON)



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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO		REC QTY	USE CODI
CR27	E4-V5	Diode, silicon, high speed switching	289595	03508	1N4148	REF		1
CR35	G4-R4	Diode, silicon, 200 ma, 25 piv	190272	93332	1N456A	REF	est)	
CR36	G2-P4	Diode, silicon, high speed switching	289595	03508	1N4148	REF	4561	- '9
CR37	F2-W4	Diode, zener, 3.9v	113316	07910	1N748	1	8:D.)	린상
CR45	C3-N5	Diode, silicon, 1 amp, 600 piv	112383	05277	1 N 4822	REF	123	5.5
CR46	F1-N5	Diode, silicon, high speed switching	289595	03508	1N4148	REF	1.11	2
CR47	D3-S3	Diode, silicon, high speed switching	289595	03508	1N4148	REF		0
CR48	D5-S1	" Diode, silicon, high speed switching	289595	03508	1N4148	REF	et al	\$11.6) \$
CR49	I1-V4	Diode, silicon, 150 ma	234468	07910	CD9039	REF	- 2.1	183
DS2	E4-L5	Lamp, Neon	266478	74276	A261	1	2	ιψ.
F2	D1-X5	Fuse, fast acting, ¼amp, 250v	109314	71400	Type AGC	1	194	"gadi(
Q1	F3-Y1	Tstr, silicon, NPN	183004	95303	40250	1	1	
Q2	H1-Y1	Tstr, silicon, PNP	195974	04713	2N3906	9	15	181
Q3	H2-X5	Tstr, silicon, PNP	195974	04713	2N3906	REF	2.1	
Q4	B5-S2	Tstr, silicon, NPN	272237	07263	2N4946	2	2	
Q5	B5-S4	Tstr, silicon, NPN	272237	07263	2N4946	REF		
Q6	G4-Z1	Tstr, silicon, PNP	195974	04713	2N3906	REF	e pi	
Q7	H1-Y5	Tstr, silicon, NPN	218396	04713	2N3904	11	12	
Q8	H1-Y5	Tstr, silicon, NPN	218396	04713	2N3904	REF	(nai)	
Q9	H1-Y2	Tstr, silicon, PNP	195974	04713	2N3906	REF	n o l	
Q10	H1-Y3	Tstr, silicon, NPN	168716	07263	S19254	1		
Q11	H4-Z2	Tstr, factory selected & matched to R15 and R22	$\overline{1}$	affiliadio prom	ans (m)			
Q12	G2-R5	Tstr, factory selected & matched to Q52	2>	C Satrona	nto azi			
Q13	H2-Q5	Tstr, FET, N-channel	288324	15818	U1994E	19	4	
Q14	H2-R3	Tstr, silicon, PNP	195974	04713	2N3906	REF		
Q15	H2-R4	Tstr, silicon, PNP	195974	04713	2N3906	REF		
Q16	G2-S1	Tstr. FET. N-channel	288324	15818	U1994E	REF		
Q17	G2-R3	Tstr, FET, N-channel	288324	15818	U1994E	REF		1
Q18	G2-R1	Tstr, FET, N-channel	288324	15818	U1994E	REF		
Q19	12-03	Tstr, silicon, PNP	288761	49956	RS2048	9	4	

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO		REC QTY	USE CODE
Q20	12-02	Tstr, silicon, PNP	288761	49956	RS2048	REF		19.80
Q21	I2-N4	Tstr, silicon, PNP	288761	49956	RS2048	REF	-16-3	à lư
Q22	12-N2	Tstr, silicon, PNP	288761	49956	RS2048	REF	212	3 25
Q23	I2-M5	Tstr, silicon, PNP	288761	49956	RS2048	REF	-3	$T_{1}^{i}(F)$
Q24	H5-M4	Tstr, silicon, PNP	195974	04713	2N3906	REF	ē g	-
Q25	I2-P5	Tstr, silicon, PNP	288761	49956	RS2048	REF	- de	ana()
Q26	I2-Q1	Tstr, silicon, PNP	195974	04713	2N3906	REF	-831	våge:
Q27	H1-Q1	Tstr, FET, N-channel	288324	15818	U1994E	REF	112	5일(종)
Q28	G3-Q2	Tstr, FET, N-channel	288324	15818	U1994E	REF	0.61	1.389
Q29	H1-Q2	Tstr, FET, N-channel	288324	15818	U1994E	REF	271	(1984) (1984)
Q30	H1-O3	Tstr, FET, N-channel	288324	15818	U1994E	REF	-135	- uçâ
Q31	G5-O3	Tstr, FET, N-channel	288324	15818	U1994E	REF		1.339
Q32	G3-O3	Tstr, FET, N-channel	288324	15818	U1994E	REF	1753	- 55
Q33	G2-O3	Tstr, FET, N-channel	288324	15818	U1994E	REF	112	1.0
Q34	H1-P1	Tstr, FET, N-channel	288324	15818	U1994E	REF	2.2.11	ie.
Q35	G3-P2	Tstr, FET, N-channel	288324	15818	U1994E	REF		1997
Q36	H1-P3	Tstr, FET, N-channel	288324	15818	U1994E	REF	-ind j	-30
Q37	G3-P3	Tstr, FET, N-channel	288324	15818	U1994E	REF	54	393
Q38	H1-P4	Tstr, FET, N-channel	288324	15818	U1994E	REF	1 1471	20
Q39	G3-P4	Tstr, FET, N-channel	288324	15818	U1994E	REF	-114	Dette
Q40	F1-P4	Tstr, germanium, NPN	182691	01295	GA3937	4	2	21 E G
Q41	E4-P4	Tstr, germanium, PNP	182709	01295	GA3938	4	2	138
Q42	E2-P4	Tstr, silicon, NPN	218396	04713	2N3904	REF		1
Q43	F1-P2	Tstr, germanium, NPN	182691	01295	GA3937	REF	-901	340
Q44	E4-P2	Tstr, germanium, PNP	182709	01295	GA3938	REF	1-231	(**1.14) (**1.14)
Q45	E2-P2	Tstr, silicon, NPN	218396	04713	2N3904	REF	1.5143	949
Q46	F1-05	Tstr, germanium, NPN	182691	01295	GA3937	REF		1.136
Q47	E4-05	Tstr, germanium, PNP	182709	01295	GA3938	REF	520	-110
Q48	E2-P1	Tstr, silicon, NPN	218396	04713	2N3904	REF	1	111
Q49	F1-03	Tstr, germanium, NPN	182691	01295	GA3937	REF	420	350
Q50	E4-03	Tstr, germanium, PNP	182691	01295	GA3938	REF	De 1	1001

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	120125-1125	REC QTY	USÉ CODI
Q51	E2-04	Tstr, silicon, NPN	218396	04713	2N3904	REF		72
Q52	E4-R3	Tstr, Factory selected & matched to Q12	2>	$\widetilde{\Omega}_{n}^{+}(\theta) _{\theta}$	i Renardinalij			3.8
Q53	F5-S2	Tstr, FET, dual, N-channel	257501	17856	DN423	1	1	
Q58 ′	F2-R3	Tstr, FET, N-channel	288324	15818	U1994E	REF	ia si	13
Q59	D1-S1	Tstr, silicon, NPN	218396	04713	2N3904	REF	40	- 32
Q60	C4-S2	Tstr, silicon, NPN	218396	04713	2N3904	REF	13. or	- Ĝ
Q65	E4-N4	Tstr, FET, N-channel	288324	15818	U1994E	REF	an)	²⁰ 115
Q66	E3-M5	Tstr, silicon, NPN	245480	07263	S24496	3	- 1 -	1168
Q67	H1-N3	Tstr, silicon, NPN	218511	95303	60994	4	2	11.2
Q68	G3-N3	Tstr, silicon, NPN	218511	95303	60994	REF		134
Q69	F5-N3	Tstr, silicon, NPN	218511	95303	60994	REF	-(1)	at a
Q70	F3-N3,	Tstr, silicon, NPN	218511	95303	60994	REF	1.001	(C13)
Q71	H1-N1	Tstr, silicon, PNP	266619	07263	2N4888	4	2	RES
Q72	G3-N1	Tstr, silicon, PNP	266619	07263	2N4888	REF		
Q73	G1-N1	Tstr, silicon, PNP	266619	07263	2N4888	REF		19
Q74	F3-N1	Tstr, silicon, PNP	266619	07263	2N4888	REF	-	13
Q75	F5-R4	Tstr, silicon, NPN	218396	04713	2N3904	REF	8-34 1	는 것(A)
Q76	E4-R2	Tstr, silicon, PNP	288761	49956	RS2048	REF	1	17.5
Q77	G4-W1	Tstr, FET, dual, N-channel	267963	17856	DN503	1	1	44
Q79	F3-W1	Tstr, silicon, PNP	195974	04713	2N3906	REF	목권	
Q80	F3-W3	Tstr, silicon, NPN	218396	04713	2N3904	REF	2-11) 	
Q81	F4-L5	Tstr, silicon, NPN	245480	07263	S24496	REF		
Q82	G2-L5	Tstr, silicon, NPN	245480	07263	S24496	REF		
Q84	E2-02	Tstr, silicon, NPN	218396	04713	2N3904	REF		
Q85	E5-R3	Tstr, FET, N-channel	261578	15818	U2366E	1		
Q86	D4-R5	Tstr, silicon, NPN	168708	03508	2N3391	2		
Q87	E2-R4	Tstr, silicon, NPN	168708	03508	2N3391	REF		
288	E2-R2	Tstr, silicon, PNP	288761	49956	RS2048	REF	6333	
Q89	D4-S1	Tstr, silicon, PNP	288761	49956	RS2048	REF		
R1	E1-Y1	Res, comp, 470Ω ±10%, 1w	109710	01121	GB4711	1		
R2	G3-X5	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	6	6254	

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	Constant State	REC QTY	USE CODE
R3	G3-X4	Res, comp, 33k ±5%, ¼w	148155	01121	CB3335	3	C - 1 - 1	l es
R4	G3-X4	Res, met flm, 38.3k ±1%, 1/8w	241372	91637	Type MFF1/8	1	315 5	- 120
R5	H2-X4	Res, met flm, 34k ±1%, 1/8w	261602	91637	Type MFF1/8	4	111	a - 1922
R6 ′	C1-R2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF1/8	5	12	
R7	C2-S2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF1/8	REF	40	
R8	C2-R2	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	5	1.1	4
R9	H1-Z2	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	REF	av. 1	$-\pi_{\mathbb{R}^n}$
R10	G3-X5	Res, comp, 270Ω ±5%, ¼w	160804	01121	CB2715	2		1981
R11	G3-Y5	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	REF	สหรื	- 29
R12	G3-Y5	Res, comp, 100k,±5%, ¼w	148189	01121	CB1045	1	1.5	5.055
R13	H1-Z1	Res, met flm, 12.1k ±1%, 1/8w	234997	91637	Type MFF 1/8	1	- 1	<2
R14	H1-Z1	Res, met flm, 2.94k ±1%, 1/8w	261628	91637	Type MFF 1/8	- 1	-275	1.1
R15	G3-Y3	Res, met flm, factory matched to Q11 and R22		Adam.	and a star			
R16	G3-Y1	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	8		
R17	G3-Y4	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	6		
R18	12-Z3	Res, met flm, 10k ±1%, 1/8w	168260	91637	Type MFF 1/8	1		
R19	12-Z3	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF		
R20	I2-Y5	Res, met flm, $412\Omega \pm 1\%$, $1/8w$	261636	91637	Type MFF 1/8	1		
R21	12-Z2	Res, met flm, 19.1k ±1%, 1/8w	148023	01121	CB1025	6		
R22	I2-Z1	Res, met flm, factory selected and matched to Q11 and R15		847126				
R23	I1-Y3	Res, var, ww, 10k ±10%, 2w	272740	71450	Type 115	3	-4/1	1.12
R24	I1-S4	Res, met flm, 1M ±0.1%, 2w	225953	03888	Type PME80	1	1.68	
R27	F4-V3	Res, comp, 1M ±10%, 2w	268227	01121	HB1051	1	970	185
R28	G3-V3	Res, comp, 1.2M ±5%, ¼w	188425	01121	CB1255	1	-isd g	1.146
R 29	G3-N3	Res, met flm, 449.1k, matched (Figure 5-4)	3	(9999) 30990	0200003914 466638(1274	alt te		
R30	G3-N2	Res, met flm, 49.9k, matched (Figure 5-4)	3	SSMR.	de Alizante T		8253	124
R31	G3-M5	Res, met flm, 4.99k, matched (Figure 5-4)	3>		antistar qossiligi		1983) 1933	- 688 - 25
R32	G3-M4	Res, met flm, 499Ω, matched (Figure 5-4)	3	62.775	Ride Sheet		515	10

REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO		REC	USE CODE
R34	D1-W3	Res, var, ww, 10k ±10%, 2w	272740	71450	Type 115	REF		
R35	E4-W4	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	REF	1 =2-	× fil
R36	E4-W1	Res, comp, 820k ±5%, ¼w	220541	01121	CB8245	1		1.1
R37	G4-S2	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	REF	8	
R39	G5-Q4	Res, comp, 18k ±5%, ¼w	148122	01121	CB1835	1	i sub	4
R40	H4-S2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF1/8	REF	1.1415	1123
R42	H4-R3	Res, met flm, 499k ±1%, 1/8w	268813	91637	Type MFF1/8	1		
R43	H1-R3	Res, met flm, 453k ±1%, 1/8w	295709	91637	Type MFF1/8	1	# 1. ij	1.1814
R45	G5-R1	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	8	115	
R46	H1-S2	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	REF	as ig	2.2.4
R47	H2-S2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF1/8	REF	- al	.₩ - 1] - 26;
R48	G1-T4	Res, var, ww, 200Ω ±20%, 1-4/w	144766'	71450	Type 110	2	ure di	291
R49	G3-T3	Res, ww, 6.45k ±0.5%, ½w	184416	89536	184416	1 0		1.200
R50	H1-T4	Res, var, ww, 10Ω ±10%, 1-¼w	112672	71450	Type 110	1		
R51	G4-T3	Res, ww, 2.853k ±0.03%, ½w	291039	89536	291039	1		-
R52	G4-S2	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	REF		261.4
R53	H4-P1	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF		- tot
R54	I2-P1	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	REF	-	-
R56	12-02	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF	5 0 m et	-
R57	I2-N5	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF	1.8 11	- 142
R58	I2-N3	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF		- (L. 11)
R59	I2-N1	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF	ien]	10-17
R60	I2-M3	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF	K TREE	
R61	I2-M4	Res, met flm, 200k ±1%, 1/8w	261701	91637	Type MFF1/8	2	104	1.003
R62	H5-04	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	REF	i seti	-
63	H5-02	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	REF		me.
64	H5-N5	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	REF		3196
65	H5-N3	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	REF	ine di	6.56
66 1	H4-N1	Res, comp, 22k ±5%, ¼w	1 R			REF		
67	H4-N1	Res, comp, 270k ±5%, ¼w			CB2745	1		20032
.68	[1-P3	Res, comp, 150k ±5%, ¼w			CB1545	2		1.52

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	Construction of the second	REC	USE CODE
R69	I1-P1	Res, comp, 470k ±5%, ¼w	188441	01121	CB4745	2	·	
R70	H4-Q1	Res, comp, 150k ±5%, ¼w	182212	01121	CB1545	REF	1. I.	4.2.58
R71	H4-Q2	Res, comp, 22k ±5%, ¼w	148130	01121	CB2235	REF		1168
R72	H4-P4	Res, comp, 1.5M ±5%, ¼w	182857	01121	CB1555	1		12 M
R73	H4-P5	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF.	-	. 5
R74	H4-Q2	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	13		2423
R75	F3-Q4	Res, met flm, 8.66k ±1%, 1/8w	260364	91637	Type MFF1/8	1		1374
R77	G1-O3	Res, ww, 3.4483k, matched	4	a state and	naan seri			- 1041
R78	G1-P3	Res, met flm, 422k ±1%, 1/8w	276626	91637	Type MFF1/8	1	4014	-
R79	F5-P4	Res, ww, 200k, matched		6.251	asaan asaan	1		
R80	D5-Q1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		195
R81	D5-P5	Res, comp, 15k ±5%, ¼w	148114	01121	CB1535	5	-1-1	or
R82	D5-P4	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	6		100-01
R83	F2-P3	Res, met flm, $232k \pm 1\%$, $1/8w$	276618	91637	Type MFF1/8	1	- 791	-
R84	F3-P3	Res, ww, 100k, matched		194281	Galeria a			×.
Section 20	D5-P2	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		4,816
6	D5-P3	Res, comp, 15k ±5%, ¼w	148114	01121	CB1535	REF	1.00	100
	D5-P1	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	REF	I	
R88	F4-03	Res, met flm, 121k ±1%, 1/8w	229369	91637	Type MFF1/8	2		
	F5-04	Res, ww, 50k, matched			n den de la			2.31
52693976	D5-05	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		. Š. L
ANY DEVELOC	D5-P1	Res, comp, 15k ±5%, ¼w	148114	01121	CB1535	REF		10 - 40
ACOLA.	D5-04	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	REF		10-57
R93	F2-03	Res, met flm, $121k \pm 1\%$, $1/8w$	229369	91637	Type MFF1/8	REF	a. 14	1.21
R94	F3-02	Res, ww, 50k, matched			unitati unit	1		- Guille
R95	D5-03	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		E HATE
R96	D5-03	Res, comp, 15k ±5%, ¼w	148114	01121	CB1535	REF	<u>इ</u> .२)म	1.94
R97	D5-01	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	REF	8794.00 10	
R98	E5-Q1	Res, ww, 200k, matched			artist as 1			
R99	G2-P5	Res, met flm, $11.9k \pm 0.25\%$, $1/8w$	290007	91637		1	dialati	
R100	G2-Q1	Res, met flm, $2k \pm 0.25\%$, $1/8w$	289991	91637	Type MFF1/8		145-11-52	2 ¹ 1919 1917-19

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	1.000	REC QTY	USE CODI
R101	H4-R3	Res, met flm, 19.1k ±1%, 1/8w	234963	91637	Type MFF1/8	REF	12.5	
R102	F1-S1	Res, met flm, 34k ±1%, 1/8w	261602	91637	Type MFF1/8	REF	6	
R103	F1-S1	Res, met flm, 34k ±1%, 1/8w	261602	91637	Type MFF1/8	REF	Best	
R104	E4-R4	Res, met flm, $45.3k \pm 1\%$, $1/8w$	234971	91637	Type MFF1/8	1	le est	
R110	E2-S1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF	shi o	
R111	H2-S2	Res, met flm, 19.1k ±1%, 1/8w	234963	91637	Type MFF1/8	REF	elsa [£
R112	H3-R3	Res, comp, 2.2M ±5%, ¼w	198390	01121	CB2255	2		1. J. J.
R118	E1-S1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF	5-1 B	lin an
R119	D1-R3	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		$p < \tau$
R120	C4-R3	Res, comp, 33k ±5%, ¼w	148155	01121	CB3335	REF	ing.	2
R121	C5-R3	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	7	gaz N.	
R124	D2-R3	Res, comp, 4.7k ±5%, ¼w	148072	01121	CB4725	1	dire.	R .
R127	F1-N1	Res, comp, 12k ±5%, ¼w	159731	01121	CB1235	2	4.5	5
R128	E2-N3	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF	igen)	i, a
R130	E4-N1	Res, comp, 62k ±5%, ¼w	220053	01121	CB6235	1		i and s
R131	H2-N5	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	8	24.1	4.15
R132	G5-N1	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF	1.1	10
R133	G4-N5	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF		2. - 19
R134	G2-N1	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF	vor si	1-13
R135	G2-N5	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF	12-111	ilr 12
R136	F5-N1	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF	Lee	Se g
R137	F4-N5	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF		
R138	F2-N1	Res, comp, 180k ±5%, ¼w	193441	01121	CB1845	REF	in d	161 - 8
R141	G5-N1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		
R142	G2-N1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF	396	iles a
R143	F4-N1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		
R144	F1-N1	Res, comp, 10k ±5%, ¼w	148106	01121	CB1035	REF		5.4
R145	E5-N1	Res, comp, 1.1k ±5%, ¼w	267336	01121	CB1125	1		Jeres
R146	C4-Q1	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	REF	201	10 1975-52
R147	D2-Q2	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	REF	1.1	-141F8
R149	E4-04	Res, var, ww, $200\Omega \pm 20\%$, 1-4/w	144766	71450	Type 110	REF	i.	463

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO		REC QTY	USE CODE
R152	F2-T3	Res, met flm, 200k $\pm 1\%$, 1/8w	261701	91637	Type MFF1/8	REF		Pig
R153	E5-T3	Res, var, ww, 10k ±10%, 2w	272740	71450	Type 115	REF		- P
R154	F3-T3	Res, met flm, 34k ±1%, 1/8w	261602	91637	Type MFF1/8	REF	6.74	10.00
R155	B5-R5	Res, comp, $15\Omega \pm 5\%$, $4w$	147876	01121	CB1505	1	1-1-1	1.4
R156	E3-S5	Res, met flm, 8.987M ±0.1%, 2w	261453	03888	Type PME80	1	1	al 3
R157	D1-T1	Res, var, comp, 50k ±30%, ¼w	281667	71450	Type U201	1	-10-	6.9
R158	C3-T3	Res, ww, 900k, matched	5	$M_{i}(i)$	in visite in		-1,8	- g - 10
R159	C2-U2	Res, ww, 90k, matched	5	.x.30(,)	genza nok		1 SET	
R160	D4-T4	Res, ww, 10k, matched	5	39997	Gines Model (* 1	1.41 Alexandria		- 3
R161	D4-W2	Res, met flm, 14.7k ±1%, 1/8w	226225	91637	Type MFF1/8	REF		15 - 222
R162	D4-W3	Res, met flm, 1.18k ±1%, 1/8w	268409	91637	Type MFF1/8	1	43	$\sim c^{2}$
R163	G4-W4	Res, met flm, 35k ±0.1%, 1/8w	229443	91637	Type MFF1/8	2	-261	1. 32
R164	G3-W4	Res, met flm, 35k ±0.1%, 1/8w	229443	91637	Type MFF1/8	REF	1.1.5	- 19
R165	H2-W4	Res, met flm, 46.4k ±1%, 1/8w	188375	91637	Type MFF1/8	1		37 'n
R166	F4-W4	Res, comp, 12k ±5%, ¼w	159731	01121	CB1235	REF	14.5	B
R169	F4-W1	Res, comp, 270Ω ±5%, ¼w	160804	01121	CB2715	REF		1 12
R170	F2-W1	Res, comp, 15k ±5%, ¼w	148114	01121	CB1535	REF	-	(- 1)g
R172	F5-W4	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF		1.68
R174	E1-W2	Res, met flm, 453Ω ±1%, 1/8w	267393	91637	Type MFF1/8	1	- A -	1.12
R175	I1-W4	Res, var, ww, $200\Omega \pm 10\%$, 2w	184465	71450	Type 115	1	1	2.19
R176	H5-X1	Res, met flm, $13.7\Omega \pm 1\%$, $1/8w$	272815	91637	Type MFF1/8	1		in set
R177	H2-W3	Res, met flm, $28.7\Omega \pm 1\%$, $1/8w$	272823	91637	Type MFF1/8	1	1.4.4	1.1
R178	F4-W4	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF	122	0.00
R180	F2-S1	Res, comp, 47k ±5%, ¼w	148163	01121	CB4735	REF	-2	1.3.13
R181	H4-X5	Res, comp, 3.3k ±5%, ¼w	148056	01121	CB3325	REF	50	51.12
R186	[254] 准备保	Res, comp, factory selected	6		a#2.5%		5.1	E
R187	G1-R2	Res, comp, 3.3Ω±5%, ¼w	182279	01121	CB33G5	1		s = z;
R188	G5-Q5	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF		2242
R189	G5-R4	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF		16-13
R191	G2-P1	Res, comp, 220k ±5%, ¼w	160937	01121	CB2245	REF		33 m3)
R193	H1-W3	Res, comp, $6.2\Omega \pm 5\%$, ¼w	272831	01121	CB62G5	1	1.3	94(5)

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REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	10-10-5600-0	REC QTY	USE COD
R194	G1-W2	Res, comp, 1.6k ±5%, ¼w	266197	01121	CB1625	REF		
R195	H4-Y1	Res, comp, $470\Omega \pm 5\%$, $\frac{1}{4}$ w	147983	01121	CB4715	2		ан ст. 1
R196	H3-Y2	Res, comp, $470\Omega \pm 5\%$, $\frac{1}{4}$ w	147983	01121	CB4715	REF	- 3	1
R197	I2-Q4	Res, comp, $27\Omega \pm 5\%$, $\frac{1}{4}$ w	160812	01121	CB2705	1	a de la	s fi
R198	H1-X1	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF	1	1.63
R199	G3-Y2	Res, comp, 1k ±5%, ¼w	148023	01121	CB1025	REF		-
R200	G3-Y3	Res, comp, 2.2M ±5%, ¼w	198390	01121	CB2255	REF		(s
R201	D2-Q1	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF		
R202	C5-P5	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF		
R203	H1-W2	Res, met flm, 2M ±1%, ½w	217760	91637	Type MFF ½	1		
R204	I3-X1	Res, met flm, 715k ±1%, 1/8w	236836	91637	Type MFF1/8	1		
R205	G4-Q3	Res, comp, 470k ±5%, ¼w	188441	01121	CB4745	REF		
R206	G1-Q4	Res, var, ww, 5Ω ±20%, 1-¼w	166348	71450	Type 110	1		
R207	E1-Q3	Res, comp, 33k ±5%, ¼w	148155	01121	CB3335	REF	1000	
R208	D5-Q5	Res, var, ww, $500\Omega \pm 10\%$, 1-4/w	112433	71450	Type 110	1		
R209	D5-R3	Res, comp, 82k ±5%, ¼w	188458	01121	CB8235	1		
R210	D5-R3	Res, comp, 1M ±5%, ¼w	182204	01121	CB1055	1	к.	
R211	E5-M4	Res, comp, 2.2k ±5%, ¼w	148049	01121	CB2225	REF		
S1 thru S10	B1-N2 F4-N2	Switch assembly, front, S1 – S10 Switch assembly, rear, S1 – S10 (Figure 5-4)	279406 279448	89536 89536	279406 279448	1	1	is R
Т102	C1-05	Transformer, inverter	253583	89536	253583	1	1	
and the second se	E2-L4 E2-L4	Tube, neon readout Tube, neon readout	266502 271494	83594 83594	B-5750S B5853ST	4 4	4	K L
1917-0 X X	D4-L4 D4-L4	Tube, neon readout Tube, neon readout	266502 271494	83594 83594	B-5750S B5853ST	REF REF		K L
1. State 1.	D1-L4 D1-L4	Tube, neon readout Tube, neon readout	266502 271494	83594 83594		REF REF		K L
and the second se	C3-L4 C3-L4	Tube, neon readout Tube, neon readout	266502 271494	83594 83594		REF REF		K L
V5	F1-L4	Tube, polarity indicator	272922	83594	B-5856ST	1	1	
XA3, XA4	C5-O3	Socket, IC, 14 contact	276527	23880	TSA-2900-14W	0.2		
XA5	D2-N5	Socket, IC, 16 contact	276535	23880	TSA-2900-16W	1	- 3	

Rev. 1

REF DES(G	INDEX NO	DESCRIP	TION	STOCK NO	MFR	MFR PART NO	and and a set		USE CODE
XDS2	F1-L5	Socket, lamp		279372	89536	279372	1		
XF1	C3-X5	Fuse holder clip	* F(e)	284984	84613	3621-2	2.		
XV1 thru		Socket, tube		268714	83594	SK207	5		
XV5		x ^{max}							
	D2-L5	Cover, tube		281550	89536	281550	1		
	F4-Y3	Heat sink, Q1		186759	89536	186759	1		
				e i jalans	Lastra .		1	2.1	
限制制度				Name:	gan h i				
Part				i Shana					
				(作例)	1844 ST			< 9.1	
				1. 1224	a Maku j				
	a di sina b			1 difference				i Ant	6 250-
1964	的导致			1883					
	para -			an Palater					
Read	(est f			1.00839					
Sec. of				THE GALL			423		
11:52							c-i-d		
[end 2 Q1 nui 3 R2 Res 4 R7	 R15 and R22 are a mat ce Amplifier Set, part num and Q52 are a J-FET ma mber 265744. thru R32 are a matched sistor Set, part number 257 R79, R84, R89, R94 an 	iber 269803. Itched set and mus set and must be re 7014. d R98 are a matcl	st be replaced as a eplaced as a eplaced as a set. Fo	set. For or replace	replacement, or ement, order A	rder pa C Conv	rt ⁄erter	
L F	205 des 5 811 Inp	595. However, the resistor ignation and all information 58, R159 and R160 are a r ut Divider Resistor Set, pa	ors may be replace on stamped on the natched set and m irt number 26978'	ed individually if m old resistor are in nust be replaced as 7.	odel, ser cluded v a set. Fo	ial number, ful hen ordering a	l refere new o	ne.	
6	6 > R18	86 is factory selected. For	replacement, repl	lace with exact val	ue.				en z Sonda Solaz

SECTION VI

ACCESSORIES AND OPTIONS

6-1. INTRODUCTION

6-2. This section of the manual contains information pertaining to the accessories and options which are available for the Model 8100A Digital Multimeter. Accessory information consists of a brief description of the accessory and its specifications. Option information is designated by the option number, which is printed in the top corner of each page. Option information includes the field installation procedure, operating instructions, and list of replaceable parts.

6-3. ACCESSORIES

6-4. High Frequency Probe

6-5. The High Frequency Probe (Fluke Model 80-RF) converts the Model 8100A into a high frequency ac voltmeter. The probe is used for ac voltage measurements of 0.25 volts to 30 volts over a frequency range of 100 kHz to 500 MHz. The accuracy of measurement is $\pm 5\%$ from 100 kHz to 100 MHz and $\pm 7\%$ to 500 MHz over a temperature range of $\pm 10^{\circ}$ C to $\pm 30^{\circ}$ C. The probe operates into any dc voltmeter with an input resistance of 10 megohms $\pm 10\%$. The shielded dual-banana plug on the probe permits direct connection to voltmeter input. Additional specifications for the probe are listed in Figure 6-1.

6-6. High Voltage Probes

6-7. The 80F-5 and 80F-15 Voltage Dividers (Figure 6-2) are designed for use with the Model 8100A Digital Multimeter. Full-scale input for the Model 80F-5 is 5000 volts dc and for the Model 80F-15 is 15,000 volts dc. In use, the dividers simply plug in to the Model 8100A input terminals, and high voltage connection is made using the attached probe. Figure 6-3 gives the specifications for both dividers.

8100A

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Figure 6-2. MODEL 80F-5/80F-15 HIGH VOLTAGE PROBES

PARAMETER	SPE	CIFICATIONS	
	MODEL 80F-5	MODEL 80F-15	
Ratio Accuracy	±0.01% of input from 500 to 5000 vdc.	±0.01% of input from 1000 to 15,000 vdc.	
Stability of Ratio	$\pm 0.01\%$ per month, $\pm 0.05\%$ per year (recalibrated by internal a ment)		
Temperature Coefficient	0.001%/ ^o C		
Voltage Coefficient	< 20 ppm total from 0 to 5000 vdc	< 20 ppm total from 0 to 15,000 vdc	
Division Ratio		1000 : 1	
Maximum Input Voltage	5000 vdc	15,000 vdc	
Input Impedance	50 ΜΩ	100 ΜΩ	
Operating Temperature Range	0°C to 50°C.		
Humidity Range	0 to 80%		
Altitude Range	Operating: 0 to 10,000 ft. No	n-operating: 0 to 50,000 ft.	
Weight	12 oz.	24 oz.	

Figure 6-3. VOLTAGE DIVIDER SPECIFICATIONS

6-8. Switched AC/DC Current Shunts

6-9. The Model A90 Current Shunt (Figure 6-4) enables the Model 8100A to be used for ac and dc current measurements to 10 amperes. The Model A90 is supplied in halfrack case to match the Model 8100A. The instruments may be conveniently mounted together in a 19-inch rack, with the Model A90 output terminals adjacent to the Model 8100A input terminals. The Model A90 provides six fullscale ranges as follows: 0.1, 1, 10, 100, and 1000 milliamps and 10 amperes (ac or dc). Full-scale input on any Model A90 range provides 100 millivolts dc to the input of the Model 8100A.

6-10. Front Panel Dust Cover

6-11. The vacuum-formed dust cover (Figure 6-5) attaches to the front of the Model 8100A. It prevents accumulation of dust on the front panel when the instrument is not in use and it affords added protection for the instrument when it is in transit. The Fluke number for the dust cover is 252205.

6-12. Rack Mount Kits

6-13. There are two rack mount kits available for the Model 8100A. One kit (No. 243287) allows two Model



Figure 6-4. MODEL A90 CURRENT SHUNT

8100A multimeters to be mounted side-by-side in a standard 19 inch rack, as shown in Figure 6-6. The two instruments are connected together at the center of the rack by two number 10 machine screws (not supplied). Holes for these screws are located in the front casting and are accessible after removing the decal at the side of the casting. The rack ears fasten to the front casting at either side, and are each held in place by two existing screws located under the decal. The second kit (No. 256594) supplies the hardware necessary to mount one Model 8100A in the center of the rack. In this case the rack ears are proportionally longer to permit center mounting.



Figure 6-5. FRONT PANEL DUST AND TOP COVER

6-14. OPTION INFORMATION

6-15. Introduction

6-16. The rechargeable battery pack kit (Option 8100– 01K) may be factory installed when the instrument is first purchased or it may be purchased separately and installed at a later time as a kit. The kit consists of a rechargeable battery, a ballast lamp, two resistors, three diodes, a battery condition meter and associated hardware. The following paragraphs give the field installation procedure, operating instructions, and list of replaceable parts for the battery option kit.

6-17. Installation Procedure

a. Disconnect the instrument from the line.

- b. Remove the top and bottom covers, the narrow trim strips just above and below the readout tubes, and the top and bottom guards; use the procedure given in Section 4, paragraph 4-9 of the manual.
 - Install and solder the 2 ohm and 11.5k ohm resistors on the PCB at the locations shown in Figure 6-7.

CAUTION

Use a 40-watt soldering iron and non-corrosive solder on all connections.

Install and solder the three diodes on the PCB at the locations shown in Figure 6-7. Be sure that the diodes are oriented according to the anodecathode markings that are etched on the PCB.

Install and solder the ballast lamp on the PCB at the location shown in Figure 6-7. Three solder connections are required: The anchor lug on the component side of the PCB should be soldered to the side of the lamp base once the lamp is in position, and the two lamp terminals should be soldered from the underneath side of the PCB.



c.

d.

e.

Figure 6-6. RACK MOUNT KIT



Figure 6-7. COMPONENT LOCATIONS FOR MODEL 8100A-01K BATTERY OPTION KIT INSTALLATION

- f. Refer to Figure 6-8 and install the meter by performing the following steps:
 - 1. Remove the nut and two washers which fasten the black tube cover to the inside of the front panel assembly near the INPUT terminals.
 - 2. Remove the meter cover away from the front panel; install the meter with the meter face against the front panel.
 - 3. Fasten the cover in place by replacing the nut and washers.
 - 4. Bend the meter lugs toward the rear of the instrument so they are parallel with the PCB and aligned with holes in the PCB. See Figure 6-8.
 - Connect each meter lug to hole in PCB directly under parallel bent meter lugs using suitable length of tinned #22 AWG copper wire. Solder each of four connections securely in place.

g. On underside of top guard, remove shield. See Figure 6-9. Install the battery pack on the underneath side of the top guard, using the battery pack holder and the six machine screws (Figure 6-9). Orient the battery pack so the grommeted battery leads pass through the grommet hole in the edge of the guard.





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6-5



BATTERY OPTION KIT INSTALLATION Figure 6-9.

REF DESIG	INDEX NO	DESCRIPTION	STOCK NO	MFR	MFR PART NO	125 SEAS	REC QTY	USE CODE
		DIGITAL MULTIMETER - KIT BATTERY OPTION 8100A-01K	296886	89536	296886	1		
BT1		Battery Pack Battery, rechargeable, 21.6v 18 cell	269837 272559	89536 06860	269837 401578	1	e	en Br
M1		Meter, 0-1 ma (See Figure 5-1)	266494	89536	266494	1	èsa è	e
XBT1		Holder, battery pack	269712	89536	269712	1		
CR32	E5-Z3	Diode, silicon, 1 amp, 600 piv (See Figure 5-3)	112383	05277	1N4822	2		
CR33	E5-Z2	Diode, silicon, 1 amp, 600 piv (See Figure 5-3)	112383	05277	1N4822	REF		
CR34	F5-Z2	Diode, silicon, 1 amp, 100 piv (See Figure 5-3)	116111	05277	1N4817	1		
DS11	G3-AA3	Lamp, incandescent, 24v (See Figure 5-3)	218354	08806	1252	1	2	15
R182	F5-Z3	· Res, comp, 2Ω ±5%, ½w (See Figure 5-3)	218735	01121	EB20G5	1		
R183	I3-AA3	Res, met flm, 11.5k $\pm 1\%$, 1/8w (See Figure 5-3)	267138	91637	Type MFF 1/8	1		

Replace the top guard and connect the battery leads to battery terminals at the right-rear of the PCB as shown in Figure 6-7. Red lead to + terminal and black lead to other terminal.

Replace the bottom guard and top and bottom covers.

6-18. **Operating Instructions**

h.

i.

6-19. The battery supplies power to the instrument when the instrument is disconnected from the line. The front panel meter monitors the charge condition of the battery during battery operation, BAT OK. During line operation, while the battery is charging, the meter indicates LINE OPR (full-scale). When fully charged, the battery will provide 8 hours of continual operation before recharging is necessary. When fully discharged, the battery requires a maximum of 10 hours of fully charge.

6-20. The ballast lamp, DS11, provides constant current charge even though line and battery levels may vary.



S4F

S5E

10 MΩ

> YEL

> RED

> BRN/WHT

- 3. Ø DENOTES INTERNAL SCREWDRIVER ADJUSTMENT
- 4. DENOTES FRONT PANEL LOCATION.
- 5. (COLOR) CONNECTORS FOR ISOLATED PRINTER OUTPUT OPTION (-02).



CAPACITANCES

CHANGES: R216 ADDED S/N 5050 (1)

AND ON

E SPECIFIED.

ON:

RIVER ADJUSTMENT.

TION.

OLATED 2).

FUNC	TIONAL SCHEMATIC DIAGR	AM
	MODEL 8100A	
1	DIGITAL MULTIMETER	
	SCHEMATIC NO. 1	
SER. NO	0. 3410 THRU 3419 AND	REV
11	4098 AND ON	c



NOTES:

1. ALL RESISTANCES IN OHMS AND ALL CAPACITANCES IN 8. MICROFARADS UNLESS OTHERWISE SPECIFIED.

2. ALL SWITCHES SHOWN IN OFF POSITION.

Rł 3. Ø DENOTES INTERNAL SCREWDRIVER ADJUSTMENT.

> 9. M

> 10

4. DENOTES FRONT PANEL LOCATION.

5. TLK TEST LINK.

6. 1> R186 SELECTED IN FINAL CALIBRATION.

(COLOR) CONNECTORS FOR ISOLATED PRINTER 7. OUTPUT OPTION (-02).





NOTES:

1. ALL RESISTANCES IN OHMS AND ALL CAPACITANCES IN MICROFARADS UNLESS OTHERWISE SPECIFIED.

10

- 2. ALL SWITCHES SHOWN IN OFF POSITION.
- 3. Ø DENOTES INTERNAL SCREWDRIVER ADJUSTMENT.
- 4. DENOTES FRONT PANEL LOCATION.
- 5. TLK TEST LINK
- 6. 1> R186 SELECTED IN FINAL CALIBRATION.

7. → (COLOR) CONNECTORS FOR ISOLATED PRINTER OUTPUT OPTION (-02).



°7

°A

9. *PLUS GATE ON WITH POSITIVE INPUT;

MINUS GATE ON WITH NEGATIVE INPUT.

5° 6°

BOTTOM VIEW

AM
REV.
-

ALIBRATION. DLATED PRINTER

TION.



NOTES :

1.

D

CHANGES:

ON.

(1)

(2)

3

(4)

FOR S/N 3410 THRU 3419,

R217 AND C67 ADDED S/N

> (COLOR) CONNECTORS

FOR ISOLATED PRINTE

OUTPUT OPTION (-02).

5018 AND ON.

4098 THRU 4365, & 4429 THRU 4477: C65 WAS 4uf. C68 ADDED S/N 5050 AND

VALUE SELECTED AT TEST OF Q11





4-24. SWITCH MODULE REPLACEMENT – FRONT SWITCHES (Figure 4-4).

a. Remove all front panel pushbuttons.

- b. Unsolder all wires connected to terminals of switch modules.
- c. Desolder entire front switch set by removing the solder from all switch pins on component side of main PCB.

CAUTION!

Use a desoldering tool and exercise extreme caution to avoid lifting land patterns or otherwise damaging PCB.

- d. Remove switch set from PCB.
- e. Remove defective switch module shaft and contacts according to the procedure given in paragraphs 4-21 and 4-22.
- f. Bend up locking tab (on switch support) which holds defective switch module in position. Remove and discard defective switch module. See Detail A, Figure 4-4.
- g. Install new switch module by reversing foregoing procedure.

4-25. Cleaning

4-26. The instrument should be cleaned periodically to remove dust, grease, and other contamination. The following procedure should be adhered to when cleaning the instrument:

- a. Remove loose contamination with low-pressure, clean, dry air. Pay particular attention to the front panel binding posts and binding post wiring.
- b. The front panel and exterior surfaces may be cleaned with anhydrous ethyl alcohol or a soft cloth dampened in a mild solution of detergent and water.

CAUTION!

Do not use aromatic hydrocarbons or chlorinated solvents on the front panel, because they will react with the Lexan binding posts.

4-27. PERFORMANCE TESTS

4-28. The following tests are intended for use in performance testing of the 8100A. The tests compare the instrument performance to the accuracy specifications and are especially suited to acceptance testing of new instruments. Tests should be conducted under the following conditions: ambient temperature $25^{\circ}C \pm 5^{\circ}C$, relative humidity less than 70%.

NOTE!

Tolerances and test limits for performance tests are derived from the 90-day instrument specifications.

4-29. DC Voltage Test

c.

4-30. In the following procedure, dc voltages are applied to the instrument at 100% of full scale on the 10, 100, and 1000 volt ranges and at 10% and 100% of full-scale on the 1 volt range. A dc voltage source equivalent to the one shown in Table 4-1 is required for this test.

·a.	Connect the 8100A to the ac line.
Ъ.	Set the 8100A controls as follows:

POWER	ON
Function	VDC
RANGE	As Required
FILTER	ON

Apply each of the input voltages shown in Table 4-2, in turn, to the INPUT terminals of the 8100A, The 8100A readout should be as indicated.

Table 4-2. DC VOLTAGE TEST REQUIREMENTS.

INPUT	MODEL 8100A			
(VOLTS DC)	RANGE	READOUT LIMITS		
+1000	1000	+999.7 to +1000.3		
+100	100	+99.97 to 100.03		
+10	10	+9,997 to 10.003		
+1.0001	1	+.9998 to +1.0004		
+0.0999	1	+.0998 to +.1000		
+0.1000	1	+.0999 to +.1001		

d. Repeat step (c) with negative input voltages. The 8100A readout should be the same as for positive

inputs, except that the polarity indication should be negative (-).

4-31. AC Voltage Test

4.32. In this test, full-scale voltages are applied to the instrument input at 100% of full scale on the 1, 10, and 100 volt ranges and 10% of full scale on the 1000 volt range. The 1 and 10 volt ranges are checked at 1 kHz, 10 kHz, and 20 kHz and the 100 and 1000 volt ranges are checked at 1 kHz only. An ac voltage source equivalent to the one shown in Table 4-1 is required for this test.

a. Connect the 8100A to the ac line.b. Set the 8100A controls as follows:

POWER	ON
Function	VAC
RANGE	As Required
FILTER	ON

c. Apply the ac test signals shown in Table 4-3 to the 8100A INPUT terminals. The readout should be as indicated.

4-33. Ohms Test.

4-34. The ohmmeter section of the instrument is checked by measuring standard resistors, whose values correspond to full scale readings on each ohms range. Standard resistors equivalent to those given in Table 4-1 should be used.

Connect the 8100A to the ac line.
Set the 8100A controls as follows:

POWER	ON
Function	As required
RANGE	As required
FILTER	ON

c. Connect each of the standard resistors shown in Table 4-4, in turn, to the INPUT terminals of the 8100A (use short, low-resistance connecting leads). Set the RANGE and function switches as required. The 8100A readout should be as indicated.

4-35. TROUBLESHOOTING

4-36. This section contains information selected to assist in troubleshooting the 8100A. Before attempting to troubleshoot the instrument, however, it should be verified that the trouble is actually in the instrument and is not caused by faulty external equipments or improper control 11-19-71 Table 4-3. A

AC VOLTAGE TEST REQUIREMENTS.

INPUT		MODEL 8100A	
FREQUENCY (KHZ)	RMS VOLTS	RANGE	READOUT LIMITS
1	1	1	.9975 to 1.0025
1	10	10	9.975 to 10.025
- 1	100	100 .	99.75 to 100.25
1	100	1000	099.3 to 100.7
10	1	1	.9975 to 1.0025
10	5.1	1	.9975 to 10.025
20	1	1	.9940 to 1.0060
20	10	10	9.940 to 10.060

Table 4-4. OHMS TEST REQUIREMENTS.

STANDARD	MODEL 8100A		
RESISTANCE	FUNCTION	RANGE	READOUT LIMITS
1.0000k	KΩ	1	.9994 to 1.0006
10.000k	ΚΩ	10	9.994 to 10.006
100.00k	ΚΩ	100	99.94 to 100.06
1000.0k	KΩ	1000	999.4 to 1000.6
10 MΩ	10 MΩ		9.989 to 10.011

settings. The performance tests (paragraph 4-29) are suggested as a first step in troubleshooting, since they may help to localize the trouble to a particular section of the instrument. If they fail to localize the trouble, the following individual tests may be helpful. Figure 4-5 shows location of circuit areas and test points on the PCB and Figure 4-6 shows pin connections for integrated circuits used in the instrument.

4-37. Power Consumption

4-38. This test verifies proper power consumption by the instrument.

a. Connect the 8100A through an ammeter to the ac line.

b. Press the POWER-ON switch and observe the reading on the ammeter. With only the POWER-ON switch depressed, the ammeter should indicate 42 ± 4 milliamps for an instrument without batteries or 80 ± 10 milliamps for an instrument with batteries.