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1. APPLICATION

The small-size universal Frequency Meter of type DFM-128 assures fast, accurate and reliable frequency, frequency ratio and averaged time period measurement in wide range. It is designed for general application purposes and favourably applicable for development, working calibration and service of telecommunication facilities.

The continuously variable sensitivity of the direct input assures correct frequency measurement even at multiple turnover wave-forms. The high sensitivity of the instrument makes the measurement of local oscillators and RF carriers without galvanic connection (with inductive loop) possible.

The prescaler input of the instrument, using external attenuator, has min. 50 dB working dynamic range, that provides possibility for measuring different magnitude signals. The measurement can be started by external electric signal and the result appears at the output of the instrument, or on the parallel connected printer.

2. TECHNICAL DATA

Frequency measurement:	2 Hz to 60 MHz (direct input) 50 MHz to 520 MHz (prescaler input)
Gate time:	0.1 s; 1 s; 10 s
Resolution:	0.1 Hz; l Hz; l0 Hz (direct input) l Hz, l0 Hz, l00 Hz (prescaler input)
Accuracy:	- l digit + clock error
Read-out:	in kHz or MHz with automatic decimal point shift
Averaged time period mea	surement:
Measuring range:	100 ns to 0.5 s
Resolution:	10 ns, 100 ns, 1/us
Resolution: Accuracy:	
	10 ns, 100 ns, l _/ us ⁺ l digit ⁺ clock error ⁺ trigger error/num-

Frequency measurement:

Measurement result:	<u>f (direct input)</u> f (ext. clock input) x Nfr
	Nfr: 10^5 ; 10^6 ; 10^7
	<u>f (prescaler input)</u> x Nfr f (ext. clock input)
	Nfr: 10^4 ; 10^5 ; 10^6
	<u>f (ext. clock input)</u> x Nper f (direct input) Nper: 10 ⁰ ; 10 ¹ ; 10 ²
Positions of gate time switch:	x10, x100, x1000
Frequency range:	(from ext. clock input): l kHz to 10 MHz
Signal input:	
Sensitivity	
Direct input:	30 mVrms up to 20 MHz, 40 mVrms to 60 MHz (sinusoidal input signals)
Prescaler input:	10 mVrms
Divisions:	xl, xl0, xl00 steps continuously variable within the individual ranges (direct input)
Input impedance:	l MOhm less than 30 pF (direct input) 50 Ohm (prescaler input)
Overloadability:	50 Vrms, xl (up to 10 kHz) 250 Vrms, xl0, xl00 (up to 10 kHz) 10 mVrms, xl, xl0, xl00 (up to 10 MHz)
	5 Vrms (prescaler input) 5 Vrms (ext. attenuator input)
Clock:	
Internal:	TCXOA31
Frequency:	10 MHz

Stability:	better than $\frac{+}{-5}$ 5x10 ⁻⁷ 0°C to 40°C
Ageing:	less than $\frac{+}{-}$ 5x10 ⁻⁷ /month; $\frac{+}{-}$ 5x10 ⁻⁸ /day (at constant temperature)
Output:	l MHz TTL level (with switch on back-panel in INT position)
External clock input:	
Input impedance:	l kOhm
Frequency:	l MHz
Sensitivity:	min. 100 mVrms (with switch on back-panel in EXT position)
Max. input signal:	2 Vrms
General data:	
Display time:	appr. 0.2 s to 4 s, continuously variable
HOLD mode:	new measurement is started by pushbutton MAN or by external electric signal
Display:	8 digit LED display
Outputs:	Parallel measurement data output with decimal point, printer control output, new measure- ment start blocking input.
Coding:	BCD 8421
Printing command signal:	appr. 1.4 ms wide, 0 \rightarrow 1 \rightarrow 0 TTL signal
Power consumption:	220 V - 10 % appr. 40 VA
Permissible ambient data	<u>:</u>
Working temperature:	0 to 40°C
Sensitivity:	at O°C between 50 MHz - 200 MHz = 25 mVrms at 40°C between 50 MHz - 200 MHz = 15 mVrms
Relative humidity:	max. 80 %
Storage temperature:	-25 to +50°C
Dimensions:	
Depth:	254 mm
Width:	220 mm
Height:	90 mm
Weight:	appr. 35 N

Accessories (price included):

l pc	Mains connector
l pc	Manual
l pc	BNC-BNC measuring cable
l pc	31-31 pin service card
l pc	15-15 pin service card
l pc	external attenuator of 20 dB
l pc	Fuse link
2 pcs	Needle-contact plug

Rights for alterations reserved!

3. DESCRIPTION OF OPERATION

3.1. The schematic diagram of the instrument is shown in Fig.1. on page 23. The instrument is constructed from the following units:

- Input stage (direct)
 Input stage (prescaler)
 Selector circuit
 Thermo-compensated crystal oscillator (TCX0)
 Clock selector circuit
 Clock scaler circuit
 Control circuit
 Signal gate
 Counters, storages, decoders, LED displays
 Printer output
- 11. Decimal point and unit display automatics
- 12. Supply unit

3.2. Input stage (direct, Drw. No.: DFM-128-131)

The input signal to be counted gets thru a 3-way compensated divider to a source or an emitter follower, that assures high input impedance. By means of circuit elements R5, D1, D2 overcurrent protection is provided. The signal gets thru the solid-state amplifier TR3 to the Schmitt circuit constructed from inverter elements IC1, at the output of which (5) unified TTL level compatible output signals occur. The continuous sensitivity change of the direct input can be performed by potmeter P1.

3.3. Input stage (prescaler, Drw. No.: DFM-128-132)

The HF input signal to be measured gets thru the wide band amplifier IC2 to the input No. 10 of ECL construction prescaler IC3. The diodes D3, D4 assure overcurrent protection. The divided in frequency by 10:1 signal appears at output No. 4 on ECL logic level from where it is transformed to TTL compatible output signal by the Schmitt circuit IC4 constructed from NAND elements. The digital prescaler IC3 oscillates without input signal. In order to prevent this from being visible on the counter display a further gating is applicated at TTL level.

In case of sufficient magnitude input signal the peak rectifier constructed from diodes D5, D6 and trimmed source follower TR4, TR5 drives the output of the level comparator IC5 into logic "1". The level comparator passes the divided output signal thru point 3 of IC4 to the output. The comparison level (and simultaneously the sensitivity, too) can be adjusted by potmeter P2.

3.4. <u>Supply unit of -5.2 V</u> (Drw. No.: DFM-128-133)

The unit assures supply voltage for the digital prescaler IC3. The circuit is a series transistor (TR6) regulator equipped with error signal amplifier (TR7). The output voltage can be changed by means of potmeter P3.

Changing the output voltage the optimum state(sensitivity, working dynamic range) of the digital prescaler IC3 can be adjusted.

3.5. Control circuit I. (Drw. No.: DFM-128-15B)

Operation in frequency meter mode:

With switch K7 on the back-panel in INT position, the gate signal is produced from the 1 MHz signal (arriving at point CS8/3) of the thermo-compensated internal crystal oscillator, while with switch K7 in EXT position it is produced by attenuation from the external 1 MHz clock signal connected to the BNC CS3. The electronic selection of the internal or external clock is assured by gate network constructed from case IC15, while the attenuation of the clock signals is assured by number chain decades IC5, IC6, IC7, IC13, IC14, IC21. The output signal determining the magnitude of the gating signal appears at the point 8. of IC20. It can be of 0.01 s, 0.1 s, and 1 s period.The gate driver synthesizes the control signal necessary for opening the gate (IC10, output 8.) from the time quantums (of 0.01 s, 0.1 s, 1 s). The flip-flops IC2, IC3, IC4 form an 11 state number chain. The 11th state of the counter is decoded by the output 11. of NAND element IC12.

From states 1 to 10 gating level (logic "1") is present at the output 11 of IC12, while a gate closing level (logic "0") in the 11th state. The duration of the 11th state can be extended by the output signal arriving at point CS8 of the monostable multivibrator (DFM--128-16B) described later. This provides possibility for changing the sampling rate. The auxiliary signals necessary for evaluating the measurement are generated by the monoflops IC11 (zeroing signal) and IC19 (overfeed signal), while the printing command necessary for recording the result by the monostable circuit IC16. The zeroing signal appears at the junction point CS7b/23, the overfeed signal at CS7b/1, CS7b/2, and CS7b/4, while the printing command signal at CS7b/28. The visual display of the gate signal is assured by the LED titled GATE (DFM-128-14B) connected to point CS7b/3. One input of the signal gate (terminal 10 of IC10) is supplied with properly unified by the input stages signal to be measured, while its other input (terminal 9 of IC10) with the decimal gating signal derived from the clock. In frequency meter mode the switch K2 should be set

either to 60 MHz or to 520 MHz position, accordingly either the output signal of the 60 MHz (direct) input stage connected to point CS7b/10, or that of the 520 MHz (prescaler) input stage connected to point CS6b/15 appears at the output 6 of IC10. The first counter decade (IC9) of the counted pulses comming from signal gate is located near the signal gate due to velocity reasons. The display store of the first lowest digit.is the IC17 at the output (CS7b/17, CS7b/18, CS7b/20, CS7b/21) of which the result appears in BCD code.

The space ratio of periods to be measured necessary for display counters is formed in case of maximum operating frequency approximately to value 1:1 by the monostable multivibrator ICl assuring proper operation for the 2nd counter decade (DFM-128-14B, LD 12).

Operation in average time period mode:

In this mode the signals arriving at the signal gate inputs interchange. The periods to be measured get to input 9 of IClO, while the signal of the clock to its input 10. The measured periods unified by the 60 MHz input stage get thru connection CS7b/10 to input 13 of IC24, from where passing thru an electronic switching circuit arrive at point 6 of IC24.

The magnitudes of period averaging are specified by switch K3 and these can be of times 10,times 100 and times 1000. The necessary for quantization 10 kHz signal originating from the clock is connected to output 4 of IC18. With switch K2 in PER position this signal appears at the output 8 of IC18 from where thru output 11 of IC18 and output 6 of IC10 gets inverted to the input 10 of signal gate IC10. The operation of the further circuit units is identical to that described under frequency meter mode.

Operation in frequency ratio measurement mode:

In this mode 3 cases are possible:

- 1./ The higher frequency is connected to the 60 MHz (direct) input, while the lower one to input (CS3) titled EXT. INP. on the back-panel. In this case the switch K7 should be in EXT position. The operation is identical to that described under frequency meter mode, except function of the clock is undertaken by the input signal led to connection titled EXT. INP. The result will be as described in "Technical data".
- 2./ The higher frequency is connected to the 520 MHz(prescaler) input CS2, while the lower frequency to the input (CS3) titled EXT. INP. on the back-panel. The switch K7 should be in EXT position. The operation is identical to that

described under frequency meter mode, but the function of the clock is undertaken by the input signal led to connection EXT.INP. The result will be as described in "Technical data".

3./ The higher frequency is connected to the external clock input titled EXT. INP., while the lower one to the 60 MHz (direct)input CS1.

The switch Kl should be set to PER. position. The operation is identical to that described under period measuring mode, but the function of the clock is undertaken by the input signal led to connection EXT. INP. The result will be as described in"Technical data".

3.6. Control circuit II. (Drw. No.: DFM-128-16B)

This card contains the internal clock consisting of the thermo--compensated crystal oscillator Ql, the inverter element construction Schmitt circuit IC2, and the decade attenuator IC1.

For internal clock functions a 1 MHz TTL level signal appears at the junction point CS10b/13 for the control circuit I. The light intensity modulation change of the numeric display is assured by changing the quasi-stable state of the monostable multivibrator IC9. The change of the quasi-stable is performed by means of potmeter P1.

The modulating signals for the display panel appear with 1 MHz repetition rate at the junction points CS10b/8 and CS10b/4. Performing continually repeating measurements by the instrument, the sampling rate is determined by the length of quasi-stable state of the monostable circuit consisting of IC14 and IC5. The output pulse Q of the monostable multivibrator (at point 6 of IC14) passing thru the gate circuit IC13 appears at the junction point CS9b/29 and determines the length of 11th state of the numeric chain located on the control circuit I. PCB (DFM-128-15B). The quasi-stable state is changed by means of potmeter P2 titled "SAMPLE RATE". With turn-knob in "HOLD" position the J-K marked IC4 serves a flip-flop.

After turnover it supplies such a level thru its output 0 (point 11 of IC4) that getting to the junction point CS9b/29 holds the above mentioned number chain in the 11th state. Thus no new measurement can start until the flip-flop IC4 is tilted back either by the push--button "HAND" or by connecting the signal transition 1-0 arriving at the "PRINT.INPUT" (CS10b/6) to its point 8. The R-S flip-flop constructed from NAND elements IC12 prevents the push-button K4 from bounce. The monostable pulse (resetting IC4) arriving from two places is generated by circuits IC3, IC10, and IC12. The monostable multivibrator IC11 play role in printing mode. After printing is performed, 1-0 signal transition occurs at the junction point CS10b/6 providing delay according to the quasi-stable state of monostable circuit IC11.

Periodicity of printing is determined by positions of switch K6. In position III the printing is repeated most frequently, in position II the printing is less frequency, while in position I no new printing is started. In this latter case the printing can be started by depressing push-button K4 titled "HAND" on the front panel

The decimal point and the unit display circuit - the operation of which is determined by the positions of switch K2 and K3, respectively - is constructed from IC6, IC7, IC8, IC15, IC16. The decimal point informations necessary for driving the display panel (DFM-128-14B) appear at the junction points CS9b/9, CS9b/10, CS9b/11 and CS9b/12, while those necessary for displaying the unit at the junction points CS9b/7, CS9b/5.

3.7. Power supply circuit I. (Drw. No.: DF?-128-122)

The ac voltage arriving from coil 7-8 of the mains transformer is rectified by Dl and stabilized by the series stabilizer consisting of ICl and Tl. The C2 performs buffering while the other capacitors prevent the singing. The stabilized 5 V appears at the junction point CS14b/9 and 10.

3.8. Power supply circuit II. (Drw. No.: DFM-128-123)

The ac voltage arriving from coil 3-4 and 5-6 of the mains transformer is rectified by D2 and D3 and buffered by C6 and C11, respectively.

The regulation is performed by stabilizer IC2 and IC3, respectively.As a result of different grounding of the output of ICs +15 V and -15 V occurs at the junction point CS15b/7 and CS15b/10, respectively.

3.9. Display circuit (Drw. No.: DFM-128-14B)

The function of this panel is to decode and display by LD12 the BCD coded signals arriving from the outputs of storage IC17 of the control circuit I (DFM-128-15B), further to count, store, decode and display the gated pulses arriving at its input 43. These functions are performed by (7 pcs. TIL 306 type) ICs LD5-LD11.

The control of storages is performed by overfeed pulses arriving the point 42, while the counters are zeroed by means of zeroing pulses arriving at the point 41. The output of each storage as well as the decimal points (from 10° to 10°) are tapped out for numeric printer connectible to the instrument. The intensity modulation of displays is performed by pulses arriving at the points 44 and 45.

The control for displaying decimal point is performed thru junction point 33, 34, 35, and 36.

The LEDs indicating the unit and the gate time, as well as the switches K8, K4 and potmeter P2 are located also on this panel.

4. INSTRUCTIONS FOR OPERATIONS

4.1. Putting the instrument into operation

Use only mains connection (of 220 V, 50 Hz) equipped with protective grounding.

The mains voltage is switched on by setting the switch on the back-panel to "ON" position.

The instrument should be connected to the mains by means of the (3-core) mains cord supplied as accessory. The occassional replacement of the primary fuse (of nominal value 0.5 A) can easily be performed on the back-panel.

The back-panel mains cord first should be connected to the instrument, first pull the mains cord out of the mains connector. When the instrument is switched on allow 10-minute warm-up.

4.2. Description of controls of the instrument (Drw. No.: DFM-128 B)

With switch K2 in FREQ positions the required input (of 60 MHz or 520 MHz) can be selected for frequency measurement purposes.

In position PER the instrument measures the average time period of the signal led to the 60 MHz input.

The signal to be measured is led to the BNCs (CS1, CS2) titled INPUT.

By means of switch Kl the measurement ranges (xl, xl0, xl00) are adjusted according to magnitude of input signal led to input CSl. The stepless change of the sensitivity at this input can be performed

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by means of potmeter Pl "SENSITIVITY". The required gate time in frequency meter mode, and the rate of averaging (x10, x100, x1000) in average time period measuring mode is set by means of switch K3. In frequency ratio measurement modes by means of this switch the decimal factors occuring in the results can be adjusted as specified by Technical data.

Visual display of the signal gate opening time is assured by LED (LD4) titled GATE.

The required sampling rate can be adjusted by means of potmeter P2 titled SAMPLE RATE.With potmeter is HOLD position, the periodicity of sampling terminates and the new measurement can be started either by depressing push-button MAN or by external electric signal (1-0 signal transition) led to printer connection.

The unit (MHz, kHz, ms) of the measured result is displayed by LEDs.

Controls located on the back-panel of instrument:

When switching the instrument on the switch on the back-panel should be set to ON position. Using internal base-pulse (clock) the switch K7 should be set to INT position.

In this case 1 MHz TTL level base pulse (clock signal) can be obtained from the BNC (CS3) titled IN/OUT on the back-panel. Using external clock or frequency ratio measurement mode the signal should be connected to the BNC jack (CS3) IN/OUT with switch K7 in EXT position. If printer is used the measurement result to be recorded (in BCD code) appears at the 20-pin needle contact connection marked 1, 2.

The rate of printing is set by means of sliding switch PRINT SPEED.

5. INSTRUCTION FOR USE

By means of the multi-purpose meter DFM-128 frequency, frequency ratio, and average time period can be measured as specified in "Technical data". The measurement result appears in BCD code at the two 20-pin connections on the back-panel making record by numeric printer possible.

5.1. Frequency measurement at the (direct) input titled 60 MHz

Switch on the instrument by means of switch on the back-panel. In case of using internal clock (base-pulse generator) the two-way sliding switch on the back-panel should be set to "INT" position.

In this case a 1 MHz TTL level output signal can be obtained from the BNC titled IN/OUT.

Using external clock the two-way switch is set to EXT position and the signal of the 1 MHz external clock should be led to connection IN/OUT.

The signal to be measured should be connected to the intput "60 MHz"and the input attenuator should be set according to the magnitude of the signal.

When the rms value of the signal is between 30 mV and 300 mV, the input attenuator should be set to position "xl", with signal between 0.3 V and 3 Vrms - to position "xl0", and above 3 Vrms - to "xl00".The continuous attenuation of the signal within the individual ranges is performed by means of the potmeter "SENSITIVITY". The instrument is most sensitive with attenuator in "xl" position and potmeter turned to "MAX". If the magnitude of the signal to be measured is unknown, it is practicable to set the input attenuator to "xl00" position. The 3-way sliding switch titled "PER FREQ" should be set to neutral position and the required gate time - determining the measurement result resolution - adjusted by means of 3-way sliding switch titled "0.1 s", "1 s", "10 s". (For example in "1 s" position the lowest digit corresponds to Hz.)

The decimal point appearing on the display should be understood as MHz or kHz depending upon the illuminating LED.

The measurement rate is continuously variable by means of the potmeter "SAMPLE RATE" and with potmeter in "HOLD" position single measurement can be performed by depressing the pushbutton "MAN".

During open time of the gate the LED "GATE" illuminates.

5.2. Averaged period time measurement at the (direct) input 60 MHz

The signal to be measured should be connected to input "60 MHz". The 3-way sliding switch "PER FREQ" should be set to "PER" position. Depending upon the magnitude of signal to be measured the input attenuator "x1, x10, x100" and the potmeter "SENSITIVITY" should be adjusted. The magnitude of averaging can be selected by sliding switch "0.1 s, 1 s, 10 s" for times 10, 100 or 1000 values.

An advantage of the average period measurement is that the trigger error reduces according to the rate of averaging. The decimal point appearing on the display should always be understood as ms.

The increase of the number of averaging increases the resolution (the result appears with more digits) and the measurement time.

The change of the measurement speed is the same as described at frequency measurement. During the open-time of periods to be measured the LED "GATE" illuminates.

5.3. Frequency measurement at the "520 MHz" (prescaler) input

The signal to be measured should be connected to the input titled "520 MHz". Make sure the input signal does not exceed the 5 Vrms voltage level. If necessary use the external 20 dB attenuator - supplied as accessory - assuring min. 50 dB working dynamic range for the prescaler input.

The 3-way sliding switch "PER FREQ" should be set to right"FREQ" position. The required gate time - determining the measurement result resolution - is adjusted by means of the 3-way sliding switch "0.1 s, 1 s, 10 s". Due to the times 10 prescale the resolution eg. in "1 s" position is 10 Hz.

The decimal point appearing on display should be understood as kHz or MHz according to the illuminating LED.

5.4. Frequency measurement in "FREQ" mode

By means of the instrument ratio of two frequencies can be measured with the following adjustment.

The sliding switch on the back-panel should be set to "EXT" position, while the switch "PER FREQ" on the front panel to "FREQ" position. (With switch on back-panel in center position the 60 MHz input, while in right position the 520 MHz one is used.)

The measurement range is: f_1/f_2 where: f_1 is 2 Hz to 60 MHz using direct input, or 50 MHz to 520 MHz using prescaler input. f_2 is the 1 kHz to 10 MHz signal connected to external clock input "IN70UT". The measurement result will be as specified in "Technical data". The measurement speed is changed as described earlier.

5.5. Frequency measurement in "PERIOD" mode

The switch on the back-panel should be set to "EXT" position and the switch "PER FREQ" on the front panel to "PER" position.

The measurement range is: f_1/f_2 where: f_1 is the l kHz to 10 MHz signal connected to external clock input "IN/OUT".

f₂ is the 2 Hz to 60 MHz signal connected to direct input. The measurement result will be as specified in "Technical data". The measurement speed is changed as described earlier.

5.6. Recording the measurement result by printer (eg. of EMG-14892 type)

The measurement result appears at the connection "1" and "2" on the back-panel in form of parallel BCD coded TTL logic level signals.

Connect the connections "1" and "2" to the input points of the printer conforming the proper digits.

The decimal point and the necessary unit display should be wired on the printer by the user. Before printing the measurement result - that can be frequency, average time period or frequency ratio - the turn knob "SAMPLE RATE" should be set to "HOLD" position. The printing command signal (eg. signal B2 of EMG-14892 printer) appears at terminal A5 of connection "2" on the back-panel. The new measurement can be started by 1-0 signal transition arriving at point C5 of connection "2" (it is the signal M1 of EMG-14892 printer). This 1-0 signal transition is delayed in different degree depending upon the position of switch "PRINT SPEED" on the back--panel.

With switch in position "III" the printing repetition rate is the highest appr. 2 s , in position "II" is lower (appr. 10 s) while in position "I" the restart of printing is performed by means of push-button "MAN" on the front panel. In this latter way the printing is controled manually and performed optionally from the front panel. The result appearing on the display is tapped in parallel BCD code also to the connections CS4 and CS5 titled 2 and 1, resp. . Weighting of the BCD code corresponds to the usual $A-2^0$, $B-2^1$, $C-2^2$ and $D-2^3$. The wiring of the connections is shown in the below Table:

Digi†	Weighting	Connection	Junction point
MSB	A	2	A4
107	В	2	B3
10	С	2	C4
	D	2	B4
_	A	2	A3
106	В	2	B2
	С	2	C2
	D	2	C3
	A	2	A۱
.5	В	2	BI
105	С	2	CI
	D	2	A2
	A		A6
104	В		B6
10	С		C7
	D		A7
	А		A5
10 ³	В		B5
10	С		C5
	D	I	C6
	A	1	A4
10 ²	В		B3
10	C		C4
	D	· · · · · · · · · · · · · · · · · · ·	B4

Digit	Weighting	Connection	Junction point
· · · · · · · · · · · · · · · · · · ·	A	l	A3
10 ¹	В	ļ	B2
10	C		C2
	D	l	C3
LSB	A		AL
100	В	I	BI
10	С	I	CI
	D	Ι	A2
Printing	command signal /B2/	2	A5
Start of	new measurement /MI/	2	C5
Decimal p	point belonging to 5th digit /DP I.	/ 2	B6
Decimal point belonging to 4th digit /DP		/ 2	В5
Decimal p	point belonging to 3th digit /DP 3.	/ 2	A7
Decimal p	point belonging to 2th digit /DP 4.	/ 2	A6

6. MAINTENANCE AND REPAIR

The instrument requires no special maintenance, however, after a few months of operation it should be dust cleaned by blown air.

It is practicable to tune of frequency of the thermo-compensated crystal oscillator approx. every 3 months to a calibrated higher stability and accuracy standard. The control of the oscillator is screw-driver adjustable.

6.1. Repair

The cover plate of the instrument can be removed by backing off the 4 capscrews on each side. By means of the service cards supplied as accessories both the supply unit PCBs and the control circuit PCBs are accessible at both sides. The repair of the damaged instrument should be started by checking the supply unit.

If the output voltages of the supply unit are proper, go on with trouble-shooting.

After this it is practicable to check the decade—attenuated signals at the outputs IC12, IC15, IC21, IC14, IC6, IC7 of the clock decade attenuators (DFM-128-15B) for present.

The test point of the signal gate is the output 3 and 8 of IC10.

The test points of the output of input stage (DFM-128-13B) are: outputs 4 and 5 of resistors R36 and R19.

Supplying input signal of value corresponding to the sensitivity to the input, unified TTL level signals should occur at the above mentioned points.

Perform the test by means of oscillator.

In case of insufficient sensitivity locate the damaged part by means of voltage measurement.

Testing the display, the complete display (counter, storage, decoder and LED) unit, as well as the decimal point and unit display circuit (DFM-128-14B) should be checked for proper operation.

The damaged parts should be replaced.

6.2. Instruments necessary for repair

l. Signal generator:	Frequency range: Output level:	2 Hz to 520 MHz 10 mVrms to 10 mVrms adjustable
2. Oscilloscope:	Frequency range: Sensitivity:	DC, min. 60 MHz min. 10 mVrms
3. Digital frequency meter:	Frequency range: Accuracy:	2 Hz to 520 MHz min. lx10 ⁻⁸

4. Digital DC volt-		
meter:	Measuring range:	l mV to min. 20 V
	Accuracy:	min. 0.2 %







Multi-purpose frequency meter

Posi-		Drawing No.	Description	Value	Maker	Q†y
tion	symbol 2.	or Type No. 3.	4.	5.	6.	7.
	2.	J.	÷.	٦.	0.	/.
١.	K2, K3, K6	S6T 11033/004	Sliding switch	3-way As per catalog	EMI- -SOUND	3
2.	K7	KC121.121	Sliding switch	1.151.0038	Kontakta	I
3.						
4.						
5.	Cs3	R141 554 /UG 625 B/u/	Concentric conn. socket	As per catalog	Radiall	I
6.	Cs4/a Cs5/a Cs11/a Cs12/a Cs13/a	DS2112- -220.1-3	20-pole socket	1.506.0225	Kontakta	5
7.						
8.	Cs7/a Cs9/a	377 3323 222 6010	31-pole PCB connector /female/	As per catalog	RFT	2
9.	Cs8/a Cs10/a	377 3323 222 4010	15-pole PCB connector /female/	As per catalog	RFT	2
10.						
11.						
12.	CsII/b CsI2/b CsI3/b	DS2112- -220.2-3	20-pole jack	1.506.0237	Kontakta	3
13.						
14.						
15.		DFM-128-12	Supply unit	Furnished with separate		ļ

۱.	2.	3.	4.	5.	6.	7.
16.		DFM-128-13	Input stage	Furnished with separate		1
17.		DFM-128-14	Display circuit	Furnished with separate		1
18.		DFM-128-15	Control circuit I.	Furnished with separate		I
19.		DFM-128-16	Control circuit II.	Furnished with separate		1





Posi- tion	Circuit symbol	Drw. No. or. Type No.	Description	Value	Maker	Q†y
١.	2.	3.	4.	5.	6.	7.
۱.	RI, R2	R5 I O	Resistor	11 - 5 % 0,5 W MSZ 05 61.1004	Remix	2
2.						
3.	CI	C216	Capacitor	100+2x2,5 n 250 V RX-74.121/1	Remix	ł
4.	C2	B41010- -C5478-T	Electrolite capacitor	4700 µ F <mark>+50</mark> % 25 V -20 % As per catalog	Siemens	l
5.	C3	CE2089-SA	Electrolite capacitor	+100 % 25 V 22 µ −10 25/085/21 MSZ 1558-70	MM	ł
6.	C4	CE5863-SA	Electrolite capacitor	+100 % 40 V -10 % 40 V 55/085-21 MSZ 1558-70	ММ	1
7.	C 5,CIO	C 219	Capacitor	µ – 10 % 63 V RX-74.258/3	Remix	2
8.	C 6,C11	B 41010- -C5108-T	Electrolite capacitor	+50 % 25 v 10 µ −10 As per catalog	Siemens	2
9.	C7, C8, C12,C13	ТК783	Ca pacitor	+80	CsNK	4
10.	C9, CI4	C E2118-SA	Electrolite ca pacitor	+100 % 16 V -10 % 16 V 25/085/21 MSZ 1558-70	MM	2
11.	C15	C E2059-SA	Electrolite capacitor	۱00 % 25 V −10 % 25 V 25/085/21 MSZ 1558-70	ММ	

1.	2.	3.	4.	5.	6.	7.
12.	C16	FSIM N47	Capacitor	100 n ⁺ 10 % 63 V 6x9x2,5 As per catalog	Kõporc	1
13.						
14.	Trl	BD246A	Transistor	As per catalog	Texas	1
15.	DI	B40 C3700/ 2200 S1	Diode	As per catalog	AEG	I
16.	D2, D3	B80 C1500	Diode	As per catalog	ITT	2
17.						
18.	ICI	A 7805UC ىر	Integrated circuit	As per catalog	FAIR	1
19.	1C2, 1C3	A 7815UC ىر	Integrated circuit	As per catalog	FAIR	2
20.						
21.	BI	VP1-1 0,5 A	Fuse link	As per catalog	USSR	1
22.	К5	KB 140.102	Switch	1.612.0002	Kontakta	I
23.	Сѕб	Mkcf2-62k	Conn. socket	1.254.0010	Kontakta	I
24.	CsI4/a CsI5/a	1377 33231 222 4010	15-pole PCB connector /femal	As per catalog #/	RFT	2
25.						
26.	CsI4/b CsI5/b	377 3323 04 4 0	15 -po le PCB con- nector /male par	As per catalog r/	RFT	2
27.						
28.	ТΙ	NT 3008	Transformer		FMV	
	ICI	Equivalent μ Α 7805CKC LM340 T-0.5	type Integrated cir- cuit	As per catalog	Texas /National/	
	IC2,IC3		Integrated cir-	As per catalog	Texas /National/	



DF M128 - 13 B

Rojzszám

Megnevczes Berneno fokozat Input stage Eingongsstufe Bxoghoù kohryp * Beméréskor válogatni. Подбирать при регулировке. While tuning must be selected. Bei Abstimmung ouswählen.

3

(J)

T






Input stage

Posi- tion	Circuit symbol	Drw. No. or Type No.	Description	Value	Maker	Q†y
۱.	2.	3.	4.	5.	6.	7.
۱.	RI	R510	Resistor	75 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
2.	R2,R23	R510	Resistor	I M ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	2
3.	R3,R5	R510	Resistor	100 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	2
4.	R4	R510	Resistor	0 k - 5 % 0,25 W MSZ 05 61.1004	Remix	1
5.	R6 (I)	R510	Resistor	7,5 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	1
6.	R7,R8	R510	Resistor	47 - 5 % 0,25 W MSZ 05 61.1004	Remix	2
7.	() Sel	ect at calibrat	ion. For assortm	ent refer to page 40-41		
8.	R9	R510	Resistor	2,2 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
9.	RIO (2)	R510	Resistor	5 k ⁺ 5 % 0,25 ₩ MSZ 05 61.1004	Remix	1
10.	RII	R510	Resistor	5,6 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
	R12,R32, R34	R5 10	Resistor	220 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	3
12.	RI 3 (3)	R510	Resistor	5 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	1
13.	RI4	R510	Resistor	10 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	l
14.	(2) Sele	ect at calibra	ion. For assortm	ent refer to page 41-42		
	(3) Sel	ect at calibra	ion. For assortm	ent refer to page 42.		

١.	2.	3.	4.	5.	6.	7.
15.	R15,R31	R510	Resistor	270 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	2
16.	RI6 (I)	R510	Resistor	24 - 5 % 0,25 W MSZ 05 61.1004	Remix	1
17.	RI7,R28	R510	Resistor	200 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	2
18.	RI 8 (2)	R510	Resistor	910 - 5 % 0,25 W MSZ 05 61.1004	Remix	I
19.	R19,R20, R29,R36	R510	Resistor	51 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	4
20.	R21	R510	Resistor	100 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
21.	(I) Sele	ct at calibra	tion. For assortm	ent refer to page 42.		
	(2) Sele	ct at calibra	tion. For assortm	ent refer to page 42.		
22.	R22	R510	Resistor	2,7 k - 5 % 0,25 W MSZ 05 61.1004	Remix	l
23.	R24 (3)	R510	Resistor	3 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	l
24.	R 25	R510	Resistor	300 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
25.	R26	R510	Resistor	4,7 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	l
26.	R27	R510	Resistor	2,4 k - 5 % 0,25 W MSZ 05 61.1004	Remix	l
27.	R30	R510	Resistor	24 - 5 % 0,25 W	Remix	1
28.	(3) Sele	ct at calibra	ion. For assortm	ent refer to page 40-41		
29.	R33	R510	Resistor	I,3 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	ţ

١.	2.	3.	4.	5.	6.	7.
30.	R35 (1)	R510	Resistor	4,3 k - 5 % 0,25 W MSZ 05 61.1004	Remix	
31.	R37 , R38	R510	Resistor	390 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	2
32.	R39	R510	Resistor	2 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	
33.						
34.						
35.	(I) Sele	ct at calibra	tion. For assor t	ment refer to page 40-41		
36.	PI	SzP0-05 OSZ 3	Potentiometer	2,2 k ⁺ 20 % L = 13 OZSO.468.047 TU	USSR	1
37.	P2,P3	P72 72	Potentiometer	I k ⁺ 30 % 0,5 W RX-74.297/2	Remix	2
38.						
39.	CI	MKFM T4000	Capacitor	300 n <mark>-20</mark> % 100 V -20 % 100 V 10x10x3,5 As per catalog	Kõporc	
40.	C3	KD-I M47	Capacitor	,5 p − 0,4 p 250 V GOSZT 7159-69	Kőporc	
41.	C4	C2262	Capacitor	220 p ⁺ 5 % 63 V RX-74.330	Remix	I
42.	C5,CII, C4O	CE2059-SA	Electrolite capacitor	100 % 25 V -10 % 25 V 25/085/21 MSZ 1558-70	MM	3
43.	C6,C9, C12,C14, C22,C23, C24,C25, C29,C33	ТК783	Capacitor	10 n +80 g 3E4 -20 % 32 V As per catalog	CsNK	10

1.	2.	3.	4.	5.	6.	7.
44.	C7	FSIM N47	Capacitor	100 n - 10 % 63 V 6x9x2,5 As per catalog	Kõporc	I
45.	C8	CE2162-SA	Electrolite capacitor	220µ+100 % 25 V -10 25/085/21 MSZ 1558-70	ММ	l
46.	CIO (I)	C2262	Capacitor	68 p ⁺ 5 % 63 V RX-74.330	Remix	i
47.	CI3	CE578-SA	Electrolite capacitor	470 +100 % 10 V -10 25/085/21 MSZ 1558-70	ММ	ł
48.	(I) Sele	ct at calibra	tion. For assort	ent refer to page 43.		
49.	C20, C30	TRIM Ø 5 N750/IB	Capaci tor	33 p <mark>+</mark> 10 % 160 V As per catalog	Kõporc	2
50.	C21, C32	ТК783	Capacitor	47 n +80 3E4 47 n -20 32 V As per catalog	CsNK	2
51.	C26, C31, C34	TAG 43212 16168	Electrolite capacitor	6,8µ <mark>+50</mark> % 25 V -20 % As per catalog	ITT	3
52.	C27, C28	тк724	Capacitor	I n ⁺ 20 % E2000 40 V As per catalog	CsNK	2
53.	C35, C36, C37, C38, C39	КТР 2АА N - 70	Transfer capacitor	6800 p <mark>+80</mark> % 400 v ozso.460.021 TU	USSR	5
54.						
55.						
56.						
57.	DI,D2, D3,D4	IN4151	Diode	As per catalog	EIVRT	4

١.	2.	3.	4.	5.	6.	7.
58.	D5, D6	D18	Diode	As per catalog	USSR	2
59.						
60.	ZI	ZPD 12	Zener diode	As per catalog	EIVRT	1
61.	Z2	ZPD 5,6	Zener diode	As per catalog	EIVRT	I
62.	Z3	ZPD 2,7	Zener diode	As per catalog	EIVRT	1
63.						
64.	Trl	BF2 5 6A	Transistor	As per catalog	Texas	1
65.	Tr2, Tr3	BFWI6A	Transistor	As per catalog	Valvo	2
66.	Tr4	BF244A	Transistor	As per catalog	Texas	1
67.	Tr5,Tr7	BC212	Transistor	As per catalog	EIVRT	2
68.	Тгб	BD240	Transistor	As per catalog	Texas	I
69.						
70.						
71.	ICI	SN74SO4N	Integrated circuit	As per catalog	Texas	I
72.	IC2	OM335	Integrated circuit	As per catalog	Philips	I
73.	IC3	SP8630B	Integrated circuit	As per catalog	Plessey	I
74.	IC4	SN74SOON	Integrated circuit	As per catalog	Texas	1
75.	IC5	A710PC بر	Integrated circuit	As per catalog	EIVRT	l
76.						
77.						
78.	LI, L2	DM 0,4 100	Choke coil	100 д Н GIO.477.005 TU	USSR	2

1.	2.	3.	4.	5.	6.	7.
79.						
80.	кі	S6T 11033/004	Sliding switch	3-way As per catalog	EMI- -SOUND	ł
81.	Csl, Cs2	R141 554 /UG 625 B/u/	HF socket	As per catalog	Radiall	2
82.						
83.						
84.						
		Selection	set			
85.	R6	R510	Resistor	1,5 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
86.	R24	R510	Resistor	,8 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
87.	R6	R5 I O	Resistor	2 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
88.	R24	R510	Resistor	2,2 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
89.	R6,R24, R35	R510	Resistor	2,7 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,3
90.	R35	R510	Resistor	3 k - 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
91.	R35	R510	Resistor	3,3 k [±] 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
92.	R6,R24, R35	R510	Resistor	3,6 k - 5 % 0,25 W MSZ 05 61.1004	Remix	0,3
93.	R35	R510	Resistor	3,9 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
94.	R6,R24, R35	R510	Resistor	4,7 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,3

۱.	2.	3.	4.	5.	б.	7.
		Selection	set			
95.	R24,R35	R510	Resistor	5,6 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
96.	R35	R510	Resistor	6,2 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
97.	R6,R24, R35	R510	Resistor	6,8 k - 5 % 0,25 W MSZ 05 61.1004	Remix	0,3
98.	R35	R510	Resistor	8,2 k - 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
99.	R6	R510	Resistor	IO k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
100.	R6	R510	Resistor	12 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
101.	R6	R510	Resistor	15 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
102.	RIO	R510	Resistor	7,5 k [±] 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
103.	RIO	R510	Resistor	12 k [±] 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
104.	RIO	R510	Resistor	8 k ⁺ 5 % 0,25 ₩ MSZ 05 61.1004	Remix	0,1
105.	RIO	R510	Resistor	22 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
106.	RIO	R510	Resistor	33 k - 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
107.	RIO	R510	Resistor	43 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
108.	RIO	R510	Resistor	47 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1

١.	2.	3.	4.	5.	6.	7.
		Selection	set			
109.	RIO	R510	Resistor	56 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,1
110.	RIO	R510	Resistor	12 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
111.	RI 3	R510	Resistor	13 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
112.	R13, R16	R510	Resistor	18 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,4
113.	R13, R16	R510	Resistor	20 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,4
4.	RI3, RI6	R510	Resistor	22 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,4
115.	RI3	R510	Resistor	24 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
116.	R16	R510	Resistor	27 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
117.						
118.	R18	R510	Resistor	820 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
119.	R18	R510	Resistor	k − 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
120.	RI8	R510	Resistor	I,I k − 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
121.	RI8	R510	Resistor	,2 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
122.	R18	R510	Resistor	1,3 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
123.						

۱.	2.	3.	4.	5.	6.	7.
		Selection	set			
124.						
125.						
126.	C10	C2262	Capacitor	33 p ⁺ 5 % 63 V RX−74.330	Remix	0,1
127.	C10	C2262	Capacitor	47 p ⁺ 5 % 63 V RX-74.330	Remix	0,1
128.	C10	C2262	Capacitor	56 p ⁺ 5 % 63 V RX-74.330	Remix	0,1
129.	C10	C2262	Capacitor	82 p ⁺ 5 % 63 V RX-74.330	Remix	0,1
130.	C10	C2262	Capacitor	IOO p ⁺ 5 % 63 V RX-74.330	Remix	0,1
131.	C10	C2262	Capacitor	20 p ⁺ 5 % 63 V RX-74.330	Remix	0,1
132.	C10	C2262	Capacitor	150 p ⁺ 5 % 63 V RX-74.330	Remix	0,1
133.						

١.	2.	3.	4.	5.	б.	7.
		Equivalent	type			
134.	101	MH74SO4	Integrated circuit	As per catalog	Tesla	
135.	IC4	MH74SOO	Integrated circuit	As per catalog	Tesla	
136.						
137.	Pl	SzP3-9a	Potentiometer	2,2 k - 20 % OSz3-20 OZSO.468.012 TU	USSR	
			-			





Display circuit

Posi- tion	Circuit symbol	Drw. No. or Type No.	Description	Value	Maker	Q†y
۱.	2.	3.	4.	5.	6.	7.
١.	RI (1)	R510	Resistor	15 [±] 5 % 0,25 W MSZ 05 61.1004	Remix	I
2.	P2	SzPO-05 OSz3	Potentiometer	470 k - 20 % L= 19 0ZS0.468.047 TU	USSR	I
3.						
4.	CI	CE217-SA	Electrolite capacitor	470µ+100 % 6;3 V -10 % 6;3 V 25/085/21 MSZ 1558-70	ММ	I
5.	LDI-LD4	LD52/11	LED	As per catalog	Siemens	4
6.	(1) Seł	ect at calibra	tion. For assortm	ent refer to page 48.		
7.						
8.	LD5 - LD11	TIL 306	LED /digi† disp./	As per catalog	Texas	7
9.	LDI2	TIL 308	LED /digi† disp./	As per catalog	Texas	I
10.						
11.	К4	83.547.001 83.132	Mikroswitch with push- button	As per catalog	Crouzet	I
12.						
13.	K8/a, K8/b	Pm2-111 1.203.0018	Mikroswtich	As per catalog	Kontakta	2
14.						

۱.	2.	3.	4.	5.	6.	7.
		Selection	set			
15.	RI	R510	Resistor	10 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
16.	RI	R510	Resistor	12 - 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
17.	R1	R510	Resistor	3 <mark>-</mark> 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
18.	RI	R510	Resistor	18 ⁺ 5 % 0,25 ₩ MSZ 05 61.1004	Remix	0,2
19.	RI	R510	Resistor	20 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	0,2
20.						
		Equivalent	type			
21.	P2	SzP3-9a	Potentiometer	470 k ⁺ 20 % 0Sz3-25 0ZSO.468.012 TU	USSR	
22.						
23.						
24.						
25.						
26.						
27.						
28.						
29.						
30.						
		:				



i



Control circuit I.

Posi- tion	Circuit symbol	Drw. No. or Type No.	Description	Value	Maker	Q†y
۱.	2.	3.	4.	5.	6.	7.
١.	RI, R9, RIO	R510	Resistor	2 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	3
2.	R2,RI6, R24, R25	R510	Resistor	51 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	4
3.	R3,R17	R510	Resistor	47 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	2
4.	R4,R5, R6,R7, R8	R510	Resistor	220 - 5 % 0,25 W MSZ 05 61.1004	Remix	5
5.	RII, RI2, RI3	R510	Resistor	240 - 5 % 0,25 W MSZ 05 61.1004	Remix	3
6.	RI 4, RI5, R22, R23	R510	Resistor	510 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	4
7.	RI8	R510	Resistor	15 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	
8.	R19	R510	Resistor	560 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	
9.	R20	R510	Resistor	k - 5 % 0,25 W MSZ 05 61.1004	Remix	
10.	R21	R510	Resistor	470 - 5 % 0,25 W MSZ 05 61.1004	Remix	1
11.						
12.						

١.	2.	3.	4.	5.	6.	7.
13.	CI	CE2004-SA	Electrolite capacitor	+100 % 63 V 1/4-10 % 63 V 25/085/21 MSZ 1558-70	ММ	I
14.	C2	MKFM N47	Capacitor	33 p − 5 % 50 V 5x5x2,5 As per catalog	Корогс	ţ
15.	C3-C7, C9, C12-C16	ТК783	Capacitor	10 n +80 % 3E4 -20 % 32 V As per catalog	CsNK	{
16.	C8,CIO	MKFM N47	Capacitor	220 p <mark>-</mark> 5 % 50 V 5x5x2,5 As per catalog	Kõporc	2
17.	СП	CE2082-SA	Electrolite capacitor	22 + 100 % 10 V -10 25/085/21 MSZ 1558-70	ММ	l
18.	C17	MKFM T I 000	Capacitor	33 n - 20 % 100V 7,5x7,5x2,5 As per catalog	Kőporc	1
19.						
20.						
21.						
22.	ICI, ICII, ICI6, ICI9	74 2 PC	Integrated circuit	As per catalog	EIVRT	4
23.	1C2, 1C3	7476 PC	Integrated circuit	As per catalog	EIVRT	2
24.	1C4	SN74S112N	Integrated circuit	As per catalog	Texas	1

١.	2.	3.	4.	5.	6.	7.
25.	IC5,IC6, IC7, IC13, IC14, IC21	7490 APC	Integrated circuit	As per catalog	EIVRT	6
26.	IC8, IC10, IC12, IC15, IC18, IC24	SN74SOON	Integrated circuit	As per catalog	Texas	6
27.	1C9	SN74S196N	Integrated circuit	As per catalog	Texas	1
28.						
29.	IC17	7475 PC	Integrated circuit	As per catalog	EIVRT	1
30.	IC20	7453 PC	Integrated circuit	As per catalog	EIVRT	
31.	IC22, IC23	7404 PC	Integrated circuit	As per catalog	EIVRT	2
32.						
33.	Cs7b	1377 33231 104 6110	31-pole PCB connector /male part/	As per catalog	RFT	
34.	Cs8b	377 3323 04 4 0	5-pole PCB connector /male part/	As per catalog	RFT	
35.						
36.						
		Equivalent	type			
	1C4	MH74S112	Integrated circuit	As per catalog	Tesla	

۱.	2.	3.	4.	5.	6.	7.
	2. IC8, IC10, IC12, IC15, IC18 IC24	3. MH74SOO	4. Integrated circuit	5. As per catalog	6. Tesla	7.





Control circuit II.

Posi- tion	1 1	Drw. No. or Type No.	Description	Value	Maker	Qty
١.	2.	3.	4.	5.	6.	7.
۱.	RI	R510	Resistor	100 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
1 1	R2,R4, RI1, RI7	R510	Resistor	51 <mark>-</mark> 5 % 0,25 W MSZO5 61.1004	Remix	4
3.	R3,R5, R14, R16, R18, R19, R26, R29	R510	Resistor	510 - 5 % 0,25 W MSZ 05 61.1004	Remix	8
4.	R6	R510	Resistor	560 - 5 % 0,25 W MSZ 05 61.1004	Remix	
5.	R7	R510	Resistor	5,1 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	1
6.	R8	R510	Resistor	470 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
7.	R9	R510	Resistor	7,5 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	1
8.	RIO	R510	Resistor	47 ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	
9.	RI2	R510	Resistor	3 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	
10.	RI3	R510	Resistor	30 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	
11.	RI5	R510	Resistor	240 - 5 % 0,25 W MSZ 05 61.1004	Remi×	

ι.	2.	3.	4.	5.	6.	7.
12.	R20	R510	Resistor	33 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
13.	R21	R5 I O	Resistor	9,1 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	I
14.	R22, R23, R24, R28	R510	Resistor	200 [±] 5 % 0,25 W MSZ 05 61.1004	Remix	4
15.	R25	R510	Resistor	24 k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	1
16. 17.	R27	R510	Resistor	k ⁺ 5 % 0,25 W MSZ 05 61.1004	Remix	
18.	ΡI	P7272	Potentiometer	22 k ⁺ 30 % 0,5 W RX-74.297/2	Remix	
19.						
20.	CI, C3	CE2059-SA	Electrolite capacitor	ا0µ <mark>+</mark> 100 % 25 V 10µ <u>-</u> 10 25/085/21 MSZ 1558-70	ММ	2
21.	C2	MKFM N47	Capacitor	68 p - 5 % 50 V 5x5x2,5 As per catalog	Kõporc	1
22.	C4, CII- -CI5	ТК783	Capacitor	+80	CsNK	6
23.	C5, C6	ТК783	Capacitor	100 n <mark>+ 80 g</mark> 3E4 - 20 ^g 32 V As per catalog	CsNK	2
24.	C7	CE217-SA	Electrolite capacitor	470/1-10 % 6,3 V 25/085/21 MSZ 1558-70	MM!	-

۱.	2.	3.	4.	5.	6.	7.
25.	C8	CE2082-SA	Electrolite capacitor	22/u -10 % 10 V 25/085/21 MSZ 1558-70	ММ	1
26.	С9	MKFM N47	Capacitor	560 p ⁺ 5 % 50 V 5x5x2,5	Kőporc	1
27.	C10	ТК724	Capacitor	n ⁺ 20 % 40 V E2000 As per catalog	CsNK	ł
28.						
29.						
30.	ZI	ZPD 12	Zener diode	As per catalog	EIVRT	1
31.	Trl	BC108C	Transistor	As per catalog	EIVRT	l
32.						
33.	ICI	7490 APC	Integrated circuit	As per catalog	EIVRT	. 1
34.	IC2, IC15, IC16	7404 PC	Integrated circuit	As per catalog	EIVRT	3
35.	C3, C5, C9, C , C 4	74121 PC	Integrated circuit	As per catalog	EIVRT	5
36.	IC4	7476 PC	Integrated circuit	As per catalog	EIVRT	
37.	IC6, IC7, IC8	7453 PC	Integrated circuit	As per catalog	EIVRT	3
38.	ICIO, C 2, C 3; C 7	7400 PC	Integrated circuit	As per catalog	EIVRT	4

١.	2.	3.	4.	5.	6.	7.
39.	QI	TCXO A31	Quartz crystal /crystal os- cillator/	IO MHz As per catalog	Kristall	1
40.						
41.	Cs9b	377 3323 04 6 0	31-pole PCB connector /male part/	As per catalog	RFT	I
42.	CsIOb	377 3323 04 4 0	5-pole PCB connector /male part/	As per catalog	RFT	



20dB-es oszłó

DFM126-14 B

20 dB attenuator

Posi- tion	Circuit symbol	Drw. No. or Type No.	Description	Value	Maker	Q†y
۱.	RI, R3	R510 I.	Resistor	62 <mark>-</mark> 5 % I W	Remix	2
2.	R2	R510 I.	Resistor	240 <mark>-</mark> 5 % I W	Remix	I
3.						
4.	ні	RI4I 554 /UG 625 B/u/	HF socket		Radiall	1
5.	DI	RI41 008 /UG 88 C/u/	HF plug		Radiall	
6.						
7.						
8.						
9.						
10.						
12.			х. Х			
13.						
14.						
15.						
16.						
17.						
18.						
19.						
20.						