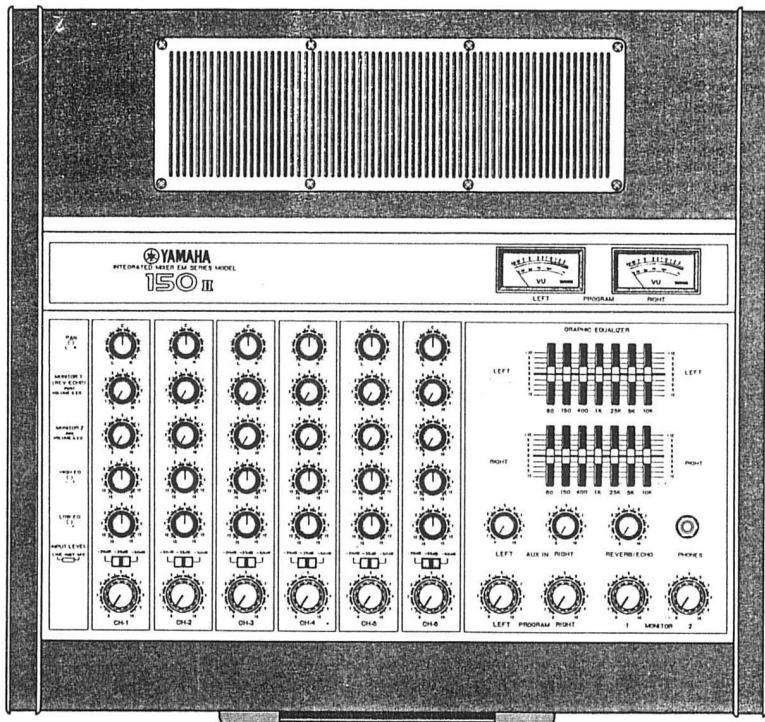


EM-150 II

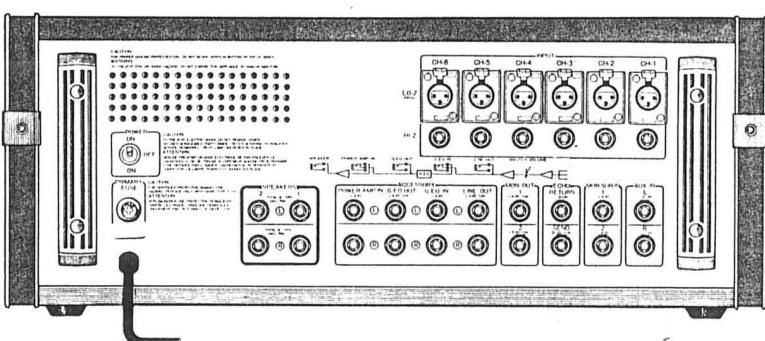
SERVICE MANUAL

■ FRONT PANEL



■ REAR PANEL

U.S. Model



■ CONTENTS

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006407

 YAMAHA
2 K.  Printed in Japan. 5.79

OVERALL SPECIFICATIONS

Channel controls (CH1 ~ CH6)	PAN POT MONITOR 1 (REV/ECHO) MONITOR 2 HIGH-EQ LOW-EQ INPUT LEVEL switch (-20/-35/-50dB) Channel volume	(Monitor out) Maximum output levels Frequency response Total harmonic distortion Hum and noise level	+18dB (10kΩ, 1kHz, T.H.D. 1%) 20Hz ~ 30kHz, +4dB, -3dB Less than 0.2% (1kHz, +10dB, 10kΩ) Less than 0.5% (20Hz ~ 15kHz, +10dB, 10kΩ)
Master controls	AUX IN controls (L, R) REVERB/ECHO controls MONITOR master volume (1, 2) PROGRAM master volume (L, R) GRAPHIC EQUALIZER (L, R)	Hum and noise level	-118dB (Equivalent Input Noise) -71dB (MASTER VOLUME → Max.) (ALL INPUT MONITOR VOLUME → Min.) -64dB (MASTER VOLUME → Max.) (ONE INPUT MONITOR VOLUME → Max.)
Others	VU meters	Maximum gain	• 80dB (CH IN → SPEAKER OUT) • 54dB (CH IN → MON OUT) • 30dB (CH IN → ECHO SEND) • 54dB (CH IN → LINE OUT) • 54dB (CH IN → G-EQ OUT) • 50dB (AUX IN → SPEAKER OUT) • 60dB (ECHO RETURN → SPEAKER OUT)
(Speaker out)		Equalizer	• ±15dB (100Hz) • ±15dB (10kHz) • ±12dB (60/150/400/1k/2.5k/5k/10k)Hz
Power out	100W per channel (4 Ω, 1kHz, T.H.D. 0.5%) 75W per channel (8 Ω, 1kHz, T.H.D. 0.5%) 65W per channel (8 Ω, 20Hz ~ 20kHz, T.H.D. 0.5%)	Channel separation	Less than -55dB, 1kHz
Frequency response	20Hz ~ 20kHz, 0 ± 1dB (35W, 8 Ω)	Power supply	U.S. MODEL AC120V, 170W, 50/60Hz CANADIAN MODEL AC120V, 2A, 50/60Hz GENERAL MODELS AC220/240V, 500W, 50/60Hz
Total harmonic distortion	Less than 0.2% (1kHz, 65W, 8 Ω) Less than 0.5% (20Hz ~ 20kHz, 65W, 8 Ω)	Dimensions (W x D x H)	560(W) x 508(D) x 214(H)mm (22 x 20 x 8-1/2")
Intermodulation distortion	Less than 0.5% (70Hz : 7kHz = 4 : 1, 35W, 8 Ω)	Weight	24.5kg (54 lbs)
Hum and noise level	• -118dB (Equivalent Input Noise) • -44dB (MASTER VOLUME → Max.) (ALL INPUT VOLUME → Min.) • -38dB (MASTER VOLUME → Max.) (ONE INPUT VOLUME → Max.)		

Nominal Value
Specifications subject to change without notice.

INPUT/OUTPUT SPECIFICATIONS

- Input jacks

Connection	Actual Load Impedance	For Use With Nominal	Sensitivity (at max. gain)	Input level		Connector
				Nominal	Max. before Clip	
'INPUTS (1 ~ 6) -50 -35 -20	HI-Z 20kΩ LO-Z 1kΩ	HI-Z 3kΩ ~ 10kΩ LO-Z 150Ω ~ 600Ω	-50dB* (2.5mV) -35dB (14mV) -20dB (78mV)	-50dB (2.5mV) -35dB (14mV) -20dB (78mV)	-22dB (62mV) - 7dB (346mV) + 8dB (1.95V)	XLR-3-31 and Phone Jack
AUX IN (L, R)	30kΩ	5kΩ	-20dB (78mV)	-20dB (78mV)		Phone Jack
ECHO RETURN	30kΩ	5kΩ	-30dB (25mV)	-30dB (25mV)		Phone Jack
GEO IN (L, R)	100kΩ	5kΩ	+ 4dB (1.23V)	+ 4dB (1.23V)	+ 18dB (6.2V)	Phone Jack
POWER AMP IN (L, R)	30kΩ	5kΩ	+ 4dB (1.23V)	+ 4dB (1.23V)		Phone Jack
MON. SUB IN (1, 2)	30kΩ	5kΩ	+ 4dB (1.23V)	+ 4dB (1.23V)	+ 24dB (12.3V)	Phone Jack

- Output jacks

Connection	Actual Source Impedance	For Use With Nominal	Output level (Power)		Connector
			Nominal	Max. before Clip	
SPEAKER OUT (L, R)	0.065 Ω	8Ω (4Ω)	75W (100W)		Phone Jack
LINE OUT (L, R)	390 Ω	10kΩ 600 Ω	+ 4dB (1.23V) 0dB (775mV)	+ 18dB (6.2V) + 14dB (3.9V)	Phone Jack
GEO OUT (L, R)	390 Ω	10kΩ 600 Ω	+ 4dB (1.23V) 0dB (775mV)	+ 18dB (6.2V) + 14dB (3.9V)	Phone Jack
MON OUT (1, 2)	390 Ω	10kΩ 600 Ω	+ 4dB (1.23V) 0dB (775mV)	+ 18dB (6.2V) + 14dB (3.9V)	Phone Jack
ECHO SEND	220Ω	10kΩ	-20dB (78mV)	- 6dB (0.39V)	Phone Jack
PHONES	130 Ω	8 Ω	+ 2dB (0.98V)		Phone Jack (Stereo)

Note: All the inputs and outputs are unbalanced.

■ GENERAL ADJUSTMENT AND CHECK SPECIFICATIONS

- Use an oscilloscope and AC voltmeter/dB meter with an input impedance of over $500k\Omega$ for measurement.
- To measure the noise level, use a AC voltmeter/ dB meter with a bandwidth of 20Hz to 20kHz or a Low Pass Filter of 6dB/oct with fc of 12.47kHz.
- Except those items that require measurement at every channel such as gain and equalizer variation response, perform measurement at CH-1 as a representative of all channels in such cases as distortion and maximum output power.

- Perform measurement at the phone jack, after making sure that there is no level difference when the inputs are applied to the connector (XLR) and the phone jack (P.J.) of each channel.
 - Connect a 4Ω load resistance to the SPEAKERS jacks and a $10k\Omega$ load resistance to the other output jacks. In the latter case, connection of the load resistance is necessary only for measurement.
- * Input impedance of XLR: $1k\Omega$
Input impedance of P.J.: $25k\Omega$

I. GENERAL ADJUSTMENT

• Idling Current Adjustment (MA circuit board)

Adjust the variable resistor ($B470\Omega$) so that the voltage across the test points RE(-) and CT(+) on MA circuit board is set to $DC23\pm2mV$ under no-signal condition.

* Perform this adjustment within 30 seconds after setting the POWER switch to ON.

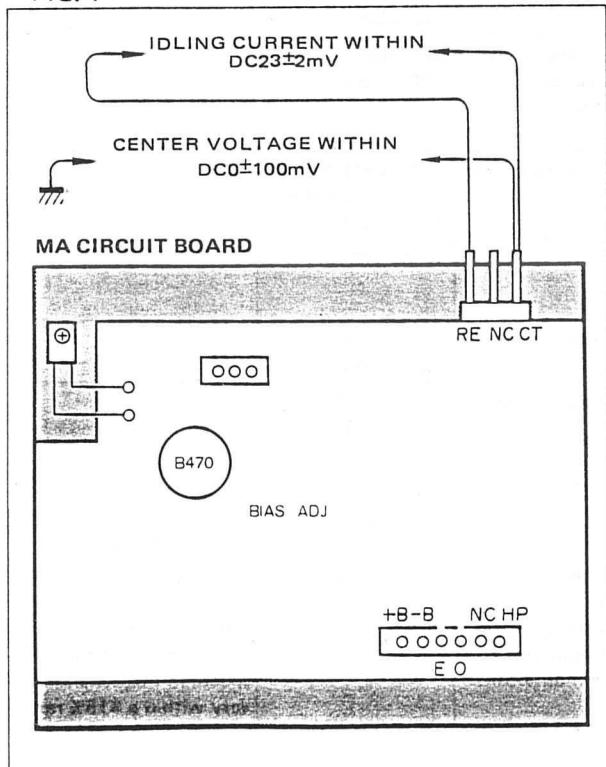
* The acceptable variation in the idling current after adjustment is $23\pm10mV$ across the 15°C to 45°C temperature range of the radiator.

* When readjusting all over again, make sure to turn the variable resistor counterclockwise.

• Center Voltage (MA circuit board)

Check that the voltage across the test point CT and the earth on MA circuit board in Fig. 1 is within $DC0\pm100mV$ under no-signal condition.

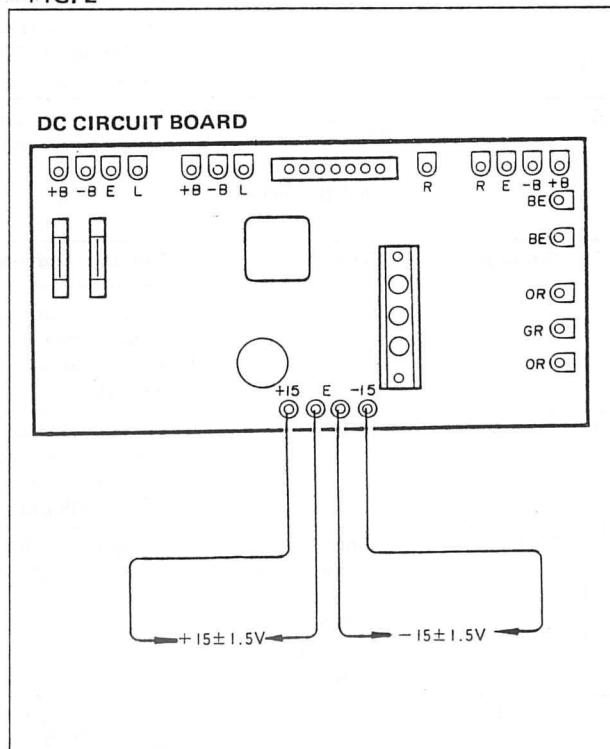
• FIG. 1



• Power Supply Voltage Check (DC circuit board)

Check that at the terminals +15 and -15 on DC circuit board the specified voltages are obtained. (Fig. 2)

• FIG. 2



• Relay Circuit (DC circuit board)

Check that the relay turns ON 3 ± 2 seconds after the POWER switch is set to ON and turns OFF within a second after the POWER switch is set to OFF.

II. CHECK SPECIFICATION

	Check item	Set position of control	Measurement conditions	Specifications	Remarks
1	Gain	Table 2	Apply a -55dBm 1kHz sine wave signal to each of the INPUT jacks.	Output jack: output level listed in Table 3.	The difference in level between the channels for all the outputs must be within 2dB.
2	Distortion	Table 2	Apply a 1kHz sine wave signal to the INPUT jacks and set the output level of the SPEAKERS jack to $+25\text{dBm}$ (4.7W).	T.H.D. less than 0.2%	
3	Frequency response	Table 2	Apply 20Hz, 1kHz and 20kHz/ -55dBm sine wave signals to the INPUT jack.	Frequency response: Within $-1 \pm 2\text{dB}$ with 1kHz as a standard, within $\pm 2\text{dB}$ of the basic curve of FLAT given in Fig. 3.	Refer to FLAT in Fig. 3.
4	Equalizer response	Table 2	Apply 100Hz and 10kHz/ -70dBm sine wave signals to the INPUT jacks and measure the output levels when the LO-EQ and HI-EQ controls are set to the maximum and minimum positions.	Variation range: $\pm 14(\pm 2)\text{dB}$ with FLAT as a standard	Refer to Fig. 3.
5	Maximum output power	Table 2 Make either the L or R PAN the measurement channel.	Apply a 1kHz sine wave signal to the CH-1 INPUT jack.	SPEAKERS jack: 28.2dBm (100W/ 4Ω) with the T.H.D. less than 0.5%	
6	Separation	Table 2	Turn the measurement channel PAN control to the R channel side and adjust the input signal level so that the output of the R channel is $+25\text{dBm}$. Perform the same adjustment on the L channel.	L channel output: -30dBm (separation 55dB) R channel output: -30dBm (separation 55dB)	
7	AUX IN (L,R)	Table 2	Apply a -30dBm 1kHz sine wave signal to either L or R of AUX IN jacks and measure the output level at the SPEAKERS jacks.	SPEAKERS JACK: $+20 \pm 2\text{dBm}$	
8	MONITOR 1, 2	Table 2 Set MONITOR 1 and MONITOR 2 controls to their maximum positions.	Apply a -55dBm 1kHz sine wave signal to the INPUT jack.	MON OUT 1, 2 jacks: $-1 \pm 2\text{dBm}$ ECHO SEND jack: $-25 \pm 3\text{dBm}$	
9	MON SUB IN	Table 2	Apply a -6dBm 1kHz sine wave signal to MON SUB IN 1 and 2 jacks.	MON OUT 1, 2 jacks: $-6 \pm 2\text{dBm}$	
10	REVERB/ECHO	Table 2 Set the REVERB/ECHO control to its maximum position.	Apply a -40dBm 1kHz sine wave signal to the ECHO RETURN jack.	SPEAKERS jack: $+20 \pm 2\text{dBm}$	
11	GRAPHIC EQUALIZER	Table 2	Apply a -70dBm sine wave signal to the INPUT jack and check that the specified frequencies display a variation within the specified variation range when each of the band controls of the graphic equalizer is set to the maximum and minimum positions.	Variation range: $\pm 12(\pm 1.5)\text{dB}$	Refer to Fig. 4. Specified frequencies: 60Hz, 150Hz, 400Hz, 1kHz, 2.5kHz, 5kHz, 10kHz (Each of the specified frequencies may vary within a $\pm 15\%$ range)

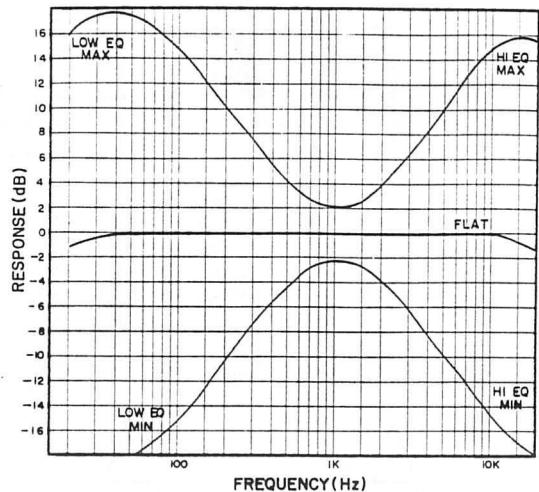
12	Noise level	Table 2	Short the INPUT jack with a 150Ω resistance and measure the noise level at the SPEAKERS jacks. Also measure the residual noise level when the MASTER VOLUME control is set to its minimum position.	Noise level: less than -37dBm Residual noise level: less than -62dBm	
13	PHONES	Table 2	Apply a -55dBm 1kHz sine wave signal to the INPUT jack and measure the output at the PHONES jack when a $+25\text{dBm}$ output is obtained at the SPEAKERS jacks.	PHONES jack: $-3 \pm 2\text{dBm}$	Connect the load resistance given in Fig. 5
14	dBm meter	Table 2	Check the dBm meter when a 27dBm ($75\text{W}/4\Omega$) output is obtained at the SPEAKERS jacks.	dBm meter $0 \pm 1\text{VU}$	

Table 1

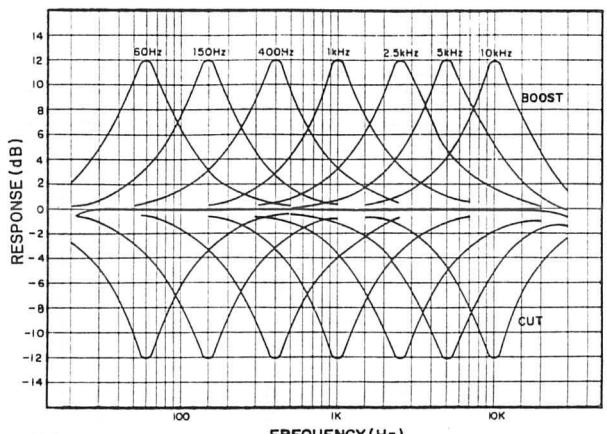
• TABLE 2 MEASUREMENT CONDITIONS

Switch, control	Set position
Channel Volume	Max for measurement channel only, all others to minimum position
EQ (Hi, Low)	Center
MONITOR 1, 2	Max for measurement channel only, all others to minimum position
INPUT LEVEL switch	-50 (no conditions)
PAN POT	Center
AUX IN (L,R)	Max for measurement only otherwise minimum
REVERB/ECHO	Max for measurement only, otherwise minimum
GRAPHIC EQUALIZER (L,R)	Center (Max or min for measurement only)
PROGRAM master Volume (L,R)	Max
Monitor master Volume	Max

• FIG. 3 HI-EQ LO-EQ VARIATION



• FIG. 4 G-EQ VARIATION

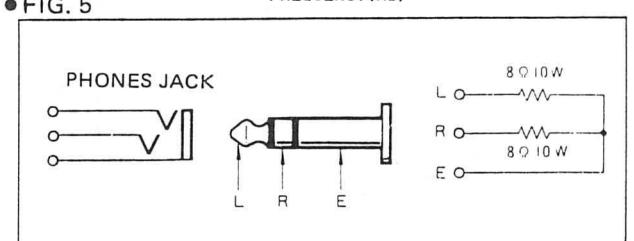


• TABLE 3 OUTPUT LEVEL (CH1 ~ CH6)

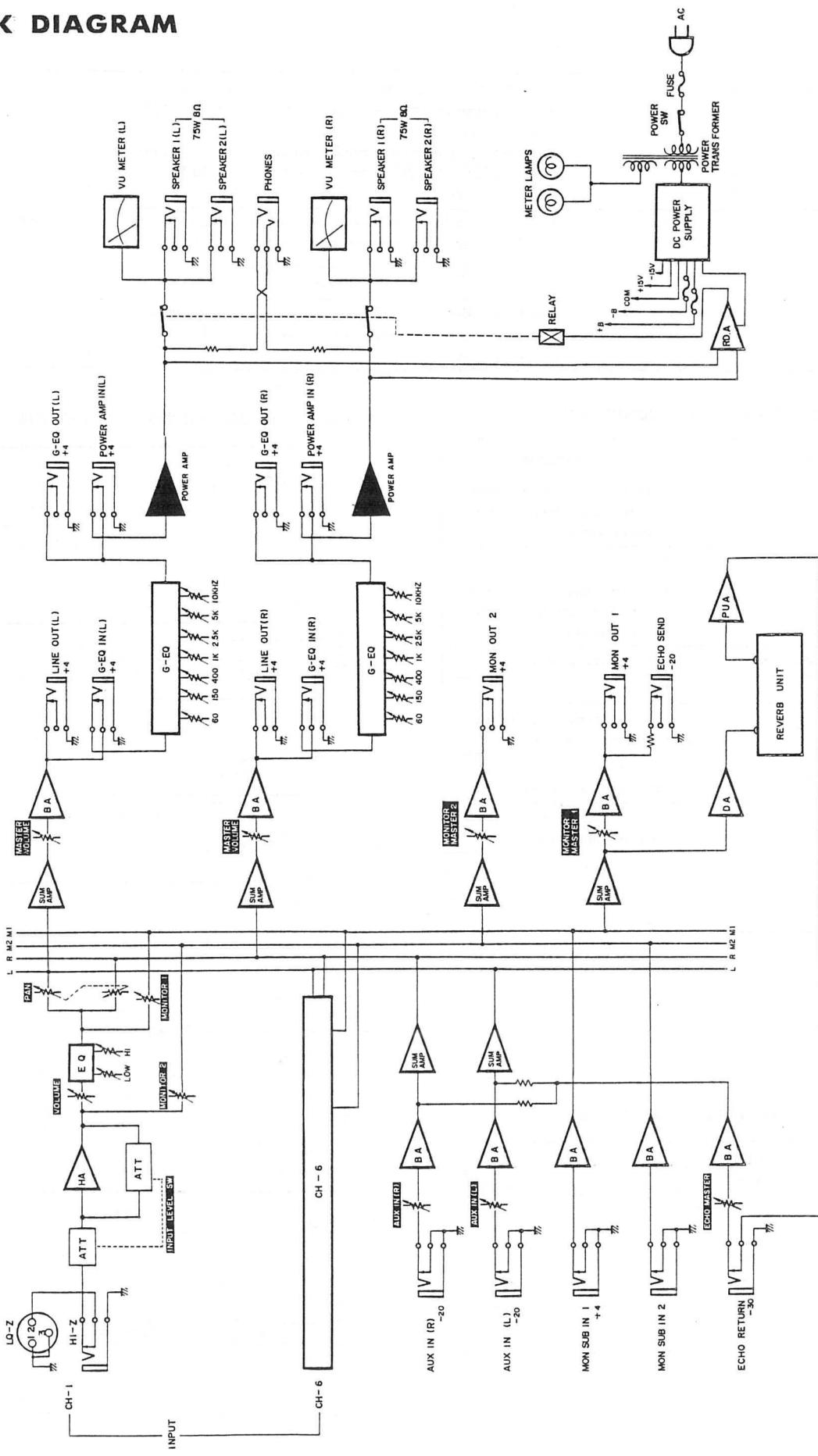
	INPUT LEVEL	LINE OUT	G-EQ OUT	SPEAKER OUT
CH INPUT CH1 ~ 6	-50	-1	-1	+25
	-35	-16	-16	+10
	-20	-31	-31	-5

(Unit : dBm)

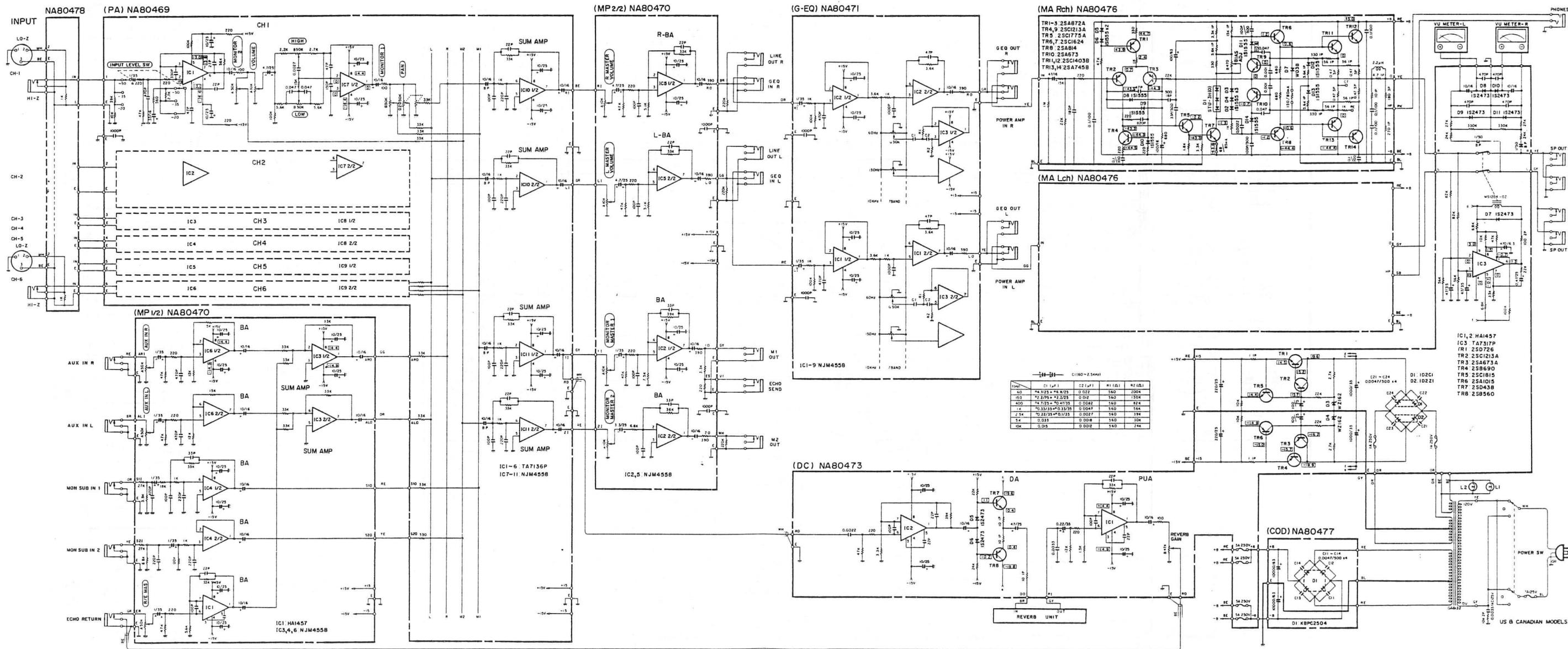
* The difference in level between the channels for all the outputs must be within 2dB.



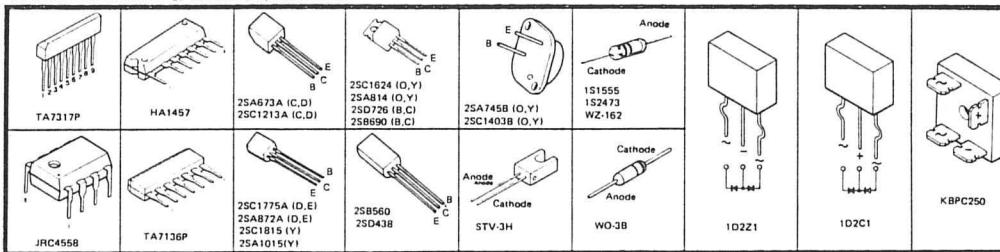
■ BLOCK DIAGRAM



■ SCHEMATIC DIAGRAM



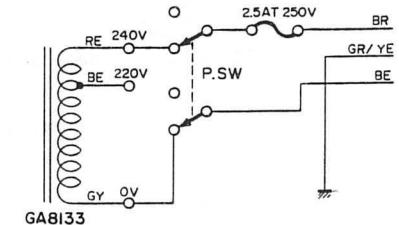
PIN-CONNECTION DIAGRAM OF TRANSISTORS, DIODS AND IC.



• WIRE COLOR ABBREVIATIONS

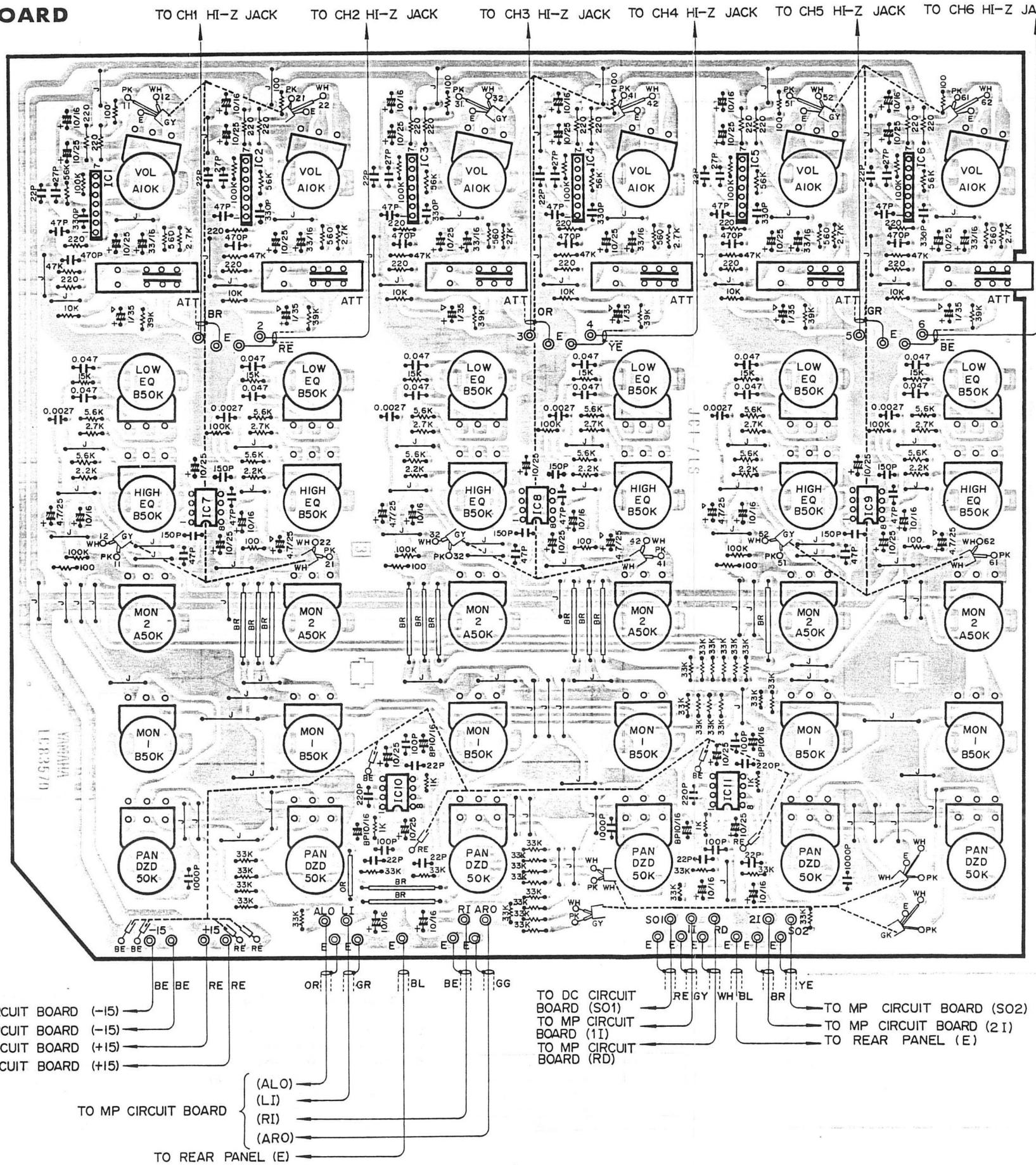
BL	► Black	V1	► Violet
BR	► Brown	GY	► Gray
RE	► Red	WH	► White
OR	► Orange	GG	► Grass Green
YE	► Yellow	SB	► Sky Blue
GR	► Green	PK	► Pink
BE	► Blue		

GENERAL MODEL

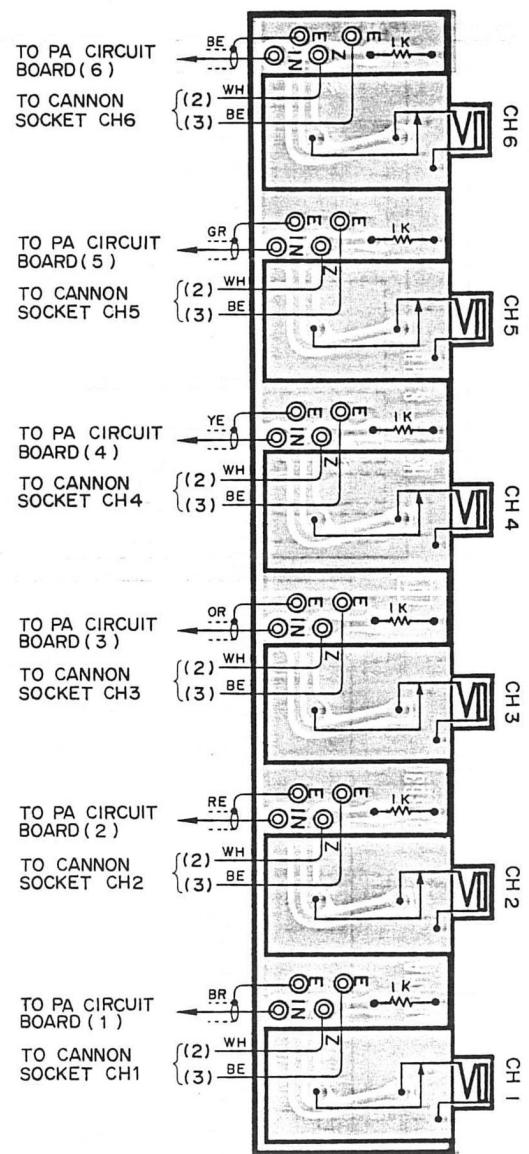


PRINTED CIRCUIT BOARD

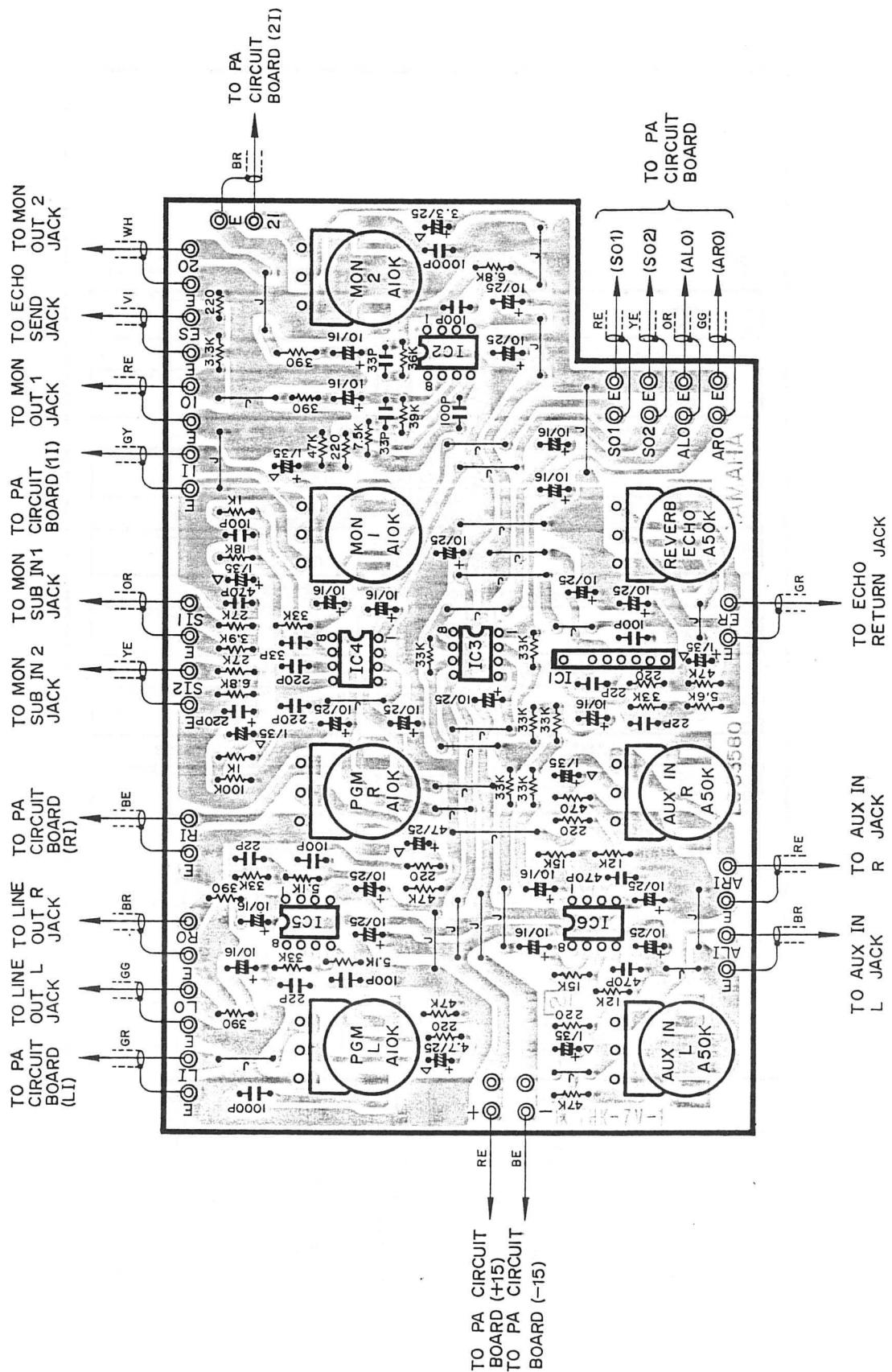
- PA CIRCUIT BOARD NA80469
(Pattern Side)



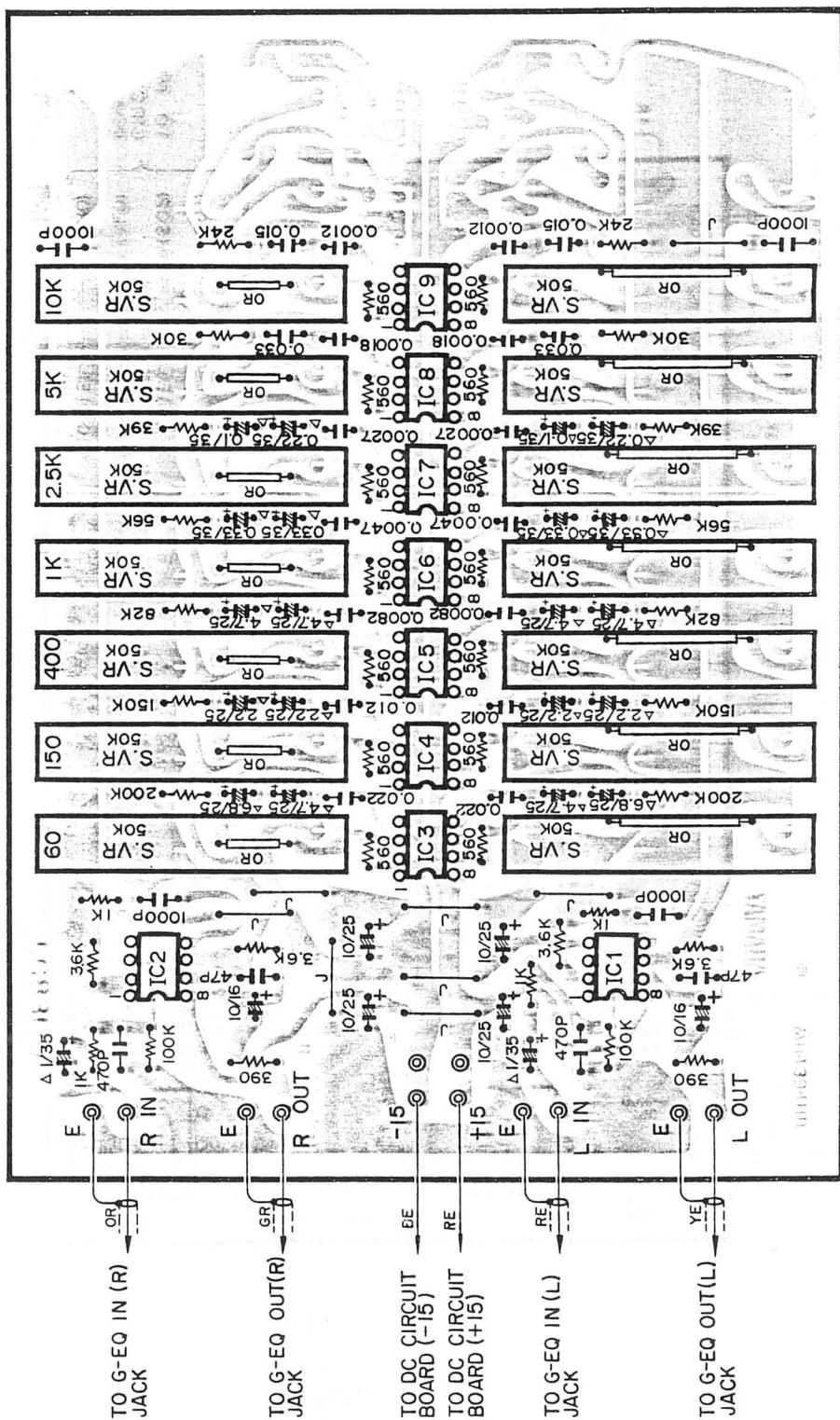
- IN CIRCUIT BOARD NA80478 (Pattern Side)



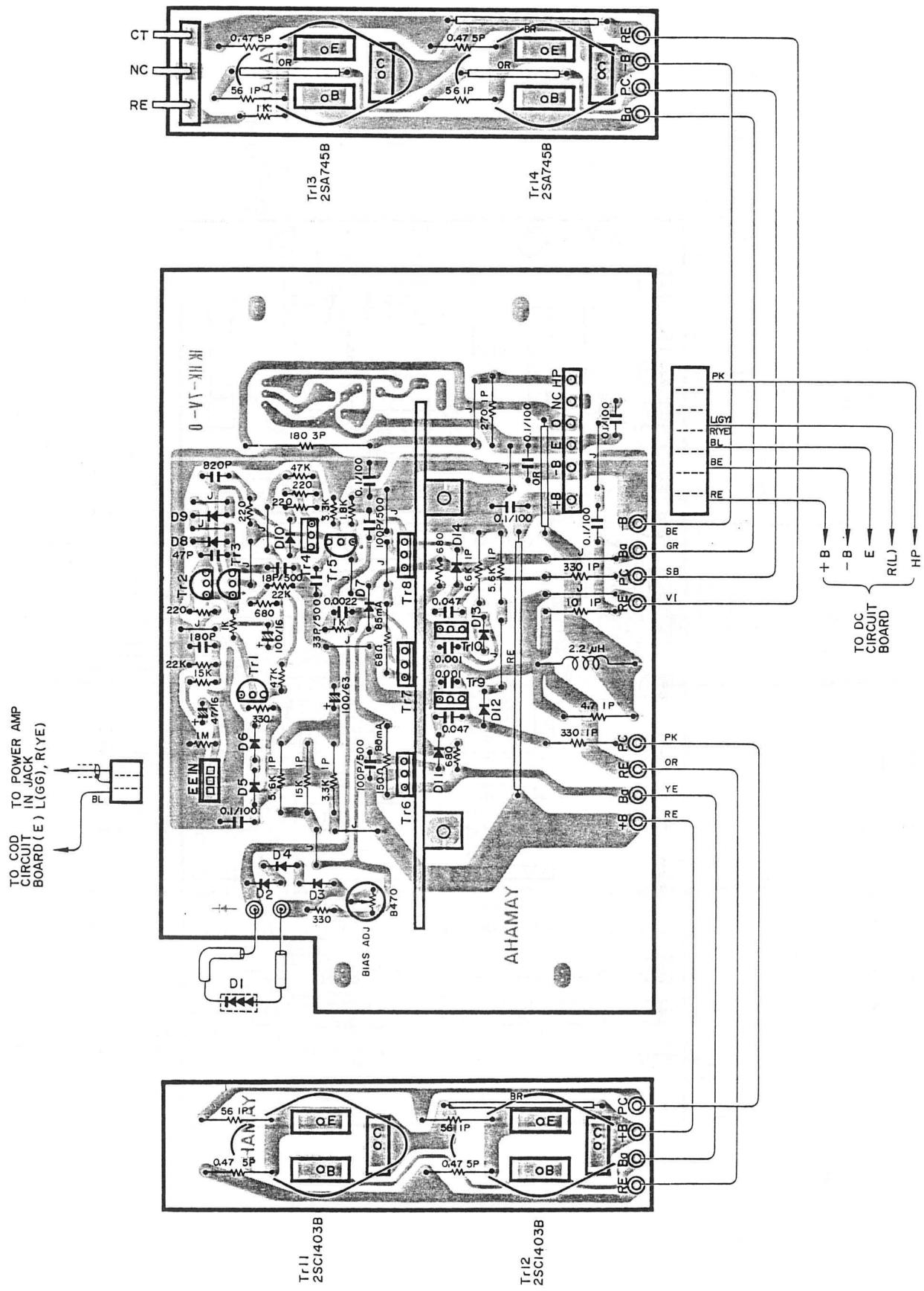
● MP CIRCUIT BOARD NA80470 (Pattern Side)



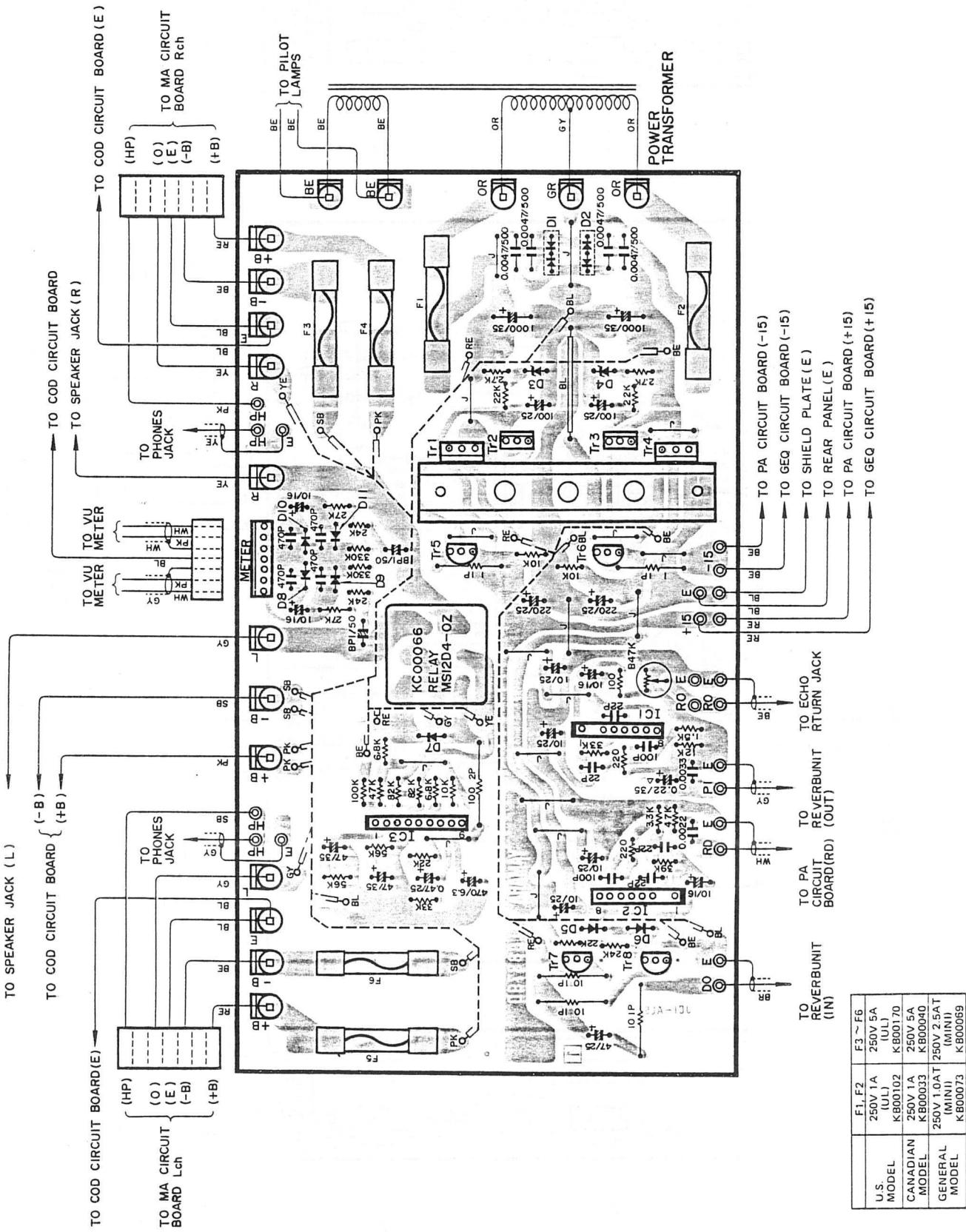
● G-EQ CIRCUIT BOARD NA80471 (Pattern Side)



● MA CIRCUIT BOARD NA80475 (Parts Side)

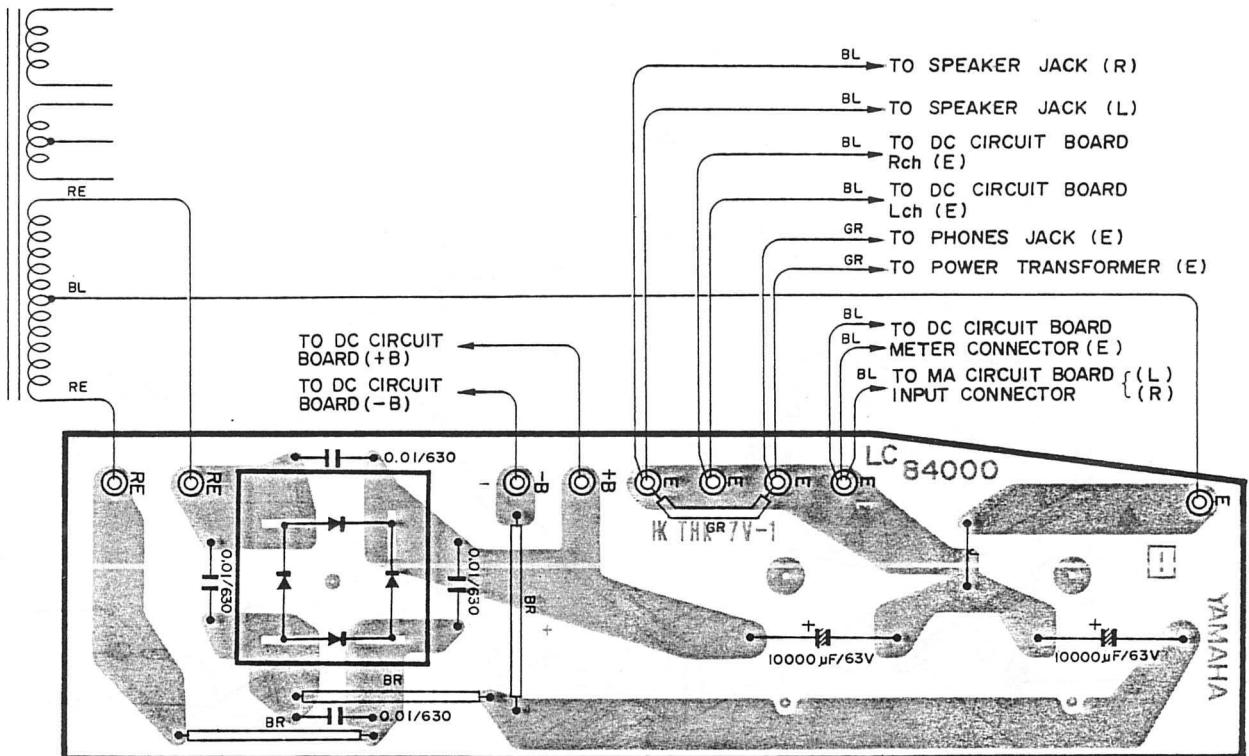


● DC CIRCUIT BOARD NA80472 (Parts Side)

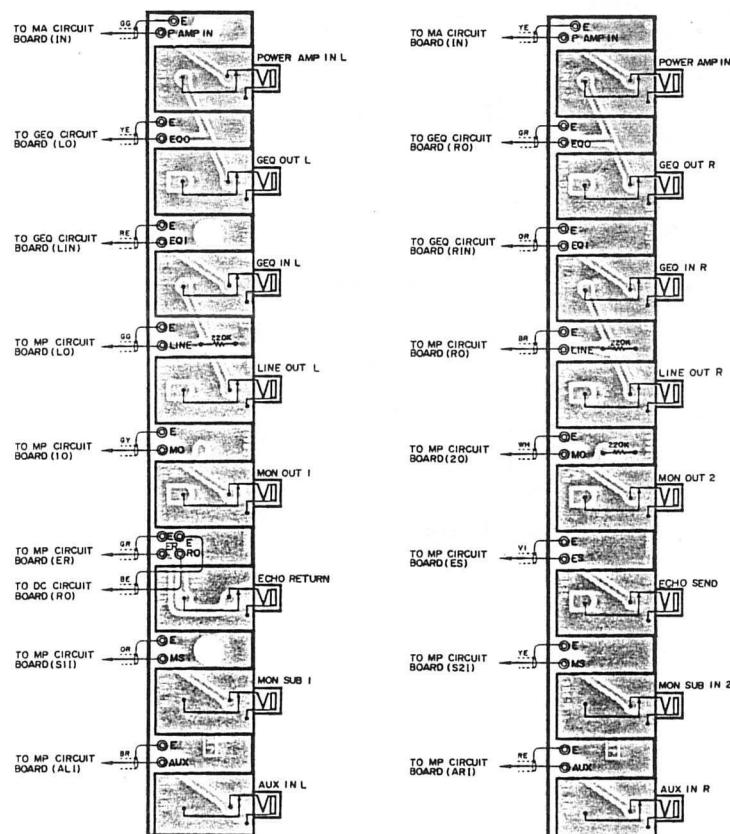


● COD CIRCUIT BOARD NA80477 (Pattern Side)

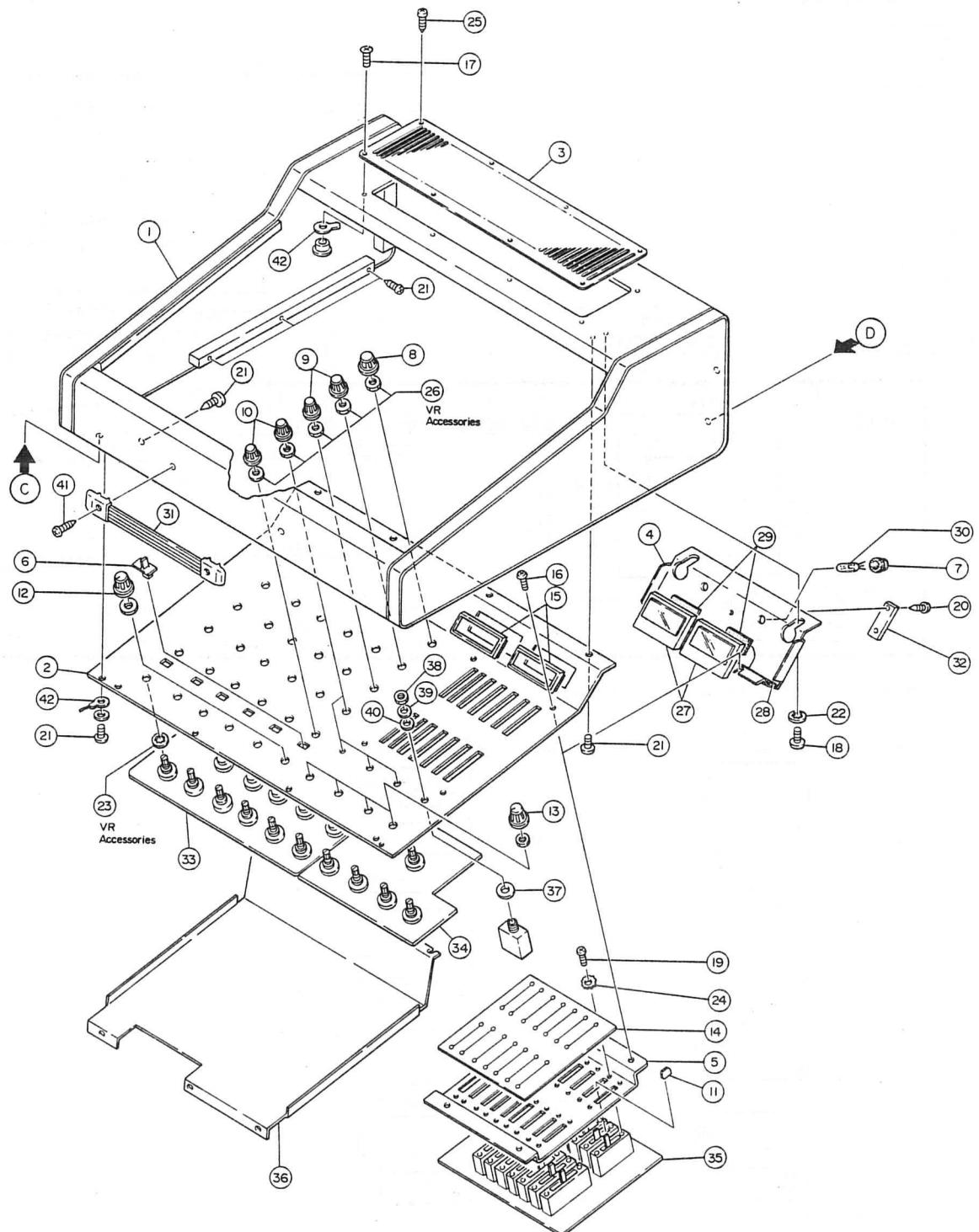
POWER TRANSFORMER



● JK CIRCUIT BOARD NA80534 (Pattern Side)



PARTS LIST

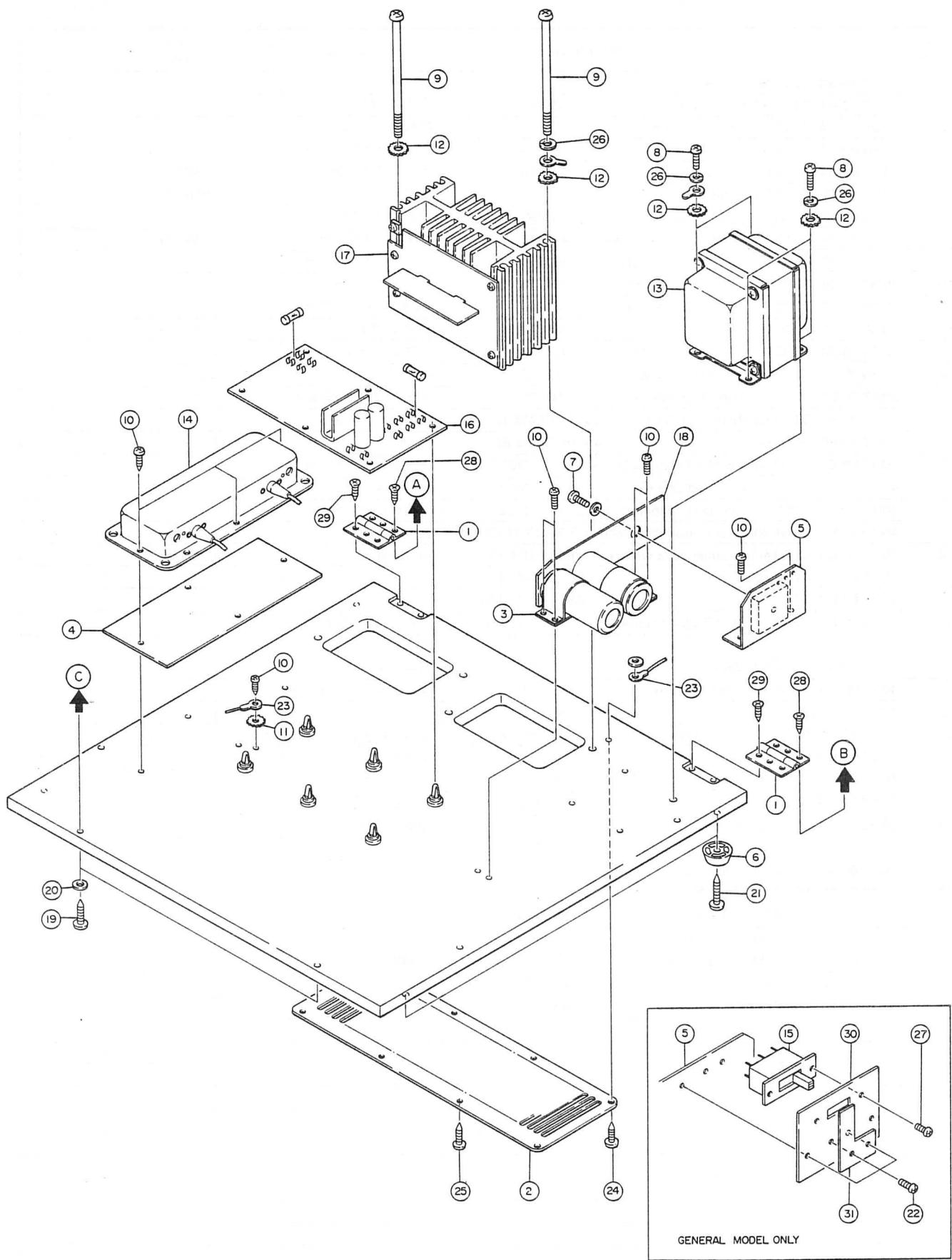


DX802610

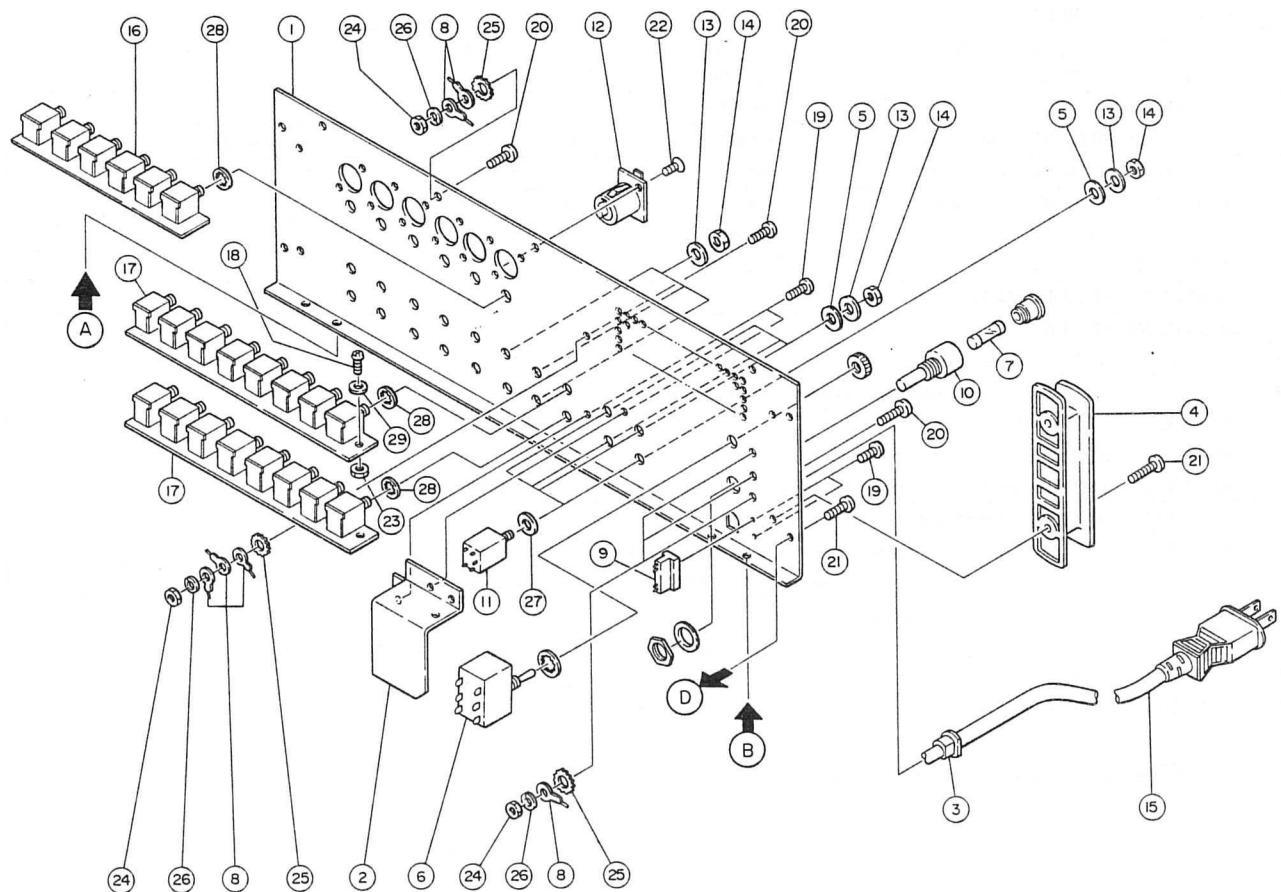
DESTINATION ABBREVIATIONS

G : General C : Canadian
U : US J : Japan

※ NEW PARTS



GENERAL MODEL ONLY



* NEW PARTS

ELECTRIC PARTS

Ref No.	Part No.		Description		(部品名)	Remarks	Common model	
*	30:56:00:NA	80:47:70	COD Board		CODシート			
	40:10:00:FZ	00:15:10	Electrolytic Cap.	10,000μF/63V	ケミコン		EM200	
V	40:10:00:iH	00:03:90	Bridge Rectifier	KBPC250	整流ブリッジ			
	40:10:00:FZ	00:06:50	Mylar Capacitor	0.01μF/630V	マイラーコン			
*	30:56:00:NA	80:46:90	PA Board		PAシート			
	40:10:00:FM	09:71:00	BP Cap.	10μF/16V	BPケミコン			
	40:10:00:FP	34:64:70	Tantalum Cap.	4.7μF/25V	タンタルコン			
	40:10:00:FP	35:61:00	"	1μF/35V	"			
	40:10:00:iG	00:12:20	IC	TA7136P	I C		EM-200	
	40:10:00:iG	00:13:90	"	JRC4558	"			
	40:10:00:HS	31:05:50	Variable Resistor	A10KΩ	ボリューム			
	40:10:00:HS	31:04:30	"	A50KΩ	"		EM-200	
	40:10:00:HS	31:04:40	"	B50KΩ	"		EM-200	
	40:10:00:HS	31:04:50	"	DZD50KΩ	"			
	40:10:00:KA	40:06:30	Slide Switch	#40063	スライドスイッチ		EM85	
*	30:56:00:NA	80:47:00	MP Board		MPシート			
	40:10:00:FP	35:61:00	Tantalum Cap.	1μF/35V	タンタルコン			
	40:10:00:FP	34:63:30	"	3.3μF/25V	"			
	40:10:00:FP	34:64:70	"	4.7μF/25V	"			
	40:10:00:iG	02:62:00	IC	HA1457	I C		EM-200	
	40:10:00:iG	00:13:90	"	JRC4558	"			
	40:10:00:HS	31:05:50	Variable Resistor	A10KΩ	ボリューム			
	40:10:00:HS	31:04:30	"	A50KΩ	"		EM-200	
*	30:56:00:NA	80:47:10	GEQ Board		G EQシート			
	40:10:00:FP	35:51:00	Tantalum Cap.	0.1μF/35V	タンタルコン			
	40:10:00:FP	35:52:20	"	0.22μF/35V	"			
	40:10:00:FP	35:53:30	"	0.33μF/35V	"			
	40:10:00:FP	35:54:70	"	0.47μF/35V	"			
	40:10:00:FP	35:61:00	"	1μF/35V	"			
	40:10:00:FP	34:62:20	"	2.2μF/35V	"			
	40:10:00:FP	34:64:70	"	4.7μF/25V	"			
	40:10:00:FP	34:66:80	"	6.8μF/25V	"			
	40:10:00:iG	00:13:90	IC	JRC4558	I C			
	40:10:00:HQ	30:03:50	Slide Variable Resistor	G50KΩ	スライドボリューム		EM-200	
*	30:56:00:NA	80:47:80	IN Board		INシート			
	40:10:00:LB	10:05:00	JACK	#10050	ジャック			
*	30:56:00:NA	80:53:40	JK Board		JKシート			
	40:10:00:LB	10:05:00	JACK	#10050	ジャック			

* NEW PARTS

Ref No.	Part No.		Description	(部品名)	Remarks	Common model	
*	30:56:00:NA	80:47:50	MA Board	MA シート	J,C,G		
*	30:56:00:NA	80:47:60	"	"	U		
*	30:54:00:BA	80:16:40	Heat Sink #80164	放熱板			
V	40:10:00:HL	31:34:70	Metal Oxide Film Resistor 4.7Ω 1P	酸金抵抗			
V	40:10:00:HL	31:31:00	" 10Ω 1P	"			
V	40:10:00:HL	31:45:60	" 56Ω 1P	"			
V	40:10:00:HL	31:52:70	" 270Ω 1P	"			
V	40:10:00:HL	31:53:30	" 330Ω 1P	"			
V	40:10:00:HL	31:63:30	" 3.3KΩ 1P	"			
V	40:10:00:HL	31:65:60	" 5.6KΩ 1P	"			
V	40:10:00:HL	31:71:50	" 15KΩ 1P	"			
V	40:10:00:HL	33:51:80	" 180Ω 3P	"			
V	40:10:00:HM	75:24:70	Cement Molded Resistor 0.47Ω 5P	セメント抵抗			
V	40:10:00:HW	10:46:80	Fuse Resistor 85mA 68Ω	ヒューズ抵抗	J,C,G		
V	40:10:00:HW	20:46:80	" 85mA 68Ω	"	U		
V	40:10:00:HW	11:51:50	" 80mA 150Ω	"	J,C,G		
V	40:10:00:HW	21:51:50	" 80mA 150Ω	"	U		
V	40:10:00:HW	79:52:20	" 1/4W 220Ω	"			
V	40:10:00:IF	00:04:50	Varistor STV-3H (O)	バリスタ			
V	40:10:00:IF	00:00:40	Diode 1S1555	ダイオード			
V	40:10:00:IH	00:07:20	" WO3B	"	EM85		
V	40:10:00:GD	90:02:10	Coil 2.2μH	コイル	EM-200		
V	40:10:00:IA	06:73:10	Transistor 2SA673A (C,D)	トランジスタ			
V	40:10:00:IA	08:72:10	" 2SA872A (D,E)	"			
V	40:10:00:IC	12:13:10	" 2SC1213A (C,D)	"			
V	40:10:00:IC	17:75:10	" 2SC1775A (D,E)	"			
V	40:10:00:IA	08:14:20	" 2SA814 (O,Y)	"			
V	40:10:00:IC	16:24:20	" 2SC1624 (O,Y)	"			
V	40:10:00:HT	41:01:20	Variable Resistor B470Ω	ソリッドボリューム			
V	40:10:00:FC	02:51:00	Mylar Capacitor 0.1μF/100V	マイラーコン			
 The following parts are not included in MA Board. 次の部品はMA シートには含まれません。							
*	30:56:00:BA	80:37:00	Heat Sink #80370	放熱器			
*	40:10:00:IA	07:45:30	Transistor 2SA745B (O,Y)	トランジスタ			
*	40:10:00:IC	14:03:30	" 2SC1403B (O,Y)	"			
*	40:10:00:IL	00:04:40	Mica Base	マイカベース			
*	40:10:00:LB	60:26:70	Socket	トランジスタソケット			
*	40:10:00:EA	23:01:60	Pan Head Screw M3 x 16	ナベ小ネジ			
*	40:10:00:EV	30:10:30	Spring Lock Washer 3S	バネ座金			
*	40:10:00:Ei	03:50:60	Bind Head Tapping Screw 3.5 x 6 ZMC2-Y	バインドタッピングネジ			
*	40:10:00:Ei	03:50:80	" 3.5 x 8 ZMC2-Y	"			
*	30:56:00:NB	81:29:40	MA Unit	MA ユニット	J,C,G		
*	30:56:00:NB	81:29:50	"	"	U		
* NEW PARTS							