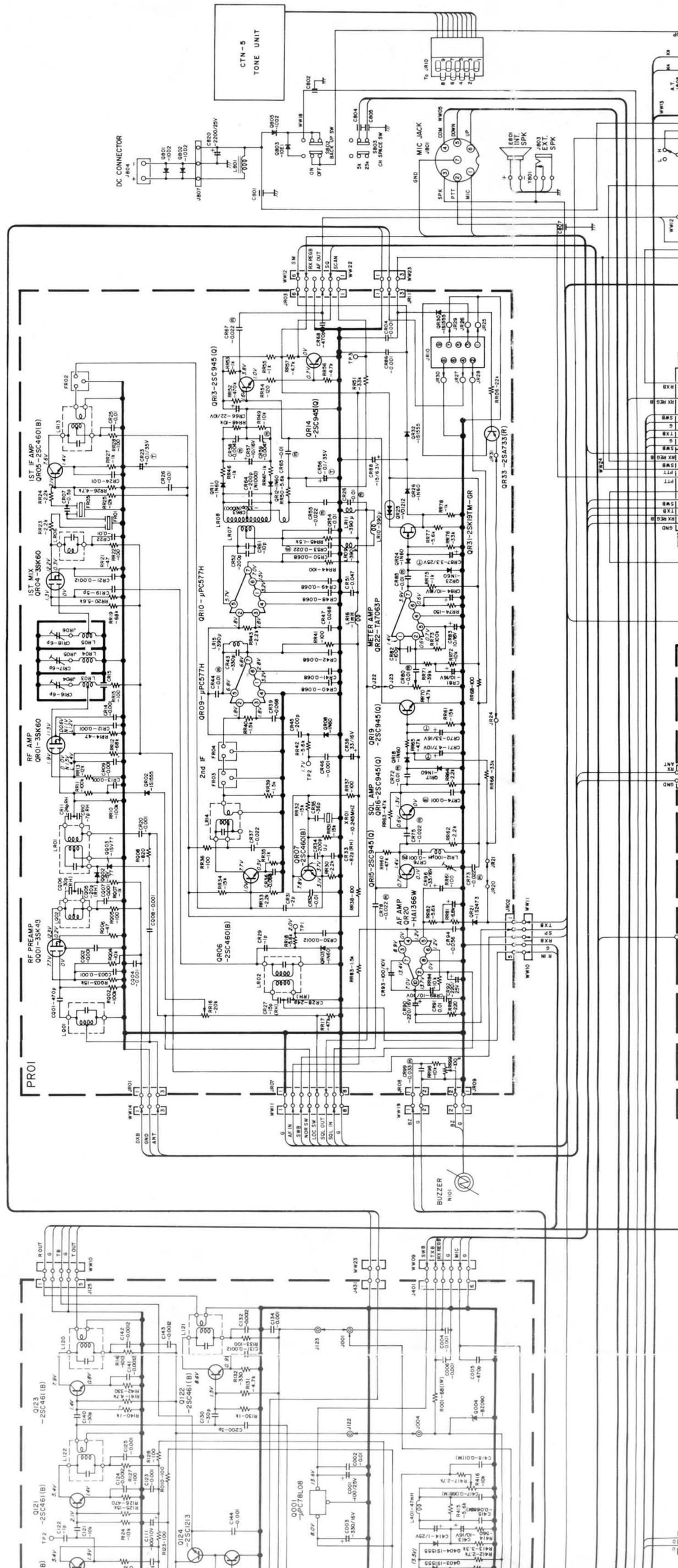
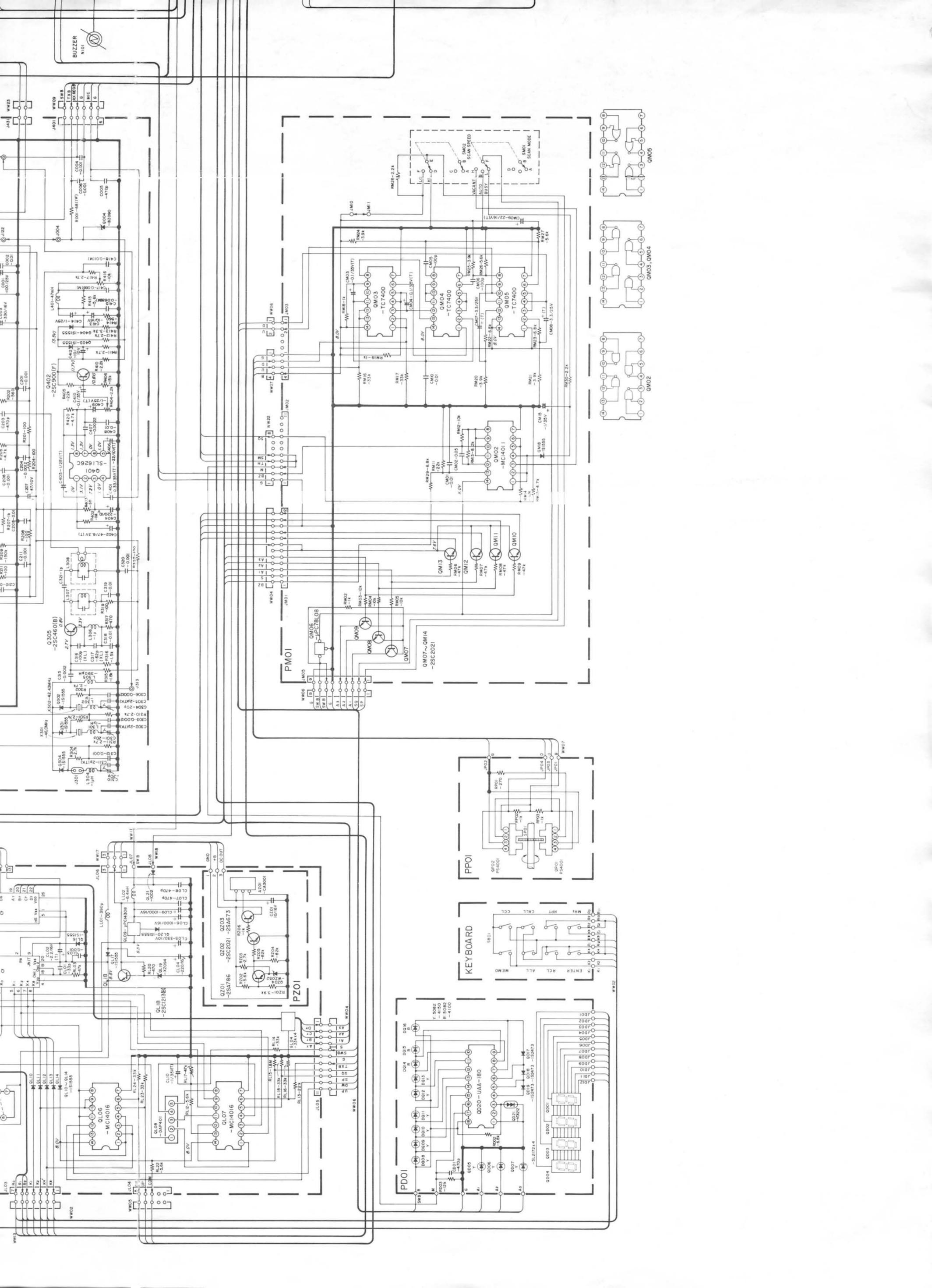
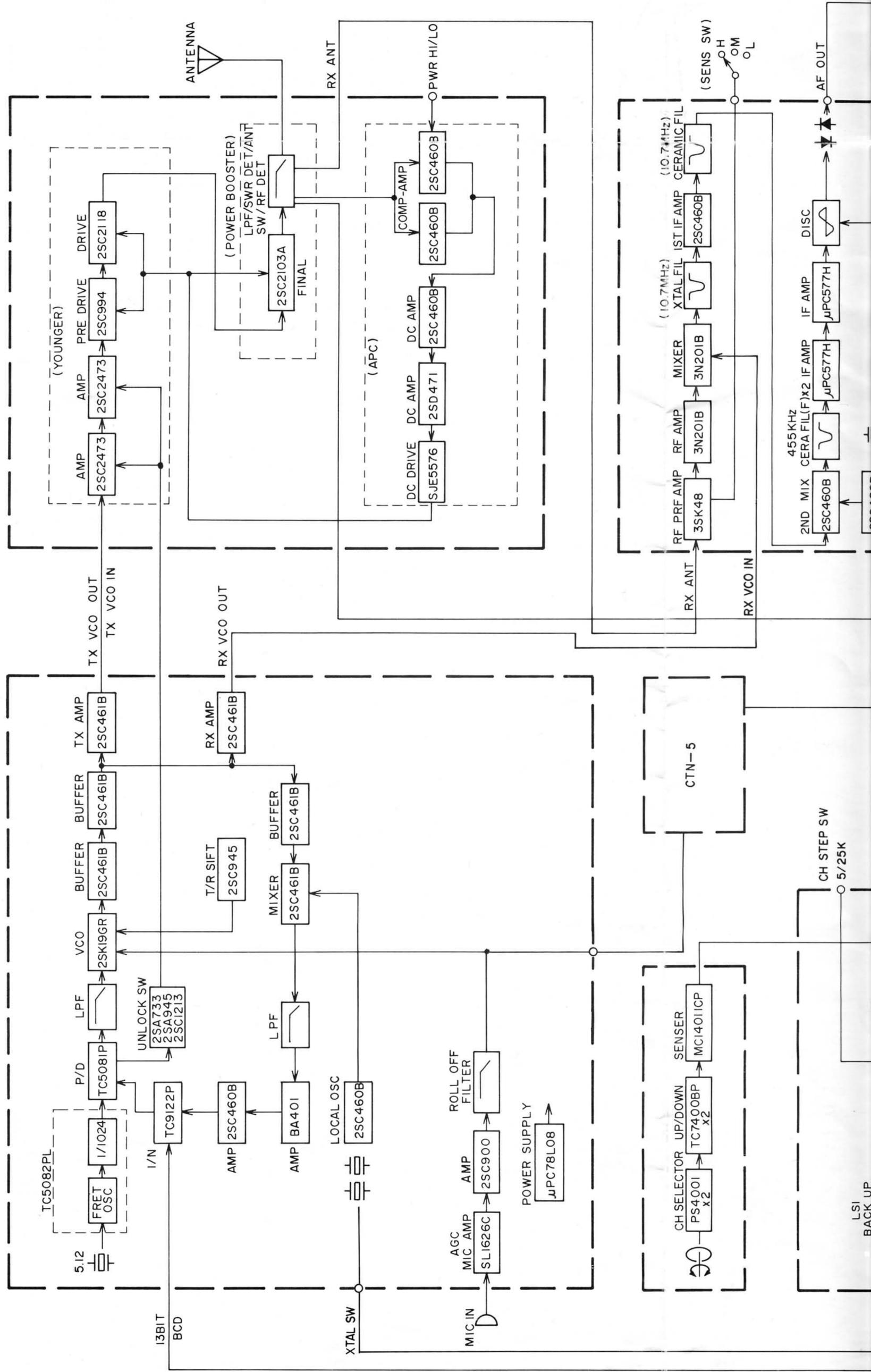


SCHEMATIC DIAGRAM FOR MODEL C8800

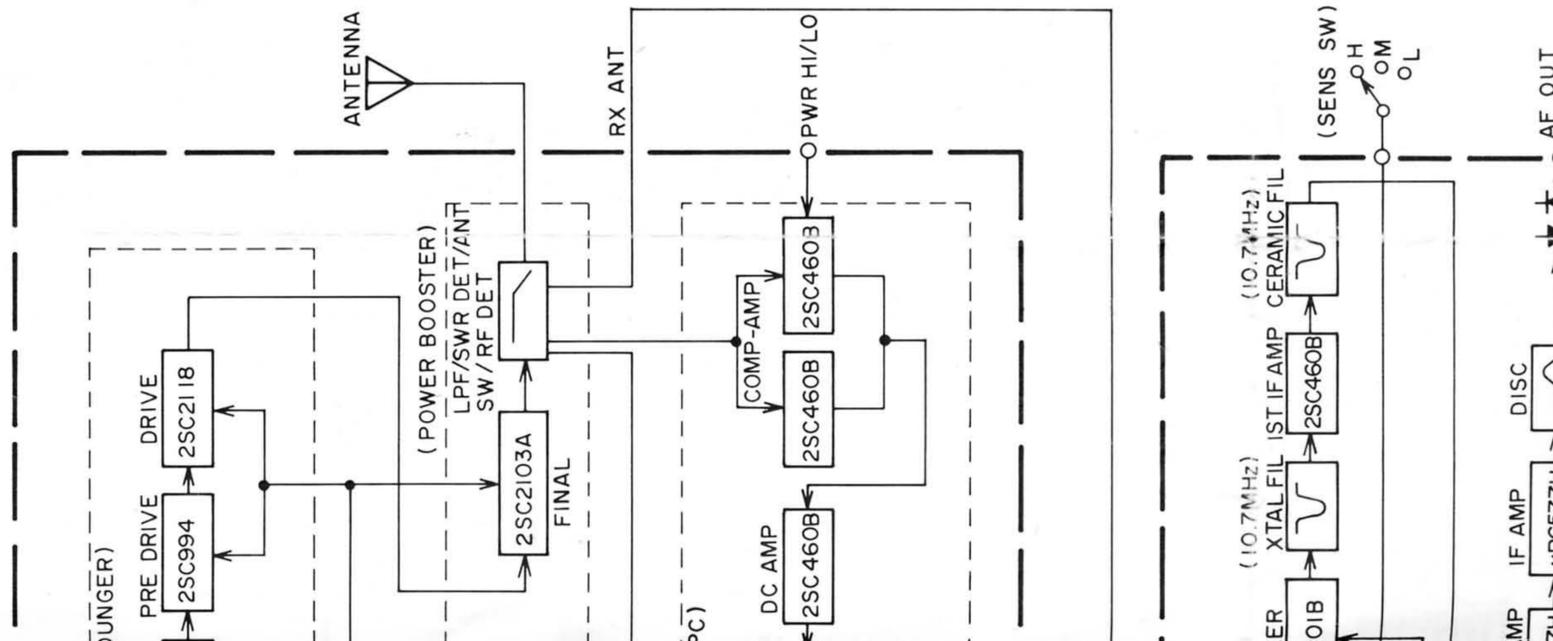
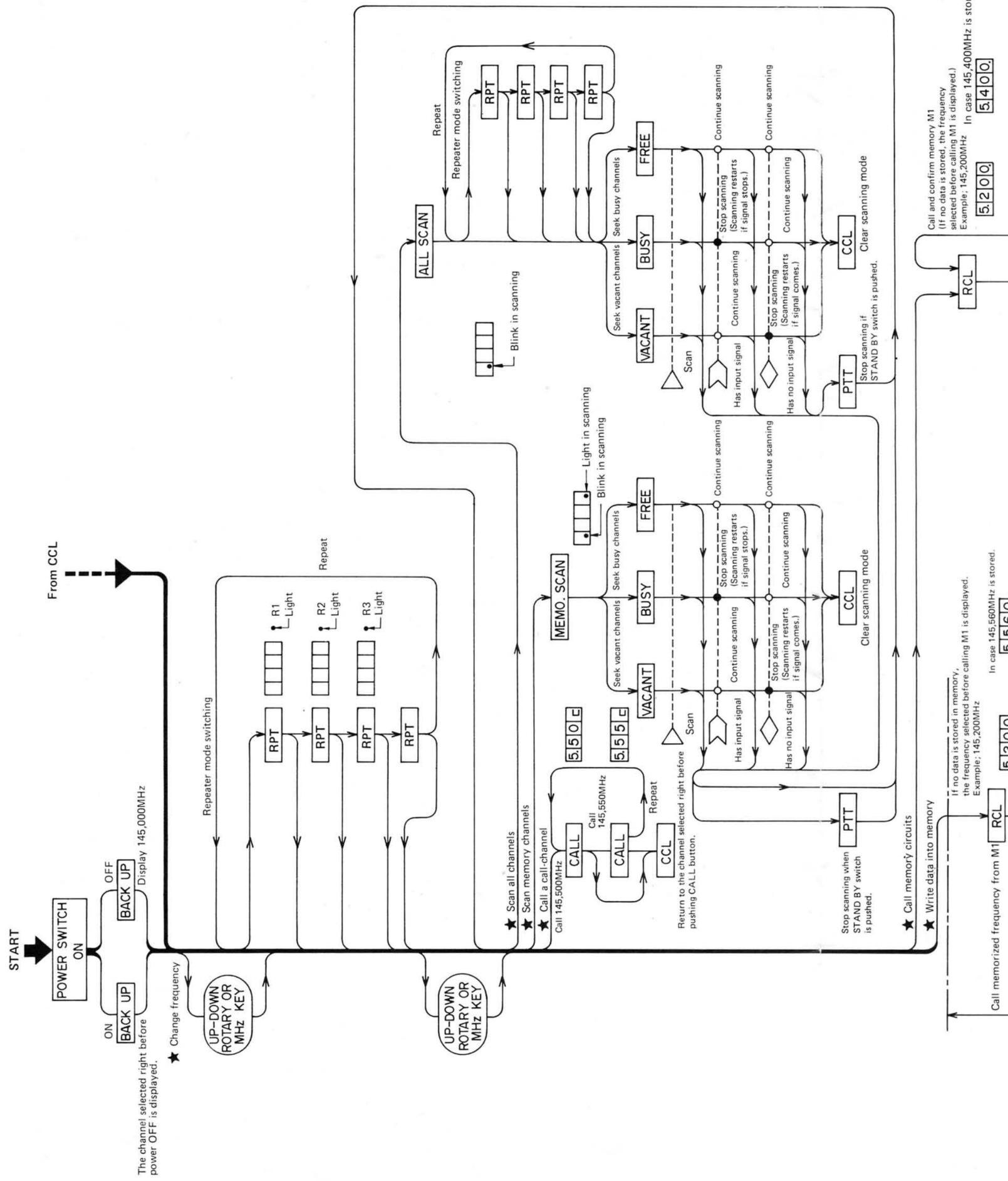


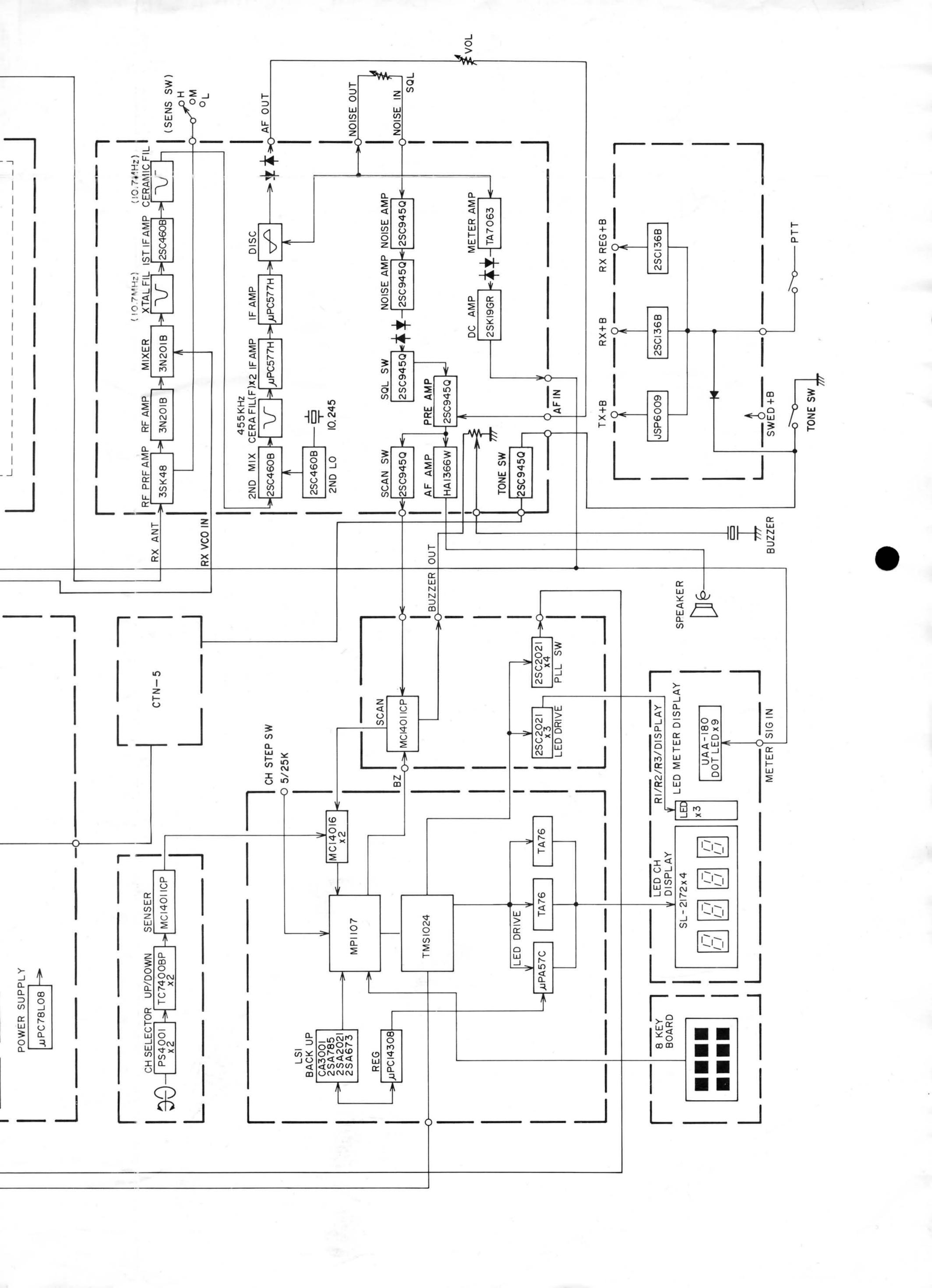


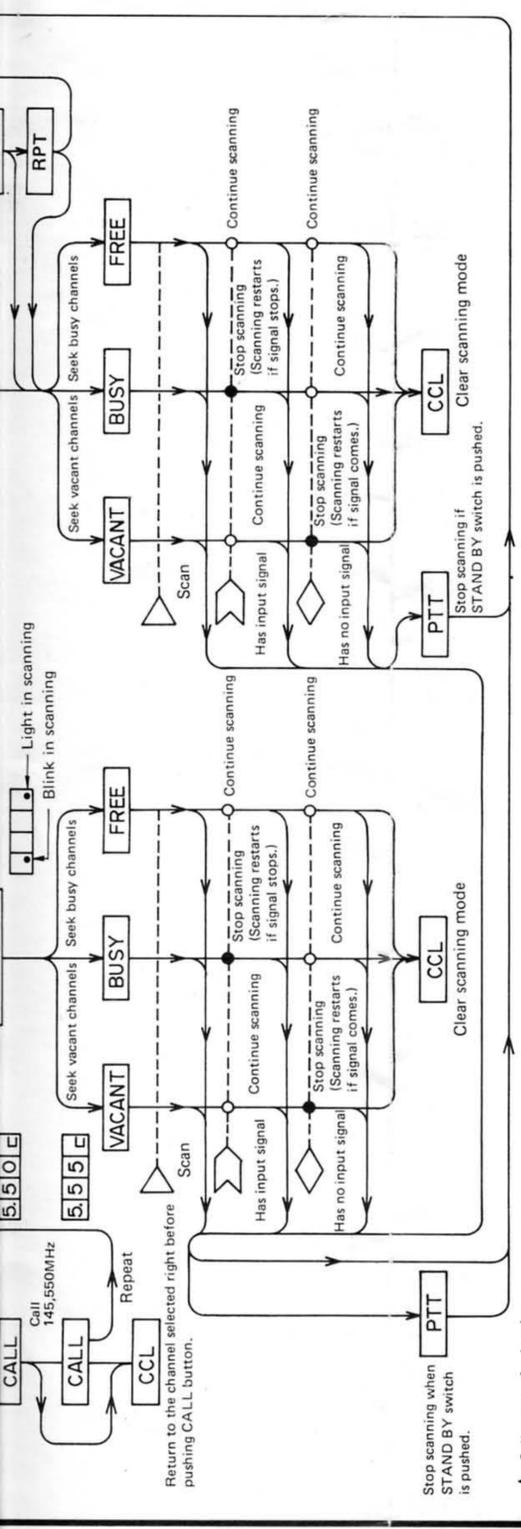
BLOCK DIAGRAM



FLOW CHART







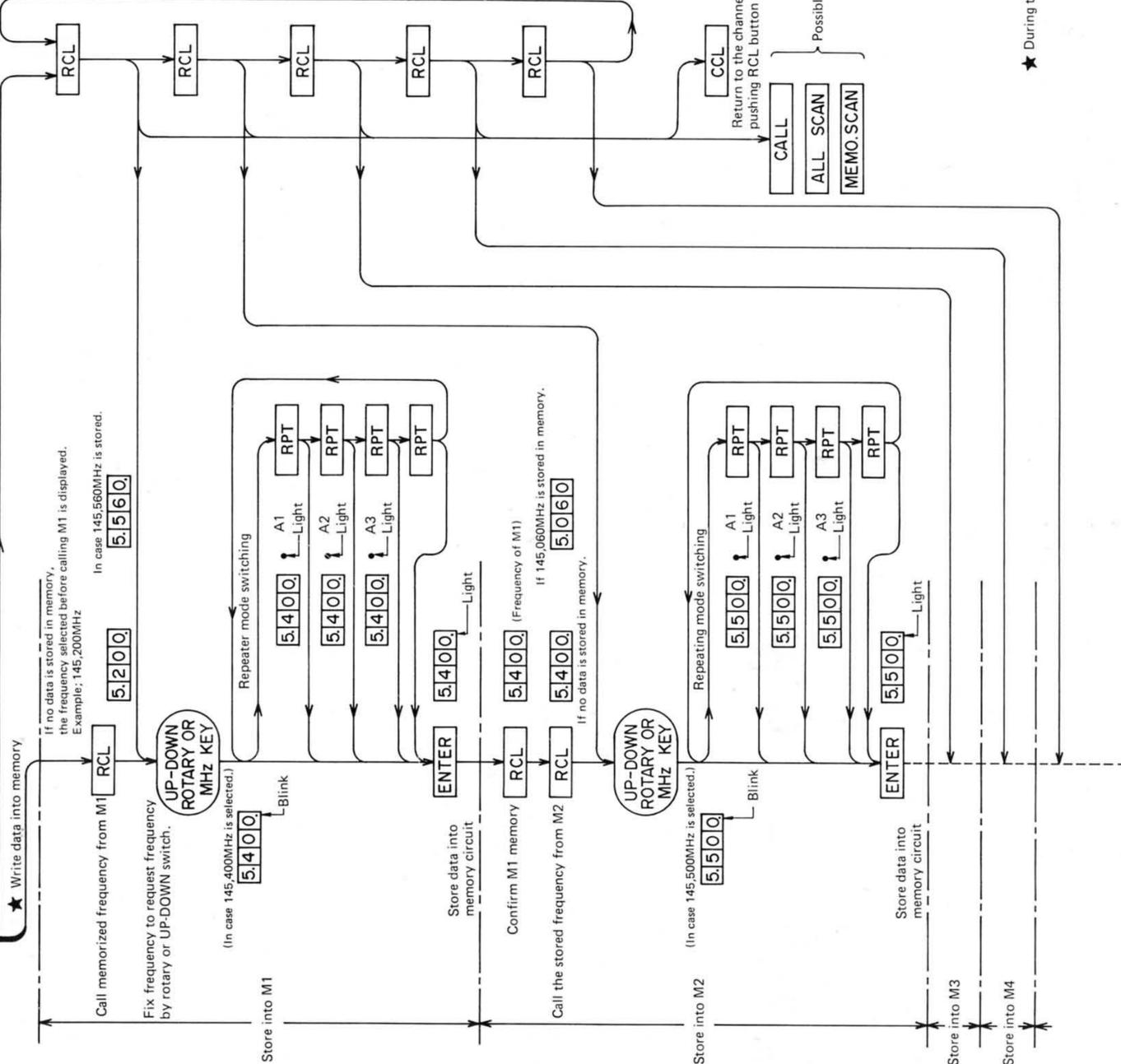
Call and confirm memory M1
(If no data is stored, the frequency selected before calling M1 is displayed.)
Example: 145,200MHz In case 145,400MHz is stored. **5.2000** **5.4000**

Call and confirm memory M2
(If no data is stored, the frequency selected before calling M2 is displayed.)
Example: 145,200MHz In case 145,500MHz is stored. **5.2000** **5.5000**

Call and confirm memory M3
(If no data is stored, the frequency selected before calling M3 is displayed.)
Example: 145,200MHz In case 145,600MHz is stored. **5.2000** **5.6000**

Call and confirm memory M4
(If no data is stored, the frequency selected before calling M4 is displayed.)
Example: 145,200MHz In case 145,700MHz is stored. **5.2000** **5.7000**

Call and confirm memory M5
(If no data is stored, the frequency selected before calling M5 is displayed.)
Example: 145,200MHz In case 145,800MHz is stored. **5.2000** **5.8000**



★ During transmission, all keys and switches don't work.

