Handbook / Kit

DB 6 NT 2,3 GHz Transverter MK2



2.3 GHz Transverter MK2

DB6NT 4.2003

Introduction

This transverter is a further development of the schematic published in 1993. Technical data of the assembly were further improved by the application of modern semiconductor devices. Redesign of the printed circuit board in respect to cooling of the high power devices improves the general performance of the transverter; its duplication has been further simplified.

The circuitry has been laid out on a ceramic-filled epoxy substrate. The receiver section achieves a noise figure (NF) of less than **0.8 dB** at an **amplification of more than 20 dB**. This eliminates the need for an external preamplifier.

The transmitter section produces an output power >1W; suppression of spurious emissions is -60 dB (typ.) while harmonics are being suppressed by -30 dB (typ.).

The transverter has been designed for the linear transfer of the 2 m band to the narrow band amateur section of the 13 cm band (2320...2322 MHz); by some small modifications it may be tuned for the Oscar P3D frequency of 2400...2402 MHz. 2 meter driving power can be adjusted from 0.5...3 Watts; a change of a resistor will even allow a driving power of 10 mW. Driving power should not be too high; this avoids unnecessary heating of the IF-subassembly due to power dissipation.

The entire transverter fits into a tinplate case measuring 60 x 150 x 30 mm. This includes IF switching, output control for coax-relays or final amplifier, crystal oscillator and local oscillator circuitry. No elaborate RF measuring equipment is needed for its alignment; a simple voltage measuring instrument will do.

Description of circuitry

The well proven "Simple Quartz Oscillator" with its FET SST310 operates at 120.889 MHz. Frequency tuning is achieved by the brass slug in the oscillator coil. Application of relevant capacitors (TK. Marked with * in the schematic) will result a temperature compensation. Two capacitor soldering points have been provided for this feature. Stability should be sufficient for average working conditions. Should there be the need for an exact and highly stable frequency, an oven-controlled oscillator (DF9LN) with an output of approx. 1 mW may be connected to the circuitry at the marked location. For this change the original crystal must be removed.

Following the oscillator is a tripler with a BFR92. A frequency of 362 MHz is being selected by a helix filter and then is being coupled into a tripler with a BFG93A. After an additional helix filter, which is to be tuned to 1088 MHz, the signal is being passed to a frequency doubler. After a helix filter with a final local oscillator (LO) frequency of 2176 MHz a LO-power of 5 mW is available.

The IF signal is being passed through individually tunable attenuator pads for transmitter and receiver; switching occurs by PIN-diodes.

Transmit/receive switching of the entire transverter is being controlled by a positive voltage during TX of the IF control cable; e.g. such a voltage is available at the output of the FT290R. Other transceivers need a small modification: connect a positive voltage, that is being generated within the transceiver during transmit mode, to the output connector via a 2K2 resistor. That's all! This type of control needs no additional control cables and has been well proven over the years. Naturally it is also possible to achieve conventional switchover with a PTT contact to ground.

Switching of operating voltages within the transverter is being controlled by transistors. Switching voltage of the transmitter is being externally provided and may be used for the

control of coax-relays or final amplifiers (max. load 2 A). This output must be protected by a fine-wire fuse under all circumstances.

The receiving section has a preamplifier stage with a HEMT - FET and an additional MMIC with a total amplification of >30 dB. This eliminates the need for an additional IF-amplifier. The input signal is being coupled to a noise-adapted NE32584C via a 4.7 pF capacitor, The second stage with the MMIC - MGA86563 follows via the multi-layer ceramic filter F6. A RX-TX PIN diode switch is being followed by another helix filter F4 and the ring mixer.

During transmit the TX-signal is being passed to a MMIC amplifier stage via the helix filter F4 following the mixer. The signal reaches a second MMIC ERA 5-SM after passing an additional helix filter F5 used for suppression of spurious emissions. The following final stage is equipped with a GaAs power FET MGF 0904.

The transmitter output has a directional coupler with the Schottky-diode BAT62-03W. It facilitates monitoring of the output power (monitoring MON.) and supports the alignment of the circuitry.

Construction:

Experience in handling SMD-parts is absolutely essential. <u>Under no circumstances</u> should this become "a first" in working with SMD, as extremely small parts are being used. Also some basic knowledge concerning the construction of VHF circuits is desirable.

Some parts, for example FET's, are very susceptible to static discharges.

It is urgently recommended to adhere to "ESD" protective measures during construction. This incorporates a grounded and temperature controlled SMD-soldering station as well as a conductive and grounded workpad.

Sequence of construction steps:

- **a.** Fitting of the printed circuit boards into the tinplate case by filing down the corners.
- **b.** Marking of the holes on the box for insertion of the SMA-coax connectors.
- c. Drilling of holes for connectors and feed-through capacitors. Tapping for M2 screws of the SMA-connectors. Shortening of SMA-connector pins to approx. 2.3 mm length. Mounting of SMA-connectors.
- d. Soldering of printed circuit boards into the case (see drawing) Solder all the way around! A 10.2 mm thick wooden board should be used as a support to achieve uniform positioning of the printed circuit board.
- e. Applying component parts and insertion of feed-through capacitors. Consult the component layout plan for soldering the helix filters. The cooling flange of the constant voltage regulator L4940V10 must be soldered to the tinplate case; its middle pin should be broken off. The FET BUZ 171 should be pressed all the way down onto the printed circuit board and its pins then be soldered. Otherwise there could be the possibility of the cooling flange causing a short at the lid of the case.
 0.5 mm soldering wire should be used for the SMD components. After completion of the soldering the module should be washed in alcohol. Should an ultrasound cleaning device

be used, the crystal should be soldered into the circuitry after completion of the cleansing. (Crystals may be damaged by intense ultrasound.) Drying at 80° C for one hour in an oven or overnight on a warm heater element.

f. Insertion of the cooling clock into the case and marking of the 2 holes that will later be used to hold the finned cooling block (drill diameter approx. 3.5 to 4 mm; smoothen the edges). Before bolting together, some heat conducting paste should be applied between cooling block, the wall of the case and the finned cooling block. Mounting the M3 x 25 mm hex screws as well as the four M2 x 4 mm screws around the final transistor should be done alternatively; any air gap between printed circuit board and the cooling block must be avoided. Some heat conducting paste should also be used when mounting the final transistor.

Alignment:

- **a.** Applying of +12 V operating voltage by using a power supply with a 1 A (approx.) current limiter. Verifying of operating voltages at the constant voltage regulators.
- **b.** Measuring of the collector voltage of the BFR92 doubler at test point 1. Screw the brass slug into the oscillator coil -- use a paper strip for "slug friction"! When oscillation occurs the voltage should drop to approx. 7.2 V.
- c. Measuring of the voltage at test point 2. By alternatively tuning the 362 MHz band filter F1-, a minimum voltage of approx. 6.2 V should be achieved (max. current = optimum drive).
 Should the ceramic screws be adjusted frequently, metal shavings will collect on their surface. An indication is a "jumpy" behavior when tuning. The coating can be removed with a fiber glass pen.
- **d.** Measuring of the voltage at test point 3. By alternatively tuning the 1088 MHz band filter -F2-, a minimum voltage of 6.6 V should be achieved (max. current = optimum drive).
- e. Screw in the brass tuning slugs according to the measurements given in the parts layout plan (Filter F3, F4 and F5).
- f. Connect a suitable antenna or a dummy load to the antenna connector of the receiver section. Measuring of the voltage at the drain of preamp transistor NE32584C and adjustment to approx. 2 V by the 1 K potentiometer at the gate of the FET's.
- g. Connect a 2 m receiver in SSB mode to the IF output. Potentiometers for RX- and TX-gain should be in the full counter-clockwise position (max. amplification). An increase in noise level should be heard in the 2 m receiver. Maximum noise and thereby maximum receiver amplification should be achieved by alternative tuning of helix filter F4. Afterwards LO-filter F3 should be adjusted. If the S-meter of the 2 m transceiver shows more than S1, the amplification of the transverter may be adjusted with the RX-gain control. This terminates tuning of the receiver section.
- h. Connect a suitable antenna or a dummy load to the transmitter output. Switch transverter to position "Senden" (=transmit). Adjustment of the idling current of the final amplifier to 220 mA or 9.5 V at the drain of the transistor. Apply a 2 m driving power of 0.5....3 Watts. Measuring of the monitor voltage at the directional coupler. This is a DC-voltage measurement which is directly proportional to the output power. Now helix filter F5 should be adjusted to maximum monitor voltage (= output power). Reduction of monitor voltage to approx. 1 volt by turning TX-gain potentiometer clockwise. Maximum output power

should be tuned by fine tuning helix circuit -F5- (2320 MHz) and LO-filter -F2- (2176 MHz).

- i. Adjustment of transmitting power by tuning the TX-gain potentiometer while monitoring the voltage of the directional coupler. In order to assure operation within the linear portion of the transverter, output power should be adjusted to 80% of the maximum value.
- j. Connect a receiving antenna. Adjustment of oscillator frequency by tuning to a known beacon. Should it not be possible to tune to the desired frequency, a choke of 0.22 μ H can 4be soldered in parallel to the crystal.
- k. Glue a piece of low resistance conducting foam into the lower lid of the case. (This eliminates the possibility of resonance of the case and thereby precludes unwanted oscillations.)
 Install the entire transverter into an enclosure. For better cooling the transverter should be mounted onto the chassis. Specially tapped holes have been provided in the cooling block. A good cooling also prevents frequency drifting of the oscillator. A suitable coax relay facilitates transmit-receive switching.

This is it! Ready to go on the Air!

Special thanks go to Lorenz **DL6NCI** whose "building" experience and numerous suggestions eventually led to series production standard of the transverter. I also would like to thank Gert **DG8EB**, Richard **DF5SL** and Norbert **DL4DTU** who verified 100% reproduction of the circuitry by "copying" the prototype construction.

References:

- 1. Rogers printed circuit board material, Mauritz Co Hamburg, data sheet RO4003
- 2. NEC data sheet NE32584C
- 3. Siemens data tables on RF semiconductors
- 4. NEOSID filter data tables
- 5. Philips semiconductor data tables
- 6. Toko helix filter data tables
- 7. Mini-Circuits data sheet on ring mixers
- 8. "Transverter for 2.3 GHz by DB6NT", DUBUS 3/93 (Dubus book IV respectively)

Purchasing hints:

Complete modules or kits

KUHNE electronic GmbH Scheibenacker 3 95180 Berg/Oberfr. Tel: 0049 (0) 92 93 800 939 Fax: 0049 (0) 92 93 800 938 Email: kuhne.db6nt@t-online.de

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Transmit/receive systems may only be operated when complying with legal regulations.







2,3 GHz Transverter MK2 DB 6 NT 2.2002





2,3 GHz Transverter MK2 DB 6 NT 6.2000

Version " 2304 / 2320 MHz "



2,4 GHz Transverter MK2 DB 6 NT 8.2002









2,3 GHz Transverter MK2 DB 6 NT 2.2004









Information zur Sende - Empfangsumschaltung der DB6NT Transverter

Um DB6NT Transverter von Senden auf Empfang Umzuschalten sind zwei Möglichkeiten vorgesehen. Zum einem besitzen die Transverter einen "PTT - Anschluss" der bei Sendebetrieb über einen Kontakt nach Masse zu schalten ist. Des weiteren ist die Umschaltmöglichkeit über das ZF - Kabel vorgesehen. Dazu ist eine Spannung von ca. +3...12V im Sendefall auf den Innenleiter der ZF - Buchse zu legen. Das erspart eine zusätzliche Verbindungsleitung zwischen Transceiver und Transverter.

Bei dem Transceiver FT290R und dem IC402 ist diese Umschaltsteuerung bereits eingebaut.

Bei dem FT290RII muß diese Schaltung nachträglich eingebaut werden. Einbauanleitung von G4DDK auf seiner Internet Seite www.btinternet.com/jewell/

Bei dem IC202 vom ICOM ist diese Steuerung leider Invers eingebaut. Das heißt wenn der Transceiver auf Empfang ist und an den Transverter angesteckt wird schaltet dieser auf Senden! Es ist eine kleine Änderung im IC202 erforderlich.

Information about RX-TX switching of DB6NT Transverters

To switch the DB6NT transverter from RX to TX you have two possibilities.

Switch the port "PTT" on the transverter to ground.

Via the IF cable, please apply 3...12 Volt to the center conductor.

You save one additional PTT patch cord between the transverter and transceiver.

In the YAESU_FT-290R (old model) and ICOM_IC-402 transceiver is the switch over to +12 V at TX on the center conductor build in. For the YAESU_FT-290RII (new model) you have to build in a switch over. Info via G4DDK internet side: www.btinternet.com/ jewell/

IMPORTANTI

The ICOM IC-202 deliver +12 Volt at RX! If you connect a DB6NT transverter to a ICOM IC-202 the transverter will switch over to TX !

With a small modification the ICOM IC-202 will apply +12 Volt on TX.



Umbau des IC202 auf richtige RX/TX Umschaltung. Modification of T/R - Switching in IC202

