

### **BC-11 EXTERNAL VIEW**



### **BC-11 CIRCUIT DESCRIPTION**

#### General

The BC-11 is a rapid charger for the PB-5 to PB-9 Ni-Cd batteries for TH-25, TH-45 and TH-55.

#### **Theory of Operation**

The operation of each block is explained below.

#### 1) + 11V AVR Circuit

This AVR circuit, consisting of a 2SD600F transistor (Q1) and DZA11Y Zener diode (DZ1) provides an output of approximately +11V as the reference voltage for the charging circuit consisting of IC2 to IC5.

### 2) Battery Pack Detect Circuit

This circuit detects whether a battery pack is inserted in the charger. Outputs from this circuit are routed to the reset circuit and the battery recognition circuit.

When a PB-6 or PB-9 is inserted in the charger, a small amount of current flows from Q2 : 2SA608E through R9 to the charging terminal B1 and Q2 turns on. As a result,

an output of approximately 11V appears at (A) in Figure 1. Similarly when a PB-5 or PB-7 is inserted Q3 : 2SA608E turns on and approximately 11V is provided at output (B). When a PB-8 is inserted Q4 : 2SA608E turns on and approximately 11V is provided at output (C).



Downloaded by Amateur Radio Directory www.hamdirectory.info

Set AN 15

Fig. 1 Battery Pack Detect Circuit Block Diagram



#### 3) Reset Circuit/Charge Status Memory Circuit/ Display Circuit

The reset circuit initializes the charging status memory circuit.



Fig. 2 Reset Circuit/Charge Status Memory Circuit/Display Circuit Block Diagram

The charge status memory circuit is an R-S flip-flop configured from transistors and resistors. The two states of the flip-flop are called COMPLETE and CHARGING. Outputs from the flip-flop drive the LED in the indicator circuit and reset the timer, complete sensor, and chopper regulator. In the COMPLETE state Q9 : 2SC536E is off and Q10 : 2SC536E is on. In the CHARGING state Q9 is on and Q10 is off.

When a battery pack is not inserted, Q8 : 2SC536E and Q7 : 2SC536E turn off. As there is no base voltage to Q9, Q9 also turns off. The base of Q10 receives enough bias from Vcc to turn on, resulting in 0V at the collector. The current flow through R41 to the COMPLETE indicator in LED1 which glows green, because of Q9 if off.

When the battery pack is inserted Q8 and Q7 turn on. As soon as Q7 turns on, charging current flows through R33, R34, and Q9 to C7 and Q9 turns on. The base voltage of Q10, which is connected to Q9 through diode D13, then drops and Q10 turns off. Since Q10 is off, current flows through R40 to the CHARGING indicator in LED1, which glows red to indicate that the battery is charging. When charging of C7 is completed, on-current continues to flow to the base of Q9 through R39 and D8.

When charging is completed the complete sensor (IC2) outputs a Low ("L")signal that ends the flow of current to the base of Q9, turning Q9 off. As a result current flows through R41 to the COMPLETE indicator in LED1, which glows green to indicate that charging is complete.



#### 4) Battery Recognition Circuit

The battery recognition circuit uses NAND logic to recognize the battery type from the outputs from the battery pack detect circuit. Outputs from this circuit are sent to the charging current limiting circuit and sensor level switching circuit.

	INF	TUY	OUTPUT			
	Α	8	D	F	G	
PB-5	L	н	Н	н	L	
PB-6	н	L	н	L	н	
PB-7	Н	н	Ł	L	н	
PB-8	L	L	н	L	L	
PB-9	н	L	н	L	н	



### 5) Charging Current Limiting Circuit

This circuit receives the output of the battery recognition circuit and limits the charging current according to the type (current capacity) of battery. The charging current ICR is detected as a voltage drop across R1 (0.15 $\Omega$ ), which is provided to pin 3 of the operational amplifier IC4(1/2) : LA6393A. Pin 4 receives a reference voltage (VREF) used as a comparison standard for limiting the charging current. The VREF is changed by ON and OFF of Q5 and Q6 (See **Table 4**).

Pin 2 of IC4 : LA6393S provides "L" output when  $V_{REF}$   $< V_{CR}$ , stopping the operation of the chopper regulator (IC1. : STK772B) and reducing the charging current. The charging current is limited by the formula :

 $I_{CR} MAX (A) = V_{REF} (V)/0.15(\Omega)$ 

**Downloaded by** 

Amateur Radio Directory

www.hamdirectory.info



Fig. 3 Battery Recognition Circuit Block Diagram

	Q5	Q6	VREF	ICR MAX
P8-5	OFF	ON	0.15V	1.0A
PB-6	OFF	OFF	0.25V	1.7A
PB-7	ON	OFF	0. <b>36</b> ∨	2.4A
PB-8	OFF	OFF	0.25V	1.7A
PB-9	OFF	OFF	0.25V	1.7A

Table 4



Fig. 4 Charging Current Limiting Circuit Block Diagram



#### 6) Sensor Level Switching Circuit

This circuit receives the output of the battery recognition circuit and aligns the voltages supplied to the charging status detect circuit according to the battery type (voltage) so that they are nearly equal at completion of charging.



Fig. 5 Sensor Level Switching Circuit Block Diagram

The pin voltages while the Ni-Cd battery is charging are approximately 1.2 times the voltages at the completion of charging, (See Figure 6.)

The battery terminal voltage EB is as follows:

Approximately 14.4V for the PB-8

Approximately 8.6V for the PB-5, 6, 7, 9

The charging line voltage EI is the EB voltage plus a  $1.4 \vee$ voltage drop added by a diode.



Fig. 6 The pin voltages while the Ni-CD Battery is charging

The EI voltage is output with a level shift as the voltage EO to the charging status detect circuit via Zener diode DZ6 and diodes D17 and D18. The amount of the shift is controlled by switching Q11 : 2SA608E and Q12 : 2SA608E on and off. (See Table 5.) If Eq. (1) is substitued into EO in Table 5, the results are:

SHIFT Es(V)

PB-5

Q11

Q12

PB-8 : EO = EB - 6.2V PB-5, 6, 7, 9 : EO = EB - 0.6V

At the completion of charging the value is approximately 8V.





#### 7) Charging Status Detect Circuit

the charging status memory circuit.

This circuit detects the completion of charging and outputs a signal to stop charging. When no battery pack is inserted or charging is completed, a High ("H") Reset signal is applied to D15. When a battery is inserted the Reset signal applied to D15 is cleared. When the Reset signal is cleared, pin 4 of IC2 : KCH-1003 holds the reset state due to the charge in C8 for the duration of the R46-C8 time constant, then goes "L" to clear the reset state. Pins 8 and 9 of IC2 receive divided portions of the battery voltage. These inputs are tracked as the charging is performed in the long-term memory capacitor "MD". As the Ni-Cd battery charges, the battery voltage reaches a peak, then declines. (See Figure 6.) The MD stores the peak voltage, which is compared with the divided voltages at pins 8 and 9. When the difference  $\Delta V$  is the same, a "L" signal is output fr. m pin 11 to indicate that charging is complete.

The signal inuicating completion of charging is applied to



Fig. 7 Charging Status Detect Circuit Block Diagram

#### 8) Timer Circuit

Battery defects may result in charging continuing indefinitely without completion, so this timer outputs a signal that stops charging approximately 1.7 hours after charging begins. When charging begins and the Reset signal is cleared at pin 3, IC3 : AN6780 begins counting. At the first count of 16384 pin 6 goes from "H" to "L".

The output from pin 6 is connected to the Stop input (pin 2), so the output of IC3 is held in the "L" state until IC3 receives another Reset signal (for example, when the battery is removed).



#### 9) Voltage Comparator Circuit

This circuit monitors the output (EO) of the sensor level switching circuit and indirectly detects abnormal conditions in the battery pack connected to the charging terminal. When the EO voltage falls to 5.2V or lower, the charging control line goes "L" to halt charging.









× New Parts

# **BC-11 (RAPID CHARGER)**

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis. Telle ohne Parts No. werden nicht geliefert.

**BC-11 PARTS LIST** 

	Ref. No.	Address	New	Parts No.	Description	Desti-	Re		
	参照者号	位置	Ħ	都品書号	部品名/規格	t 向	mari 1981		
BC-11									
	1	1A	•	A02-0815-08	CASE	1	Γ		
	2	1A,1B	•	A02-0817-08	BATTERY POCKET				
	3	18		B46-0411-00	WARRANTY CARD	к			
	4	18	•	B50-8134-08	INSTRUCTION MANUAL				
	5	1B ·	•	E23-0604-05	TERMINAL				
	6	2A		E30-2038-08	AC CORD	K,M,M2			
	6	2A		E30-2072-08	AC CORD	w			
	6	2A		E30-2073-08	AC CORD	ΙT			
	6	2A		E30-2095-08	AC CORD	×			
	8	28	•	H01-8128-08	ITEM CARTON CASE				
l	9	2B		H10-2584-02	POLYSTYRENE FOAMED FIXTURE (L)				
	10	2B		H10-2585-02	POLYSTYRENE FOAMED FIXTURE (R)				
	11	3A		J02-0439-05	FOOT				
	12	3A		J39-0424-05	SPACER				
	Т1	2A		L01-8081-08	POWER TRANSFORMER (AC120V)	к,м2			
	T1	2A		L01-8112-08	POWER TRANSFORMER (AC220V)	M,W			
	Т1	2A		L01-8122-08	POWER TRANSFORMER (AC240V)	т,х			
	A	ЗА		N30-3006-41	MACHINE SCREW (M3 X 6)				
	В	2A,1B		N34-4006-46	MACHINE SCREW (M4 X 6 TR)				
	С	2A,1B		N35-4006-45	MACHINE SCREW (M4 X 6 BI) BLK				
	D	2A		N87-3008-46	TAPTITE SCREW (Ø 3 X 8 BR)				
	Е	1A		N89-3008-45	TAPTITE SCREW (\$\$ X 8 BI) BLK				
	SW1	<u>3</u> A		S36-1407-05	POWER SW				
	7	3В	•	W02-0399-08	CHARGE CONTROL UNIT				
				CHARGE CONTI	ROL UNIT (W02-0399-08)	L			
	C1		Т	CE04EW1V222M	ELECTRO 2200µF 35₩∨				
	C2			CE04EW1C470M	ELECTRO 47µF 16W∨				
	C3			CE04EW1H010M	ELECTRO 1µF 50WV				
	C4		.	CE04EW1E471M	ELECTRO 470µF 25W∨				
	C5,6			CE04EW1C100M	ELECTRO 10µF 16WV				
	C7			CE04EW1A101M	ELECTRO 100µF 10WV				
	C8			CE04EW1C100M	ELECTRO 10µF 16WV				
	C9,10			CE04EW0J101M	ELECTRO 100µF 6.3WV				
	C11			CE04EW1C330M	ELECTRO 33µF 16WV				
	C12			CK45B1H102K	CERAMIC 0.001µF 50W∨				
	C14			CE04EW1H010M	ELECTRO 1µF 50WV				

. . .

▲ indicates safety critical components.

UE : AAFES(Europe)

A: Saudi Arabia T: England U: PX(Far East, Hawaii)

X: Australia M: Other Areas



× New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

Ref. N	Ref. No. Addres		- H - H	Parts No.	Description		Re- marks
学派者	十号	位置	Perts Sfi	8 A # 9	部 晶 名/規 格		
MD				<b>C9</b> 1-1038-08	ELECTRO		
F1				F05-2525-05	FUSE (2.5A)	w,x	
F1				F06-2522-05	FUSE (2.5A)	M,M2,T	
F1				F06-2523-05	FUSE (2.5A)	ĸ	
_				J13-0039-05	FUSE HOLDER	w	
L1				L33-0694-08	CHOKE COIL (470µH)		
R1				R92-0683-08	FL-PROOF 0.15Ω 4W		
D1-5				DSA26B	DIODE		
D6-16				DS442	DIODE		
D19-21				DS442	DIODE		4.
D13-21				GZA11Y	ZENER DIODE (11V)		1
DZ2-4				GZA10Z	ZENER DIODE (10V)	· · ·	
DZ5				GZA2.0X	ZENER DIODE (2V)		
DZ6				GZA5.6X	ZENER DIODE (5.6V)		T
DZ7		· ·		GZA7.5Y	ZENER DIODE (7.5V)		
DZ8			-	GZA3.0X	ZENER DIODE (3V)		
IC1		1		STK772B	IC (CHOPPER REGULATOR)		
IC2				KCH-1003	IC (VOLTAGE SENSOR)		ľ
IC3		-		AN6780	IC (TIMER)		
1C4				LA6393S	IC (DUAL OP IC)		
IC5				LC4011B	IC (QUADRUPLE NAND GATE)		
Q1				2SD600F,KF	TRANSISTOR		
Q2–5				2SA608E,F	TRANSISTOR		1
Q6				2SC536E,F	TRANSISTOR		1
Q7				2SA608E,F	TRANSISTOR		1
Q8-10				2SC536E,F	TRANSISTOR		
Q11,12				2SA608E,F	TRANSISTOR		
Q13,14				2SC536E,F	TRANSISTOR		
LED1		2A		SLP-540D	LED (RED/GRN)		

### **BC-11 BLOCK DIAGRAM**

Sec. Sec.











### BC-11 PC BOARD VIEW

et in Arten Alexandre Parlante

der bereit





143 .....

Contract State and Land

### **BC-11 CIRCUIT DIAGRAM**

