

## Icom ICB1050 and JWR M2

These rigs use an MC145106P programmable PLL frequency synthesis IC to control the VCO frequency. A 10.24 crystal feeds the IC to provide a reference frequency of 10 kHz. Two more crystals provide references of 16.50625 MHz for RX and 16.96125 for TX.

The VCO output is divided in the IC according to the binary code supplied by the channel switch (see chart). It is a relatively simple matter to convert the operating frequencies of the rig once you know that each increment of "1" in the code will increase the frequency by 10 kHz.

If you compare the standard binary codes with those required to produce the range 29.30 to 29.69 MHz you will note that pins 10 and 11 must be held high: pin 17 is unchanged and can remain connected to the channel switch: pin 16 requires an inverter in the line from the channel switch (except when repeater shift during TX is required). Finally, pins 12, 13, 14 and 15 bear no obvious relationship but, in fact, there is an increase of 1 0 1 0 for 10 metre operation (or an increase of 1 0 0 0 for repeater shift).

Incrementing by this amount can be performed by a "4008" 4 bit binary adder IC. The inversion of pin 16 can be done with a "4077" quad exclusive NOR IC, with gates left over for additional functions.

Since repeater shift will be required only on the top channels, we can prevent accidental out-of-band TX by reverting to normal operation (even if repeater shift is switched on) by detecting the switch codes on the green and blue wires. If either of these is high, repeater shift is inhibited.

As an aid to setting-up, a spare gate can be used to drive an LED when the Lock Detect output from the PLL IC is high. This gives a visual indication that the VCO adjustment is correct. Once the VCO has been adjusted, the LED gate connection can be removed from pin 8 of the PLL IC and connected to point "X" to indicate when repeater shift is actually happening. The LED can be fitted permanently in the front panel of the rig.

The repeater shift wire is taken from point "S", via a switch, to pin 4 on the mic socket.

Note that the ~~violet~~ wire from the channel switch must be disconnected and pins 10 and 11 linked to pin 1 of the PLL IC. The brown and the pink wires are left in place.

T202, the VCO coil, is adjacent to the PLL IC and its core must be freed from wax (carefully!) and unscrewed until the VCO locks on RX (pin 8 will be high). Adjust the VCO similarly for TX and ensure that it remains in lock for channels 1 and 40 on TX and RX. The VCO core can then be rewaxed. Little or no output will be evident on TX until you adjust T209, T301, T303 and T307. To optimise RX sensitivity adjust T101 and T102.

If you check the frequency you will find an error which can be corrected by adjusting CT202 for TX and CT201 on RX. To increase the deviation, rotate RV303 clockwise.

### NOTES:

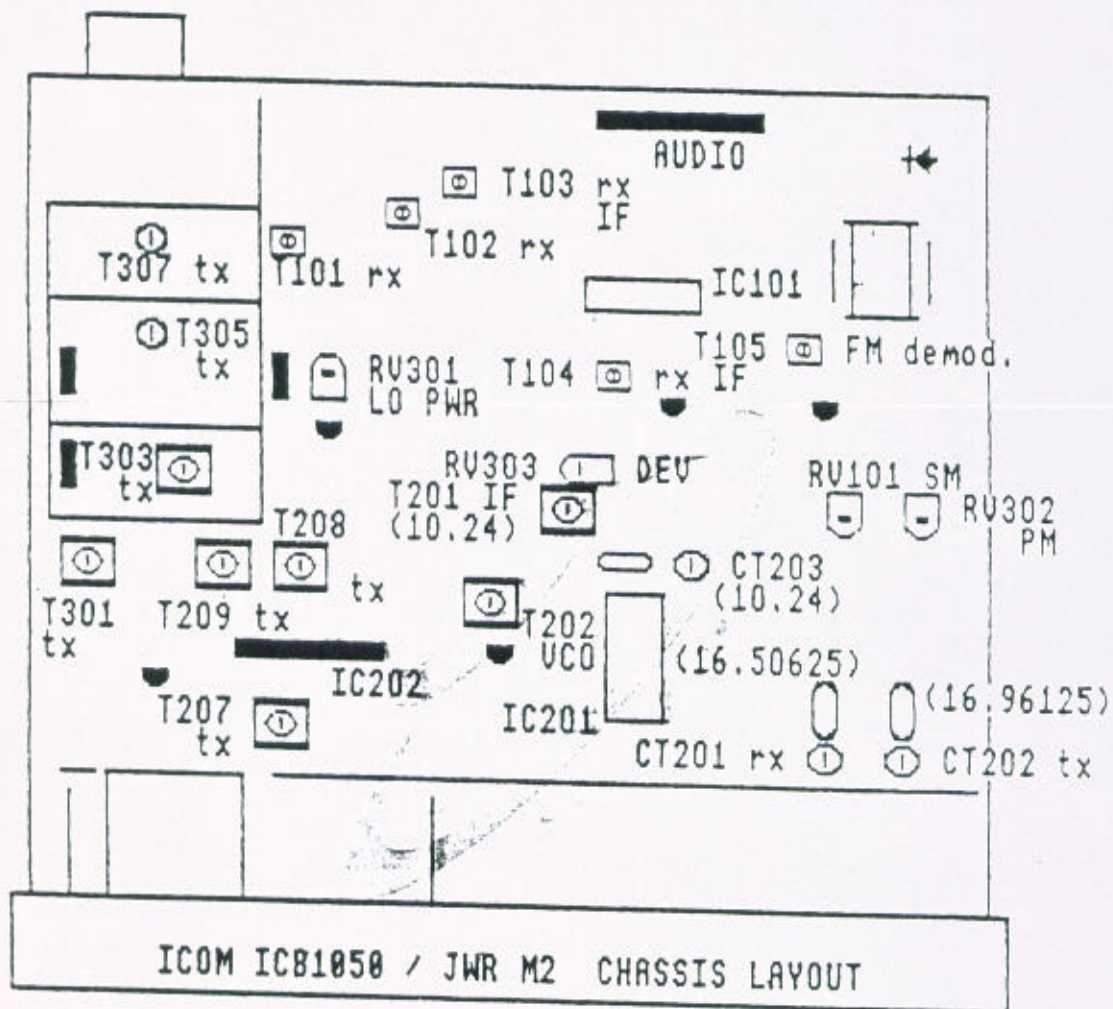
The RF gain varies with the squelch setting. If you remove the diode from beneath the board you might find operation more satisfactory.

T101 has a nasty habit of going open-circuit, making the rig extremely deaf, to say the least! Replace it with a Toko 113CN2K159DZ from CIRKIT.

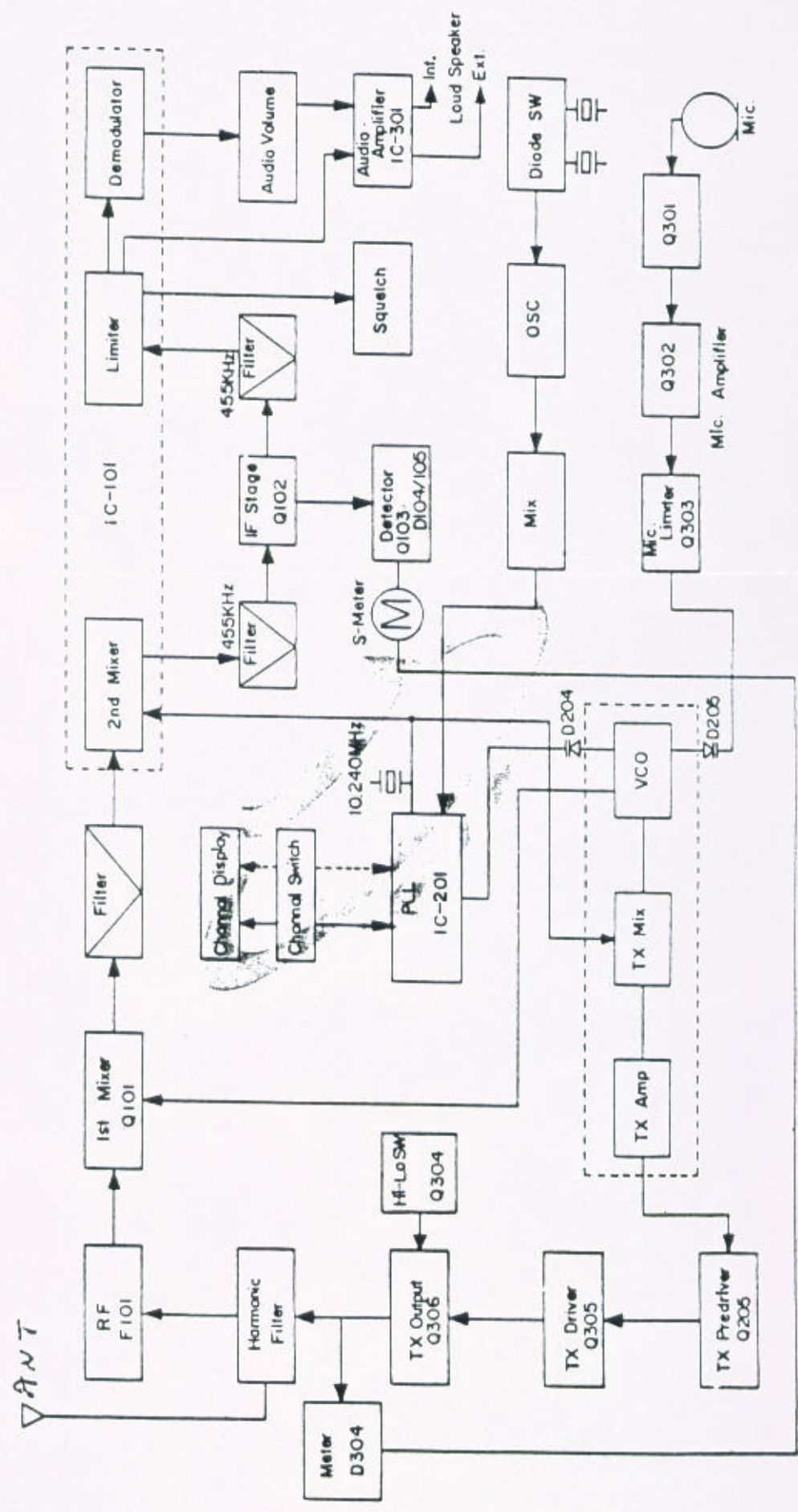
Replace C103 (100pF) with 33pF and C106 (22pF) with 10pF and retune to improve receiver performance.

Replace CF101 with a 10.695 crystal filter from P.R. Gollledge to improve I.F. selectivity.





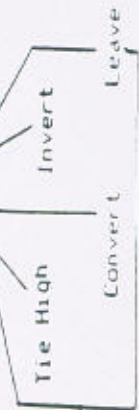
BLOCK DIAGRAM





CHAN	Standard	10 metre	Tx only	Repeater	
40	001001111	27.99125	249	011111001	29.69
39	001001110	27.98125	248	011111000	29.68
38	001001101	27.97125	247	011110111	29.67
37	001001100	27.96125	246	011110110	29.66
36	001001011	27.95125	245	011110101	29.65
35	001001010	27.94125	244	011110100	29.64
34	001001001	27.93125	243	011110011	29.63
33	001001000	27.92125	242	011110010	29.62
32	001000111	27.91125	241	011110001	29.61
31	001000110	27.90125	240	011110000	29.6
30	001000101	27.89125	239	011101111	29.59
29	001000100	27.88125	238	011101110	29.58
28	001000011	27.87125	237	011101101	29.57
27	001000010	27.86125	236	011101100	29.56
26	001000001	27.85125	235	011101011	29.55
25	001000000	27.84125	234	011101010	29.54

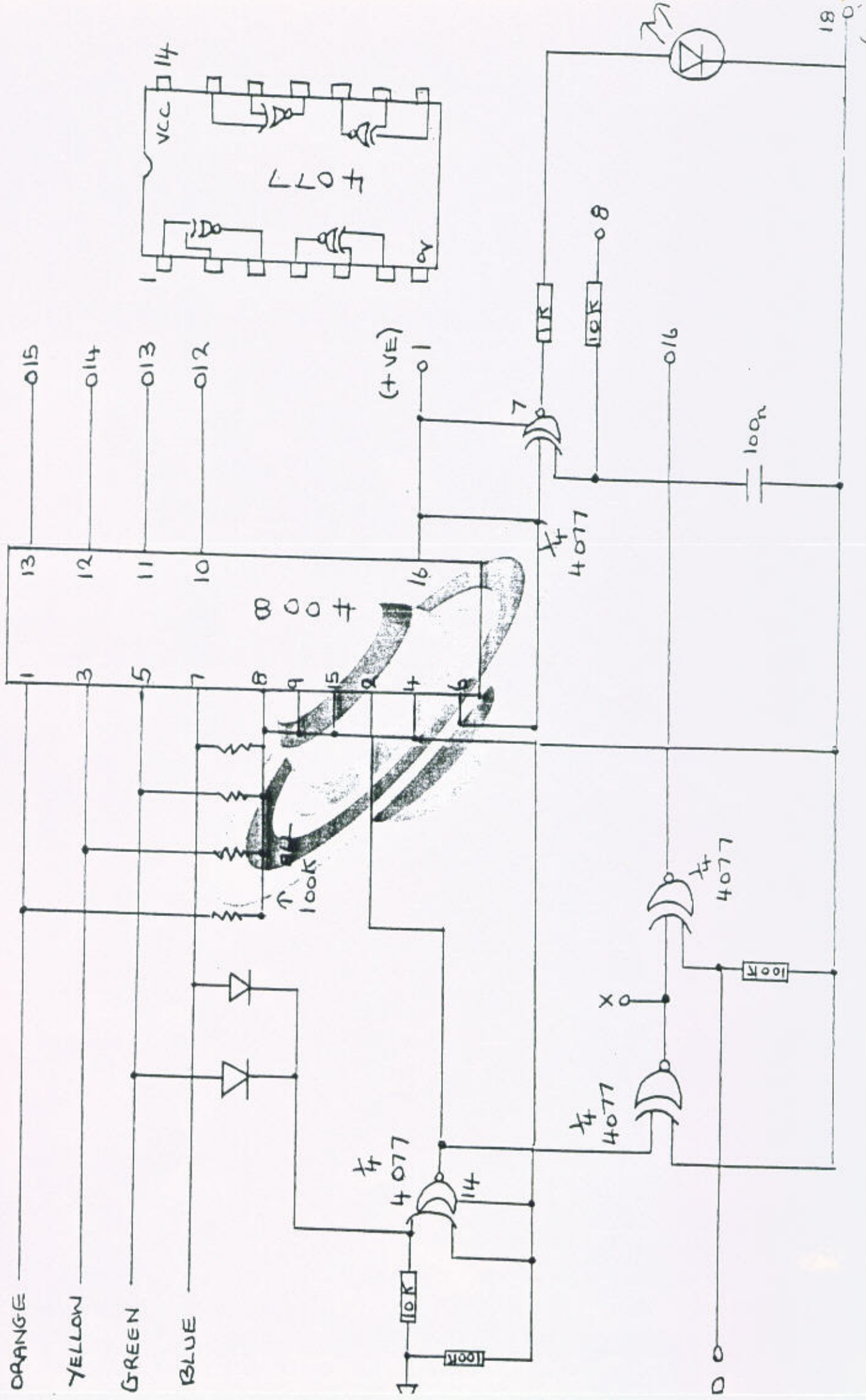
Icom ICB1050 and JWR M2



Pins 9 10 11 12 13 14 15 16 17

HERE  
↙

RED 0  
K 0  
10M CONVERSION FOR 1COM 1CB 1050 / 3WB M2





### THE SIMPLE METHOD

CONNECT TOGETHER THE SOLDER PADS OF PINS 10,11,12 ON THE MC 145106 PLL IC,VIA A 10K RESISTOR CONNECT THE COMBINED PINS TO PIN 1 OF THE SAME CHIP WHICH HAS A PERMANENT VOLTAGE. THIS WILL NOW ALLOW THE SET TO BE RETUNED TO OPERATE IN THE 29MHz RANGE. WITH CHANNEL 25 BEING 29.600.

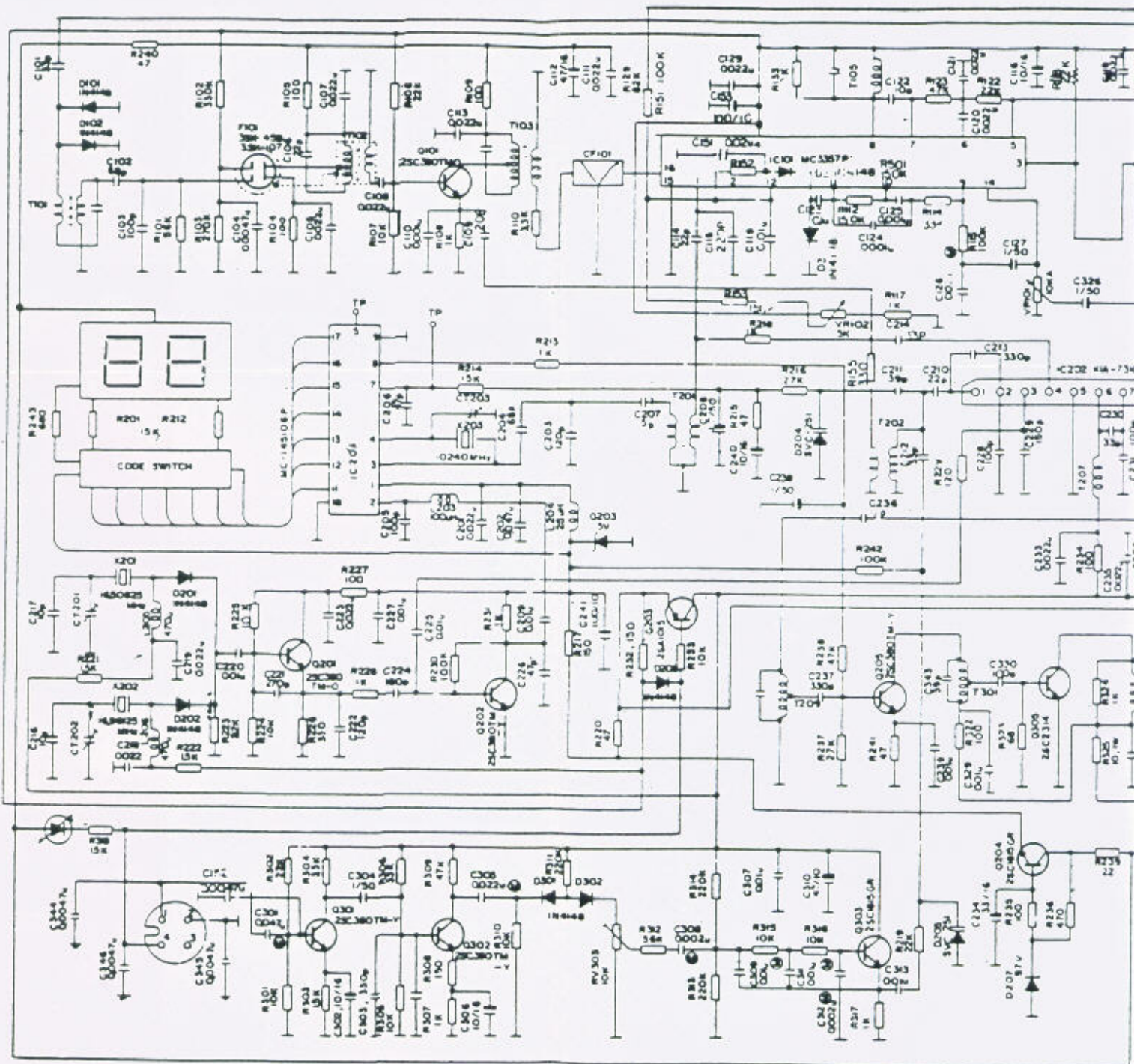
TO INCREASE COVERAGE DOWNWARDS FROM CHANNEL 25 THE GREEN LEAD FROM PIN 13 MUST BE DISCONNECTED AND DISCARDED, UNSOLDER THE VIOLET LEAD FROM PIN 11 AND TRANSFER TO VACANT HOLE AT PIN 13.THIS WILL ALLOW 10KHz STEPS FROM CHANNEL 40 TO 9, BETWEEN 1 AND 9 THE CHANNELS WILL DUPLICATE AND CAN BE IGNORED AS THEY ARE ALREADY COVERED.

RETUNING OF THE SET WILL BE NEEDED.T202 IS THE VCO CAN,T209,301,303 AND 307 OPERATE THE TX STRIP WHILE T101 AND T102 COVER RECEIVE.SLIGHT OFFSET ON THE FREQUENCY CAN BE CURED BY CT202 ON TX AND CT201 ON RX.

REPEATER SHIFT CAN BE OBTAINED BY SWITCHING A 16.861 CRYSTAL IN PLACE OF THE 16.961 IN THE TX MODE.



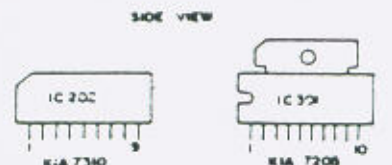
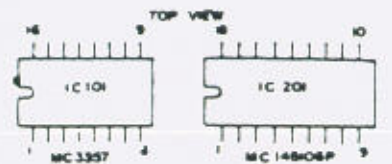
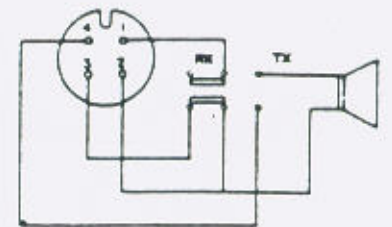
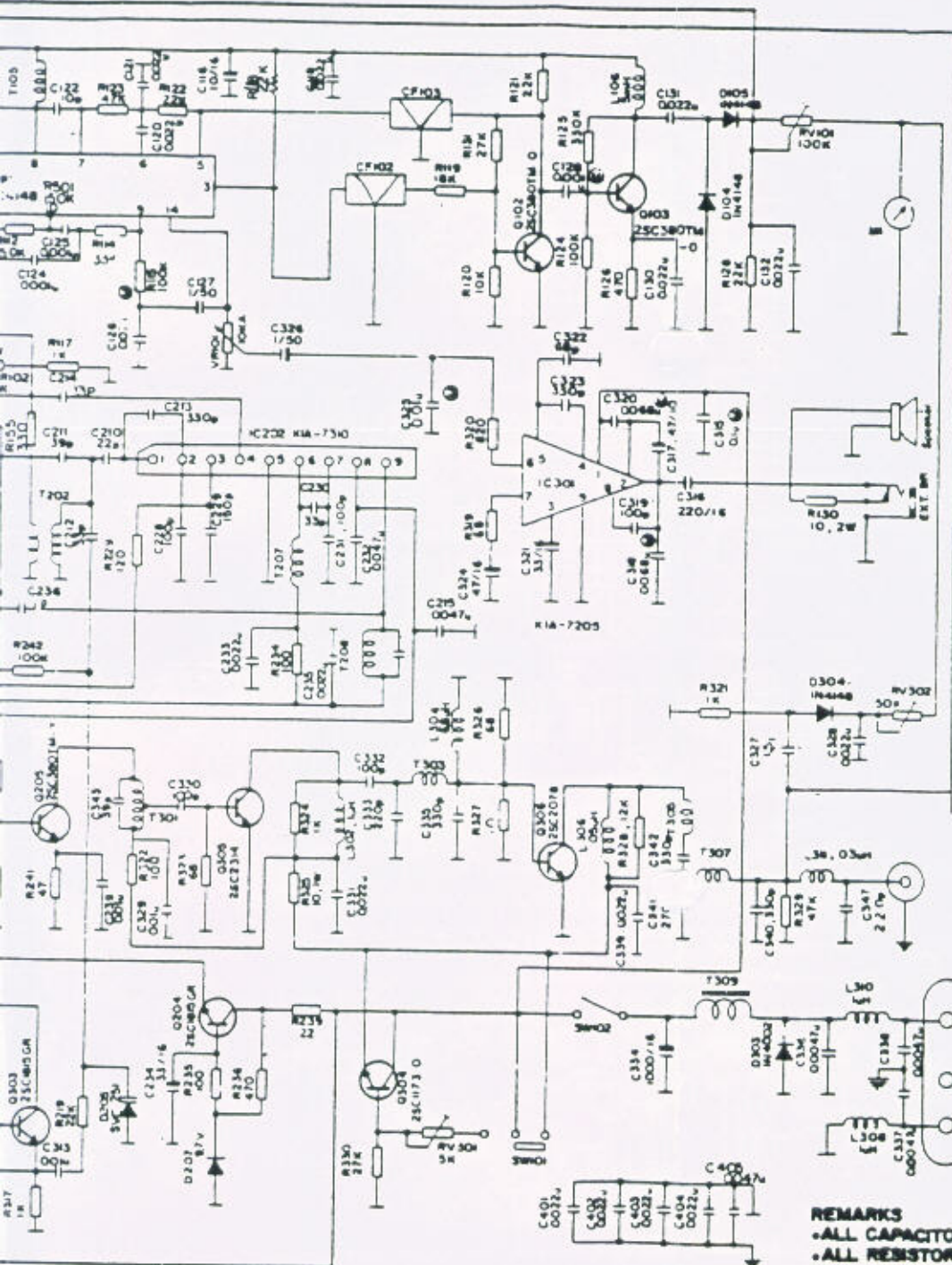
# SCHEMATIC DIAGRAM





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1COM 1050



## SCHEMATIC DIAGRAM

- REMARKS**
- ALL CAPACITOR IN FARAD
  - ALL RESISTOR IN OHM
  - ALL VOLTAGE MEASUREMENT AGAINST GROUND
  - AT MIN VOLUME AND SQUELCH